Model Reduction of Linear Switched Systems by Restricting Discrete Dynamics

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Abstract—We present a procedure for reducing the number of continuous states of discrete-time linear switched systems, such that the reduced system has the same behavior as the original system for a subset of switching sequences. The proposed method is expected to be useful for abstraction based control synthesis methods for hybrid systems.

I. INTRODUCTION

A discrete-time linear switched system [11], [18] (abbreviated by DTLSS) is a discrete-time hybrid system of the form

\[
\Sigma \begin{cases} 
  x(t+1) = A_{\sigma(t)} x(t) + B_{\sigma(t)} u(t) \quad \text{and} \quad x(0) = x_0 \\
  y(t) = C_{\sigma(t)} x(t),
\end{cases}
\]  

(1)

where \( x(t) \in \mathbb{R}^n \) is the continuous state, \( y(t) \in \mathbb{R}^p \) the continuous output, \( u(t) \in \mathbb{R}^m \) is the continuous input, \( \sigma(t) \in Q = \{ 1, \ldots, D \} \), \( D > 0 \) is the discrete state (switching signal). \( A_q, B_q, C_q \) are matrices of suitable dimension for \( q \in Q \). A more rigorous definition of DTLSSs will be presented later on. For the purposes of this paper, \( u(t) \) and \( \sigma(t) \) will be viewed as externally generated signals.

Contribution of the paper: Consider a discrete-time linear switched system \( \Sigma \) of the form (1), and a set \( L \) which describes the admissible set of switching sequences. In this paper, we will present an algorithm for computing another DTLSS

\[
\bar{\Sigma} \begin{cases} 
  \bar{x}(t+1) = \bar{A}_{\bar{\sigma}(t)} \bar{x}(t) + \bar{B}_{\bar{\sigma}(t)} u(t) \quad \text{and} \quad \bar{x}(0) = \bar{x}_0 \\
  \bar{y}(t) = \bar{C}_{\bar{\sigma}(t)} \bar{x}(t)
\end{cases}
\]  

(2)

such that for any switching sequence \( \sigma(0)\cdots\sigma(t) = q_0 \cdots q_t \in L \) and continuous inputs \( u(0), \ldots, u(t-1) \), the output at time \( t \) of (1) equals the output of (2), i.e., \( y(t) = \bar{y}(t) \) and the number of state variables of (2) is smaller than that of (1). In short, for any sequence of discrete states from \( L \), the input-output behaviors of \( \Sigma \) and \( \bar{\Sigma} \) coincide and the size of \( \bar{\Sigma} \) is smaller.

Motivation: Realistic plant models of industrial interest tend to be quite large and in general, the smaller is the plant model, the smaller is the resulting controller and the computational complexity of the control synthesis or verification algorithm. This is especially apparent for hybrid systems, since in this case, the computational complexity of control or verification algorithms is often exponential in the number of continuous states [19]. The particular model reduction problem formulated in this paper was motivated by the observation that in many instances, we are interested in the behavior of the model only for certain switching sequences.

DTLSSs with switching constraints occur naturally in a large number of applications. Such systems arise for example when the supervisory logic of the switching law is (partially) fixed. Note that verification or control synthesis of DTLSSs can be computationally demanding, especially if the properties or control objectives of interest are discrete [5]. The results of the paper could be useful for verification or control of such systems, if the properties of interest or the control objectives depend only on the input-output behavior. In this case, we could replace the original DTLSS \( \Sigma \) by the reduced order DTLSS \( \bar{\Sigma} \) whose input-output behavior for all the admissible switching sequences coincides with that of \( \Sigma \). We can then perform verification or control synthesis for \( \bar{\Sigma} \) instead of \( \Sigma \). If \( \bar{\Sigma} \) satisfies the desired input-output properties, then so does \( \Sigma \). The same applies for piecewise-affine hybrid systems, which can be modelled as a feedback interconnection of a linear switched system of the form (1) with a discrete event generator \( \phi \) which generates the next discrete state based on the past discrete states and past outputs [6], [20]. Likewise, assume that it is desired to design a control law for \( \Sigma \) which ensures that the switching signal generated by the closed-loop system belongs to a certain prefix closed set \( L \). Such problems arise in various settings for hybrid systems [19]. Again, this problem is solvable for \( \Sigma \) if and only if it is solvable for \( \bar{\Sigma} \), and the controller which solves this problem for \( \bar{\Sigma} \) also solves it for \( \Sigma \).

Related work: Results on realization theory of linear switched systems with constrained switching appeared in [14]. However, [14] does not yield a model reduction algorithm, see Remark 1 in [2] for a detailed discussion. The algorithm presented in this paper bears a close resemblance to the moment matching method of [1], but its result and its scope of application are different. The subject of model reduction for hybrid and switched systems was addressed in several papers [3], [23], [12], [4], [8], [21], [22], [7], [9], [10], [13], [17]. However, none of them deals with the problem addressed in this paper.

Outline: In Section II, we fix the notation and terminology of the paper. In Section III, we present the formal definition and main properties of DTLSSs. In Section IV we give the precise problem statement. In Section V, we recall the con-
cept of Markov parameters, and we present the fundamental theorem and corollaries which form the basis of the model reduction by moment matching procedure. The algorithm itself is stated in Section VI in detail. Finally, in Section VII the algorithm is illustrated on a numerical example.

II. PRELIMINARIES: NOTATION AND TERMINOLOGY

Denote by $\mathbb{N}$ the set of natural numbers including 0.

Consider a non-empty set $Q$ which will be called the alphabet. Denote by $Q^*$ the set of finite sequences of elements of $Q$. The elements of $Q^*$ are called strings or words over $Q$, and any set $L \subseteq Q^*$ is called a language over $Q$. Each non-empty word $w$ is of the form $w = q_1q_2 \cdots q_k$ for some $q_1, q_2, \cdots, q_k \in Q$, $k > 0$. In the following, if a word $w$ is stated as $w = q_1q_2 \cdots q_k$, it will be assumed that $q_1, q_2, \cdots, q_k \in Q$. The element $q_i$ is called the $i$th letter of $w$, for $i = 1, 2, \ldots, k$ and $k$ is called the length of $w$. The empty sequence (word) is denoted by $\varepsilon$. The length of word $w$ is denoted by $|w|$; specifically, $|\varepsilon| = 0$. The set of non-empty words is denoted by $Q^+$, i.e., $Q^+ = Q^\setminus \{\varepsilon\}$. The set of words of length $k \in \mathbb{N}$ is denoted by $Q^k$. The concatenation of word $w \in Q^+$ with $v \in Q^+$ is denoted by $vw$: if $v = v_1v_2 \cdots v_k$ and $w = w_1w_2 \cdots w_m$, $k > 0, m > 0$, then $vw = v_1v_2 \cdots v_kw_1w_2 \cdots w_m$. If $v = \varepsilon$, then $vw = v$; if $w = \varepsilon$, then $vw = v$.

If $Q$ has a finite number of elements, say $D$, it will be identified with its index set, that is $Q = \{1, 2, \ldots, D\}$.

III. LINEAR SWITCHED SYSTEMS

In this section, we present the formal definition of linear switched systems and recall a number of relevant definitions. We follow the presentation of [14], [16].

**Definition 1 (DTLSS):** A discrete-time linear switched system (DTLSS) is a tuple

$$\Sigma = (p, m, n, Q, \{\{A_q, B_q, C_q\} | q \in Q\}, x_0) \quad (3)$$

where $Q = \{1, \ldots, D\}$, $D > 0$, called the set of discrete modes, $A_q \in \mathbb{R}^{n \times n}$, $B_q \in \mathbb{R}^{n \times m}$, $C_q \in \mathbb{R}^{p \times n}$ are the matrices of the linear system in mode $q \in Q$, and $x_0$ is the initial state.

The number $n$ is called the dimension (order) of $\Sigma$ and will sometimes be denoted by $\dim(\Sigma)$.

**Notation 1:** In the sequel, we use the following notation and terminology: The state space $X = \mathbb{R}^n$, the output space $Y = \mathbb{R}^p$, and the input space $U = \mathbb{R}^m$. We will write $U^+ \times Q^+ = \{(u, \sigma) \in U^+ \times Q^+ \mid |u| = |\sigma|\}$, and $\sigma(t)$ for the $t + 1$th element $q_t$ of a sequence $\sigma = q_1q_2 \cdots q_T \in Q^+$ (the same comment applies to the elements of $U^+, X^+$ and $Y^+$).

Throughout the paper, $\Sigma$ denotes a DTLSS of the form (1).

**Definition 2 (Solution):** A solution of the DTLSS $\Sigma$ at the initial state $x_0 \in X$ and relative to the pair $(u, \sigma) \in U^+ \times Q^+$ is a pair $(x, y) \in X^+ \times Y^+$, $|x| = |\sigma| + 1, |y| = |\sigma|$ satisfying

$$x(t + 1) = A_{\sigma(t)}x(t) + B_{\sigma(t)}u(t), \quad x(0) = x_0$$

$$y(t) = C_{\sigma(t)}x(t),$$

for $t = 0, 1, \ldots, |\sigma| - 1$.

We shall call $u$ the control input, $\sigma$ the switching sequence, $x$ the state trajectory, and $y$ the output trajectory. Note that the pair $(u, \sigma) \in U^+ \times Q^+$ can be considered as an input to the DTLSS.

**Definition 3 (Input-state and input-output maps):** The input-state map $X_{\Sigma,x_0}$ and input-output map $Y_{\Sigma,x_0}$ for the DTLSS $\Sigma$, induced by the initial state $x_0 \in X$, are the maps

$$U^+ \times Q^+ \to X^+; (u, \sigma) \mapsto X_{\Sigma,x_0}(u, \sigma) = x,$$

$$U^+ \times Q^+ \to Y^+; (u, \sigma) \mapsto Y_{\Sigma,x_0}(u, \sigma) = y,$$

where $(x, y)$ is the solution of $\Sigma$ at $x_0$ relative to $(u, \sigma)$.

Next, we present the basic system theoretic concepts for DTLSSs. The input-output behavior of a DTLSS realization can be formalized as a map

$$f : U^+ \times Q^+ \to Y^+. \quad (5)$$

The value $f(u, \sigma)$ represents the output of the underlying (black-box) system. This system may or may not admit a description by a DTLSS. Next, we define when a DTLSS describes (realizes) a map of the form (5).

The DTLSS $\Sigma$ of the form (1) is a realization of an input-output map $f$ of the form (5), if $f$ is the input-output map of $\Sigma$ which corresponds to some initial state $x_0$, i.e., $f = Y_{\Sigma,x_0}$. The map $Y_{\Sigma,x_0}$ will be referred to as the input-output map of $\Sigma$, and it will be denoted by $Y_\Sigma$. The following discussion is valid only for realizable input-output maps.

We say that the DTLSSs $\Sigma_1$ and $\Sigma_2$ are equivalent if $Y_{\Sigma_1} = Y_{\Sigma_2}$. The DTLSS $\Sigma_m$ is said to be a minimal realization of $f$, if $\Sigma_m$ is a realization of $f$, and for any DTLSS $\Sigma$ such that $\Sigma$ is a realization of $f$, $\dim(\Sigma_m) \leq \dim(\Sigma)$. In [14], it is stated that a DTLSS realization $\Sigma$ is minimal if and only if it is span-reachable and observable. See [14] for detailed definitions of span-reachability and observability for LSSs.

IV. MODEL REDUCTION BY RESTRICTING THE SET OF ADMISSIBLE SEQUENCES OF DISCRETE MODES

In this section, we state formally the problem of restricting the discrete dynamics of the DTLSS.

**Definition 4:** A non-deterministic finite state automaton (NDFA) is a tuple $\mathcal{A} = (S, Q, \{q_0\}, F, \delta_q)$ such that

1) $S$ is the finite state set,
2) $F \subseteq S$ is the set of accepting (final) states,
3) $\delta_q \subseteq S \times S$ is the state transition relation labelled by $q \in Q$.

4) $s_0 \in S$ is the initial state.

For every $v \in Q^*$, define $\rightarrow_r$ inductively as follows:

$$\rightarrow_r = \{(s, s) \mid s \in S\} \quad \text{and} \quad \rightarrow_q = \{(s_1, s_2) \in S \times S \mid \exists s_3 \in S: (s_1, s_3) \in \rightarrow_r \text{ and } (s_3, s_2) \in \rightarrow_q\} \quad \text{for all } q \in Q.$$

We denote the fact $(s_1, s_2) \in \rightarrow_r$ by $s_1 \rightarrow_r s_2$. Define the language $L(\mathcal{A})$ accepted by $\mathcal{A}$ as

$$L(\mathcal{A}) = \{v \in Q^* \mid \exists s \in F : s_0 \rightarrow_r s\}.$$

Recall that a language $L \subseteq Q^*$ is regular, if there exists an NDFA $\mathcal{A}$ such that $L = L(\mathcal{A})$. In this case, we say that $\mathcal{A}$ accepts or generates $L$. We say that $\mathcal{A}$ is co-reachable, if from any state a final state can be reached, i.e., for any $s \in S$,
there exists \( v \in Q^* \) and \( s_f \in F \) such that \( s \rightarrow s_f \). It is well-known that if \( \alpha \) accepts \( L \), then we can always compute an N DFA \( \mathcal{A}_{\alpha \rightarrow r} \) from \( \alpha \) such that \( \mathcal{A}_{\alpha \rightarrow r} \) accepts \( L \) and it is co-reachable. Hence, without loss of generality, in this paper we will consider only co-reachable NFAs.

Definition 5 (L-reachability and L-equivalence): Consider an input-output map \( f \) and a DT LSS \( \Sigma \). Let \( L \subseteq Q^* \). We will say that \( \Sigma \) is an L-realization of \( f \), if for every \( u \in U^+ \), and every \( \sigma \in L \) such that \(|u| = |\sigma|\),

\[
Y_\Sigma(u, \sigma)(|\sigma| - 1) = f(u, \sigma)(|\sigma| - 1),
\]

i.e., the final value of \( Y_\Sigma \) and \( f \) agrees for all \((u, \sigma) \in U^+ \times L\), \(|\sigma| = |u|\). Note that a \( Q^+ \)-realization is precisely a realization.

We will say that two DT LSS \( \Sigma_1 \) and \( \Sigma_2 \) are L-equivalent, if \( \Sigma_2 \) is an L-realization of \( Y_{\Sigma_1} \) (or equivalently if \( \Sigma_1 \) is an L-realization of \( Y_{\Sigma_2} \)).

Problem 1 (Model reduction preserving L-equivalence): Consider a minimal DT LSS \( \Sigma \) and let \( L \subseteq Q^+ \) be a regular language. Find a DT LSS \( \bar{\Sigma} \) such that \( \dim(\bar{\Sigma}) < \dim(\Sigma) \) and, \( \Sigma \) and \( \bar{\Sigma} \) are L-equivalent.

V. MODEL REDUCTION ALGORITHM: PRELIMINARIES

In order to present the model reduction algorithm and its proof of correctness, we need to recall the following definitions from [15].

Definition 6 (Convolution representation): The input-output map \( f \) has a generalized convolution representation (abbreviated as GCR), if there exist maps \( S_i^0 : Q^+ \rightarrow \mathbb{R}^p \), \( S_i : Q^+ \rightarrow \mathbb{R}^p \times m \), such that \( S_i(q) = 0 \) if \( q \in Q \) and

\[
f(u, \sigma)(t) = S_i^0(q_0q_1 \cdots q_t) + \sum_{k=0}^{t-1} S_i(q_k q_{k+1} \cdots q_t) u_k
\]

for all \((u, \sigma) \in U^+ \times \bar{Q}^+\), \( 0 \leq t \leq |\sigma| - 1 \) with \( \sigma = q_0q_1 \cdots q_{|\sigma|-1} \).

By a slight abuse of the terminology adopted in [15], we will call the maps \( \{S_i^0, S_i^1\} \) the Markov parameters of \( f \). Notice that if \( f \) has a GCR, then the Markov-parameters of \( f \) determine \( f \) uniquely. In other words, the Markov-parameters of \( f \) and \( g \) are equal if and only if \( f \) and \( g \) are the same input-output map, i.e. \( S_i^0 = S_i^0 \) and \( S_i = S_i^0 \) if and only if \( f = g \).

In the sequel, we will use the fact that Markov parameters can be expressed via the matrices of a state-space representation. In order to present this relationship, we introduce the following notation.

Notation 2: Let \( w = q_0q_1 \cdots q_k \in Q^* \), \( k > 0 \) and \( A_{q_i} \in \mathbb{R}^{n \times n} \), \( i = 1, \cdots, k \). Then the matrix \( A_w \) is defined as

\[
A_w = A_{q_k} A_{q_{k-1}} \cdots A_{q_1}.
\]

If \( w = \varepsilon \), then \( A_\varepsilon \) is the identity matrix.

Lemma 1 ([15]): The map \( f \) is realized by the DT LSS \( \Sigma \) if and only if \( f \) has a GCR and for all \( v \in Q^* \), \( q, q_0 \in Q \),

\[
S_i^0(q_0 v q) = C_q A_{q_0} B_{q_0}
\]

and

\[
S_i^1(v q) = C_q A_{q_0} x_{q_0}.
\]

We will extend Lemma 1 to characterize the fact that \( \Sigma \) is an L-realization of \( f \) in terms of Markov parameters. To this end, we need the following notation.

Notation 3 (Prefix and suffix of L): Let the prefix \((L)_s \) and suffix \((L)_t \) of a language \( L \) be defined as follows:

\[
(L)_s = \{ s \in Q^* \mid \exists w \in Q^* : sw \in L \}, \quad \text{and} \quad (L)_t = \{ s \in Q^* \mid \exists w \in Q^* : ws \in L \}.
\]

In addition, let the 1-prefix \((L)_1 \) and 1-suffix \((L)_1 \) of a language \( L \) be defined as follows:

\[
(L)_1 = \{ s \in Q^* \mid \exists q \in Q : s q \in L \}, \quad \text{and} \quad (L)_1 = \{ s \in Q^* \mid \exists q \in Q : q s \in L \}.
\]

A language \( L \) is said to be prefix (resp. suffix) closed if

\[
(L)_1 = L \quad \text{(resp.} \quad (L)_1 = L \).
\]

See [2] for an example which further illustrates this notation. Note that if \( L \) is regular, then so are \((L)_s \), \((L)_t \), \((L)_1 \), \((L)_1 \). Moreover NFAs accepting these languages can easily be computed from an N DFA which accepts \( L \).

The proof of Lemma 1 can be extended to prove the following result, which will be central for our further analysis.

Lemma 2: Assume \( f \) has a GCR. The DT LSS \( \Sigma \) is an L-realization of \( f \), if and only if for all \( v \in Q^* \), \( q_0, q \in Q \)

\[
vq \in L \implies S_i^0(vq) = C_q A_{q_0} x_{q_0}
\]

and

\[
q_0 v q \in (L)_1 \implies S_i^1(v q) = C_q A_{q_0} B_{q_0}\]
Intuitively, the $L$-reachability space $\mathcal{R}_L(\Sigma)$ of a DTLSS $\Sigma$ is the space consisting of all the states $x \in X$ which are reachable from $x_0$ with some continuous input and some switching sequence from $L$. It follows from [14], [18] that $\Sigma$ is span-reachable if and only if $\dim(\mathcal{R}_Q) = n$.

**Definition 8 (L-unobservability subspace):** For a DTLSS $\Sigma$, and $L \subseteq Q^*$, define the $L$-unobservability subspace as

$$\mathcal{O}_L(\Sigma) = \bigcap_{v \in Q^*, \exists q \in Q, vq \in \epsilon(L)} \ker(C_qA_v).$$

If $\Sigma$ is clear from the context, we will denote $\mathcal{O}_L(\Sigma)$ by $\mathcal{O}_L$.

Recall that according to Notation 3, $\Sigma$ is span-reachable if and only if $\dim(L) = \dim(\mathcal{R}_L)$. Thus, $\mathcal{R}_L$ and $\mathcal{O}_L$ are complementary subspaces. Further, we define $\Sigma$-reachability and $\Sigma$-unobservability with respect to switching sequences from $L$.

**Lemma 3:** Let $\Sigma = (p, m, n, Q, \{ (A_q, B_q, C_q) | q \in Q \}, x_0)$ be a DTLSS and $L \subseteq Q^*$. Let $\dim(\mathcal{R}_L(\Sigma)) = r$ and $P \in \mathbb{R}^{n \times r}$ be a full column rank matrix such that 

$$\mathcal{R}_L(\Sigma) = \text{im}(P).$$

Let $\Sigma = (p, m, n, Q, \{ (\tilde{A}_q, \tilde{B}_q, \tilde{C}_q) | q \in Q \}, \tilde{x}_0)$ be the DTLSS defined by

$$\tilde{A}_q = P^{-1}A_qP, \quad \tilde{B}_q = P^{-1}B_q, \quad \tilde{C}_q = C_qP, \quad \tilde{x}_0 = P^{-1}x_0,$$

where $P^{-1}$ is a left inverse of $P$. Then $\tilde{\Sigma}$ and $\Sigma$ are $L$-equivalent.

**Proof:** See [2].

That is, Lemma 3 says that if we find a matrix representation of the $L$-reachability space, then we can compute a reduced order DTLSS which is an $L$-realization of $\Sigma$. By similar arguments we also obtain:

**Lemma 4:** Let $\Sigma = (p, m, n, Q, \{ (A_q, B_q, C_q) | q \in Q \}, x_0)$ be a DTLSS and let $L \subseteq Q^*$. Let $\dim(\mathcal{O}_L(\Sigma)) = r$ and $W \in \mathbb{R}^{n \times r}$ be a full row rank matrix such that 

$$\mathcal{O}_L(\Sigma) = \ker(W).$$

Let $\tilde{\Sigma} = (p, m, n, Q, \{ (\tilde{A}_q, \tilde{B}_q, \tilde{C}_q) | q \in Q \}, \tilde{x}_0)$ be the DTLSS defined by

$$\tilde{A}_q = WA_qW^{-1}, \quad \tilde{B}_q = WB_q, \quad \tilde{C}_q = C_qW^{-1}, \quad \tilde{x}_0 = Wx_0.$$
Algorithm 1 Calculate a matrix representation of \( A_k(G) \).
Inputs: \((\{A_q\}_{q \in Q}, G)\) and \( \mathcal{A} = (S, \{\rightarrow_q\}_{q \in Q}, F, s_0) \) such that \( L(\mathcal{A}) = K, F = \{s_1, \ldots, s_k\} \), \( k \geq 1 \) and \( \mathcal{A} \) is co-reachable.
Outputs: \( \hat{P} \in \mathbb{R}^{n \times n} \) such that \( \hat{P}^T \hat{P} = I_k, \text{rank}(\hat{P}) = \hat{r}, \text{im}(\hat{P}) = A_k(G) \).
1: \( \forall s \in S \setminus \{s_0\} : P_s := 0 \).
2: \( s_0 := \text{orth}(G) \).
3: flag = 0.
4: while flag = 0 do
5:  \( \forall s \in S : P_s^\text{old} := P_s \).
6:  \( \forall r \in S \) do
7:     \( W_r := P_r \).
8:     \( \forall q \in Q, s' \in S : s' \rightarrow_q s \) do
9:         \( W_q := \begin{bmatrix} W_q^\text{old} & \text{orth}(P_s) \end{bmatrix} \).
10: end for
11: if \( \forall s \in S : \text{rank}(P_s) = \text{rank}(P_s^\text{old}) \) then
12:     flag = 1.
13: end if
14: end while
15: return \( \hat{P} = \text{orth}(P_{s_1} \cdots P_{s_k}) \).

Algorithm 2 Calculate a matrix representation of \( O_k(H) \).
Inputs: \((\{A_q\}_{q \in Q}, H)\) and \( \mathcal{A} = (S, \{\rightarrow_q\}_{q \in Q}, F, s_0) \) such that \( L(\mathcal{A}) = K, F = \{s_1, \ldots, s_k\} \), \( k \geq 1 \) and \( \mathcal{A} \) is co-reachable.
Outputs: \( W \in \mathbb{R}^{F \times n} \) such that \( WW^T = I_k, \text{rank}(W) = \hat{r}, \ker(W) = O_k(H) \).
1: \( \forall s \in S \setminus \{s_0\} : W_s := 0 \).
2: \( \forall s \in F : W_s^T := \text{orth}(H^T) \).
3: flag = 0.
4: while flag = 0 do
5:  \( \forall s \in S : W_s^\text{old} := W_s \).
6:  \( \forall s \in S \) do
7:     \( P_s := W_s \).
8:     \( \forall q \in Q, s' \in S : s' \rightarrow_q s \) do
9:         \( P_s := \begin{bmatrix} P_s^\text{old} & \text{orth}(A_q) \end{bmatrix} \).
10: end for
11: \( W_s^T := \text{orth}(P_s^T) \).
12: end for
13: if \( \forall s \in S : \text{rank}(W_s) = \text{rank}(W_s^\text{old}) \) then
14:     flag = 1.
15: end if
16: end while
17: return \( W = W_{s_0} \).

Algorithm 3 Reduction for DTLSSs
Inputs: \( \Sigma = (p, m, n, Q, \{(A_q, B_q, C_q) | q \in Q\}, x_0) \) and \( \mathcal{A} = (S, \{\rightarrow_q\}_{q \in Q}, F, s_0) \) such that \( L(\mathcal{A}) = L, F = \{s_1, \ldots, s_k\} \), \( k \geq 1 \) and \( \mathcal{A} \) is co-reachable.
Output: \( \bar{\Sigma} = (p, m, r, Q, \{\tilde{A}_q, \tilde{B}_q, \tilde{C}_q) | q \in Q\}, \bar{x}_0 \).
1: Compute a co-reachable NDFA \( \mathcal{A}_0 \), from \( \mathcal{A} \) such that \( L(\mathcal{A}_0) = (L_1)_r \), where \( (L_1)_r \) is as in (11).
2: Use Algorithm 1 with inputs \((\{A_q\}_{q \in Q}, x_0) \) and NDFA \( \mathcal{A}_0 \). Store the output \( \tilde{P} \) as \( P_{a_0} := \tilde{P} \).
3: for \( q \in Q \) do
4:     Compute a co-reachable NDFA \( \mathcal{A}_q \) from \( \mathcal{A} \) such that \( L(\mathcal{A}_q) = \mathcal{A}_0 q(K) \), where \( \mathcal{A}_0 q(K) \) is as in (15).
5: Use Algorithm 1 with inputs \((\{A_q\}_{q \in Q}, B_q) \) and NDFA \( \mathcal{A}_q \). Store the output \( \hat{P} \) as \( P_q := \hat{P} \).
6: end for
7: \( P := \text{orth}(P_{a_0} P_1 \cdots P_D) \).
8: for \( q \in Q \) do
9:     Compute a co-reachable NDFA \( \mathcal{A}_q \) from \( \mathcal{A} \), such that \( L(\mathcal{A}_q) = \mathcal{A}_0 q(K) \), where \( \mathcal{A}_0 q(K) \) is as in (17).
10: Use Algorithm 2 with inputs \((\{A_q\}_{q \in Q}, C_q) \) and NDFA \( \mathcal{A}_q \). Store the output \( \tilde{W} \) as \( W_q := \tilde{W} \).
11: end for
12: \( W^T := \text{orth}(\begin{bmatrix} W_1^T & \cdots & W_n^T \end{bmatrix}) \).
13: if \( \text{rank}(P) > \text{rank}(W) \) then
14:     Let \( r = \text{rank}(P) \), \( P^{-1} \) be a left inverse of \( P \) and set \( \tilde{A}_q = P^{-1}A_q P, \tilde{C}_q = C_q P, \tilde{B}_q = P^{-1}B_q, \tilde{x}_0 = P^{-1}x_0 \).
15: end if
16: if \( \text{rank}(P) \geq \text{rank}(W) \) then
17:     Let \( r = \text{rank}(W) \) and let \( W^{-1} \) be a right inverse of \( W \). Set \( \tilde{A}_q = WA_q W^{-1}, \tilde{C}_q = C_q W^{-1}, \tilde{B}_q = WB_q, \tilde{x}_0 = Wx_0 \).
18: end if
19: return \( \bar{\Sigma} = (p, m, r, Q, \{\tilde{A}_q, \tilde{B}_q, \tilde{C}_q) | q \in Q\}, \tilde{x}_0 \).

Observe that the language \( L \) accepted by the NDFA \( \mathcal{A} \) is the set \( L = \{12, 123, 1232, \ldots\} \) and it can also be represented by the regular expression \( L = (123)^*12 \). As stated in Definition 4, the labels of the edges represent the discrete mode indices of the DTLSS. The model reduction procedure is applied to the single input - single output (SISO) DTLSS \( \Sigma \) of order \( n = 7 \) with \( Q = \{1, 2, 3\} \) and

\[
A_1 = [[0, 1, 0, 0, 0, 0, 0]; \text{randn}(6, 7)]
A_2 = [\text{randn}(2, 7); [0, 0, 0, 0, 0, 0, 0]; \text{randn}(4, 7)]
A_3 = [\text{randn}(1, 7); [0, 0, 1, 0, 0, 0, 0]; \text{randn}(5, 7)]
B_1 = \text{randn}(7, 1), B_2 = \text{randn}(7, 1), B_3 = \text{randn}(7, 1)
C_1 = \text{randn}(1, 7), C_2 = [1, 0, 0, 0, 0, 0, 0], C_3 = \text{randn}(1, 7).
\]

Applying Algorithm 3 to this DTLSS whose admissible switching sequences are generated by this NDFA, yields a reduced order system \( \Sigma \) of order \( r = 3 \), whose output values are the same as the original system \( \Sigma \) for the instances when the NDFA reaches a final state. Note that this corresponds precisely to an \( L \)-realization in the sense of Definition 5 (the last outputs of \( \Sigma \) and \( \bar{\Sigma} \) are the same for all the switching sequences generated by the governing NDFA, i.e., for all.

VII. NUMERICAL EXAMPLE

In this section, the model reduction method for DTLSSs with restricted discrete dynamics will be illustrated by a numerical example. The data used for the example and corresponding MATLAB code are available from https://kom.au.dk/~merth/. The example specifically illustrates the use of Lemma 3 and Algorithm 3. For an example where Lemma 3 is used, see [2]. The NDFA which generates the set of admissible switching sequences is defined as the tuple \( \mathcal{A} = (S, \{\rightarrow_q\}_{q \in Q}, F, s_0) \) where \( S = \{s_0, s_1, s_2, \ldots, s_k\} \), \( \rightarrow_1 = \{(s_0, s_1), \rightarrow_2 = \{(s_1, s_2)\}, \rightarrow_3 = \{(s_f, s_0)\} \) and \( F = \{s_f\} \).
Fig. 1. The responses of the original DTLSS $\Sigma$ of order 7 and the reduced order DTLSS $\Sigma$ of order 3 acquired by Algorithm 3 for the switching sequence in (18). Note that only the second and fifth element of the output sequence is equal for $\Sigma$ and $\bar{\Sigma}$ in first five elements, others look the same as a result of the scaling.

$\sigma \in L(\mathcal{A})$. In this example, the algorithm makes use of Lemma 4 and constructs the $W$ matrix. The matrix $W \in \mathbb{R}^{r \times n}$ computed is $W = \begin{bmatrix} I_1 & 0 \end{bmatrix}$. The outputs of $\Sigma$ and $\bar{\Sigma}$ for the switching sequence (18) of length 11 generated by the NDFA $\mathcal{A}$ are given in Figure 1 for comparison.

$$\sigma = 12312312312312. \quad (18)$$

Note that the resulting DTLSS is merely an $L$-realization of $\Sigma$ and nothing more, i.e., its output coincides with the output of $\Sigma$ for only the instances corresponding to the final states of the NDFA, see Remark 3 in [2] for further discussion. This fact is visible from Figure 1, where it can be seen that output corresponding to the final state $s_f$ of the NDFA coincides for $\Sigma$ and $\bar{\Sigma}$ (Observe that for all switching sequences generated by $\mathcal{A}$ ending with the label 2, the output values of $\Sigma$ and $\bar{\Sigma}$ are the same). The input sequence of length 11 used in the simulation is generated by the function $\text{randn}$. Finally, note that the DTLSS $\Sigma$ is minimal [2] (note that the definition of minimality for linear switched systems are made by considering all possible switching sequences in $Q^*$ [15]), whereas for the switching sequences restricted by the NDFA $\mathcal{A}$, it turns out 4 states are disposable in this case. In fact, this is the main idea of the paper.

VIII. CONCLUSIONS

A model reduction method for discrete time linear switched systems whose discrete dynamics are restricted by switching sequences comprising a regular language is presented. The method is essentially a moment matching type of model reduction method, which focuses on matching the Markov parameters of a DTLSS related to the specific switching sequences generated by a nondeterministic finite state automaton. Possible future research directions include expanding the method for continuous time case, and approximating the input/output behavior of the original system rather than exactly matching it, and formulating the presented algorithms in terms of bisimulation instead of input-output equivalence.

REFERENCES