Methodologies for Wind Turbine and STATCOM Integration in Wind Power Plant Models for Harmonic Resonances Assessment

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Abstract—This paper approaches modelling methodologies for integration of wind turbines and STATCOM in harmonic resonance studies. Firstly, an admittance equivalent model representing the harmonic signature of grid connected voltage source converters is provided. A simplified type IV wind turbine modelling is then straightforward. This linear modelling is suitable to represent the wind turbine in the range of frequencies at which harmonic interactions are likely. Even the admittance method is suitable both for frequency and time domain studies, some limitations arise in practice when implementing it in the time-domain. As an alternative, a power based averaged modelling is also proposed. Type IV wind turbine harmonic signature and STATCOM active harmonic mitigation are considered for the simulation case studies. Simulation results provide a good insight of the features and limitations of the proposed methodologies.

I. INTRODUCTION

Wind power plants (WPPs) are usually located in remote areas, where bigger wind resources are available. In remote areas, the short-circuit power tends to be small, so the term “weak grid” is usually associated to WPP. In this scenario, voltage regulation and power quality and stability phenomena become a challenge [1].

Electric resonance is one of the most challenging scenario for WPP operation. Resonance issues arise in WPPs because their circuits contain both inductive and capacitive elements, which interact with active components [2]. From the wind energy industry perspective, this poses a collection of practical problems which can be summarized as follows [3]:

- A certain harmonic voltage or current level may be unacceptable because it results in damage to equipment, mal-operation of equipment, loss-of-life-of-equipment or other practical concerns. E.g., resonances can cause stability problems in the closed-loop controllers of grid-connected converters.
- A second type of unacceptability occurs when the limits set in the main applicable standards, such as EN-50160, IEC 61000 – 2 – 2 and IEC 61000 – 3 – 6. Furthermore, the limits set by the transmission system operators (TSOs) in the grid codes should be considered.

Therefore, WPP circuit analysis and modelling are an important topic of research in order to assess potential resonance issues and also to address the effectiveness of passive/active mitigation techniques.

This work proposes different modelling approaches able to represent Type IV wind turbine (WT) and STATCOM devices in the range of frequencies at which harmonic interactions are likely. The models are suitable for different kind of software tools, such as Matlab/Simulink, PSCAD, digSilent PF, etc.

Firstly, an admittance based approach for voltage source converter (VSC) is provided [4], [5]. The main assumptions made to look for a linear model is based on the previous experience in traction applications: the EN-50388 standard recommends to address harmonic instability issues as a linear phenomenon [6]. The proposed modelling represents the harmonic interactions between the active device and the rest of the grid, i.e., the VSC harmonic signature. As shown below, the admittance explicitly includes the main VSC control and hardware parameters.

In practice, it has been found that the admittance model is more suitable for frequency domain analysis. In time domain simulation the admittance has to be implemented with the combination of a current source and a discretized transfer functions, which poses some practical limitations that are discussed below.

As an alternative, a power based averaged model is then proposed aiming for a low computational burden time domain simulations [7], [8]. This model strategy explicitly includes the VSC control loops, but substitutes the switching devices by ideal current and voltage sources driven by the averaged VSC voltage/power equations. The averaged method has been also found effective to represent the dynamics of active components in harmonic resonance scenarios.

Two different study cases are analyzed with simulation results. Firstly, a simplified type IV WT admittance is derived. Its equivalence with the averaged based method is then proved from the time-domain steady-state current spectrum [6]. This result supports the suitability of using admittance models in frequency domain analyses related to harmonics resonances in WPP. In this context, some features of the WT admittance are discussed. Subsequently, an averaged STATCOM model with active damping functionality [9] is tested in a complex WPP model. It is shown how the STATCOM successfully performs in steady and transient states. Operation waveforms are obtained for both ac and dc sides of the STATCOM.
II. ADMITTANCE BASED MODELLING OF GRID-CONNECTED POWER CONVERTERS

This section details how to obtain a simplified admittance model for grid-connected VSC working in a linear region. It aims to represent the VSC dynamics interaction when connected at any point of the power system.

1) Derivation of the Model: The output current of the converter in Fig. 1 is given by

\[ I(s) = G_c(s)I^*(s) + Y_{eq}(s)E(s) \]  

with \( G_c(s) \) being the closed loop transfer function and \( Y_{eq}(s) \) an equivalent Norton admittance. These two parameters are obtained as follows. The grid current \( I(s) \) is

\[ I(s) = Z_c^{-1}(s)E(s) - U(s) \]  

with \( Z_c(s) \) being the converter output filter impedance and \( U(s) \) the output voltage. On the other hand, the converter control law \( U^*(s) \) (PWM reference) is defined by

\[ U^*(s) = -C(s)[I^*(s) - I(s)] + U(s)H(s) \]  

with \( C(s) \) the controller and \( H(s) \) the voltage feed-forward transfer functions, respectively. If system delays (due to discrete-time operation) are neglected, \( U(s) = U^*(s) \) is assumed. This leads to the following expressions

\[ G_c(s) = \frac{C(s)}{Z_c(s) + C(s)} \]

\[ Y_{eq}(s) = \frac{1 - H(s)}{Z_c(s) + C(s)} \]

for the closed loop controller and admittance in (1).

2) Control Bandwidth Considerations: The previous approach has been derived by assuming that there is not effect of the system delays. If considered

\[ U(s) = U^*(s)e^{-sT_d} = U^*(s)e^{-s1.5/f_s} \]  

with \( T_d \) and \( f_s \) being the system delay and sampling frequency, respectively [5], [10], [11]. In order to avoid the positive feedback (instability), the bandwidth of \( G_c(s) \) is limited to 0.1\( f_s \) [11]. At the same time, \( f_s \) is limited by the PWM carrier frequency \( f_{pwm} \). In a two-level VSC using a regular sampling PWM \( f_s = f_{pwm} \) (single update) or \( f_s = 2f_{pwm} \) (double update) [11]. Clearly, the former is preferred from the control perspective. An even more convenient relation between \( f_s \) and \( f_{pwm} \) can be achieved with multilevel converters [12]. On the other hand, commercial devices tend to reduce switching rates to control power losses [12], [13], so there is a clear tradeoff between control ability and power losses. Focusing on the modelling, it is clear that a good knowledge of the device is needed to accurately assess its achievable control bandwidth.

III. AVERAGE MODEL BASED ON AC-DC POWER BALANCING

A detailed model of the power converter, including non-linear effects, can be represented by software tools. Such a detailed operation is specially suitable to accurately represent many non-linear behavior of the power converter, such as PWM patterns [14]. However, this is at the cost of a high computational burden. Focusing on harmonic resonances in the WPP circuits, the inclusion of such detailed converter models is not always feasible, and then simplified models are usually required [11], [15].

The main assumption taken to simplify the VSC modelling is to consider its currents and voltages averaged over a switching cycle [7]. This assumption is available as the VSC switching frequency is higher than the range of potential resonance interactions with the WPP [15]. It can be expressed as follows:

\[ U(t) = 0.5[m_a(t)U_{dc}(t), m_b(t)U_{dc}(t), m_c(t)U_{dc}(t)] \]  

with \( U_{dc}(t) \) being the dc-link voltage and \( [m_a(t), m_b(t), m_c(t)] \) being the control reference signals. Assuming an Space Vector Modulation (SVM-PWM) or equivalent for smart use of dc-link, the range of linear operation for the control signals is [−1.15, 1.15] [14]. Since the VSC is a three-wire system, the output voltage can be set by two controlled voltage sources driven by two of the line to line voltages (the other one is linear dependent, and hence not explicitly needed).

Another assumption, which permits to consider the VSC dc-link dynamics, is to consider an ideal power conversion between ac and dc sides of the VSC [8]. Then, the dc-link current can be derived as follows

\[ I_{dc}(t) = 0.5[m_a(t)I_a(t) + m_b(t)I_b(t) + m_c(t)I_c(t)] \]

with \( I_a(t), I_b(t), I_c(t) \) being the ac-currents. Then, the dc-current is implemented by a controlled current source.

Fig. 2 shows the implementation of the average model. “Memory blocks” (i.e., a one-simulation-step delay) should be introduced between measured signals and source commands to avoid algebraic loops.

This methodology explicitly considers converter control blocks in the time-domain. Then, advanced controllers such
as the ones used for short-term stability (e.g. low voltage ride through requirements for WTs) can be included in the controller. Therefore, averaged modelling can be also considered to assess many of the functionalities explicit in IEC-61400-27, IEC-61400-21-1 and IEC-61400-21-2 [16]. Anyway, it should be bore in mind that for harmonic studies, simulation time steps should be reduced (at the cost of computational burden) to increase the frequency resolution.

IV. CASE STUDIES AND SIMULATION RESULTS

Two different case studies are provided to show the features and performance of the proposed VSC modelling techniques. Simulation results have been obtained with PLECS.

A. Admittance Model Verification for a Type IV WT

Table I shows the parameters employed for modelling the grid VSC of a Type IV WT. The VSC is connected to the grid through an LCL converter [17], [18]. In order to simplify the analysis, SISO expressions are used (cross-coupling effects are neglected) [10]. Then, C(s) is now considered a SISO proportional controller C(s) obtained by internal model control [5]. It should be noted that, for a LCL filter connection, \( Y_{eq}(s) \) only represents the VSC dynamics set by C(s) and the converter filter \( Z_c(s) \) [5]. \( Z_c2(s) \) and \( Z_{if}(s) \) remain as passive components of the circuit.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fundamental angular frequency</td>
<td>( \omega_1 = 2\pi 50 \text{ rad/s} )</td>
</tr>
<tr>
<td>Voltage</td>
<td>690 Vrms</td>
</tr>
<tr>
<td>LCL filter</td>
<td></td>
</tr>
<tr>
<td>Converter filter inductance</td>
<td>( Z_c = 0.01 + 0.1 \text{ p.u. (resistive-inductive)} )</td>
</tr>
<tr>
<td>Capacitor</td>
<td>( Z_{c2} = 0.01 \text{ p.u. (capacitive)} )</td>
</tr>
<tr>
<td>Transformer leakage</td>
<td>( Z_{if} = 0.1 \text{ p.u. (inductive)} )</td>
</tr>
<tr>
<td>LCL resonance frequency</td>
<td>170 Hz (from ( Z_c, Z_{c2}, Z_{if} ))</td>
</tr>
<tr>
<td>Ideal Controller</td>
<td></td>
</tr>
<tr>
<td>Current controller bandwidth</td>
<td>( \alpha_{cc} = 250 \text{ Hz} )</td>
</tr>
<tr>
<td>Feedforward LPF cut-off</td>
<td>( \alpha_{f,p} = 400 \text{ Hz} )</td>
</tr>
</tbody>
</table>

Fig. 3 shows the simulation model used to validate the equivalence between admittance and averaged modelling. The admittance is implemented in the time domain by means of a controlled current source driven by \( E(t) \ast Y_{eq}(t) \). In order to avoid algebraic loops, \( Y(s) \) should be discretized, so \( Y(t) \) remains strictly proper [19].

Fig. 4 shows the steady-state harmonic spectrum obtained from the tests in Fig. 3. It can be checked the equivalence between both models.

1) Physical Interpretation of \( Y_{eq}(s) \) (Frequency Domain): Fig. 5 shows the frequency domain representation for the admittance obtained [from Table I and (4)]. It can be observed that the admittance, is well damped at low frequencies, where the proportional part of the control is dominant over \( Z_{if}(s) \). At higher frequencies, the control action is not effective and \( Y_{eq}(s) \) mainly represents the inductive filter represented by \( Z_{if}(s) \). The admittance tends to be more and more undamped as its phase-angle tends to \(-90 \text{ deg}\). If system delays (due to discrete time control operation) are included for a more accurate analysis, the phase delay can be even below \(-90 \text{ deg}\), which describes a non-passive system [5]. If grid impedance [as seen from \( E(s) \)] is available, analyses based on the Nyquist stability criterion are suitable [5], [20]. E.g., if the wind turbine point of connection presents a high capacitive behavior at a frequency at which \( Y_{eq}(s) \) is inductive, the sensitivity peak (minimum distance of the Nyquist trajectory to -1 [21]) tends to be small at such frequency. In fact, non passive system may go to instability as a “negative resistance” represents a positive feedback in the control system. This kind of situations reflects potential harmonic resonance between the WT and the WPP.
On the other hand, it should be noted that the LCL filter presents a resonance which is set in the VSC design. If a reasonable sampling frequency is considered, this LCL resonance is around or even above 1 kHz [17], [18]. Therefore, in principle, a damp harmonic signature below LCL resonance frequencies is expected for Type IV WT.

2) Features and Limitations of Admittance Modelling:
As shown, the admittance modelling is specially suitable to represent the wind turbine harmonic signature in the frequency domain. A good insight of the wind turbine behavior can be obtained with a low order transfer function. However, for time domain simulations [see Fig. 3(a)], practical limitations have been found. Firstly, it has been checked that the sampling time should be quite reduced, which means an increase of the computational burden. This behavior can be expected from the inaccuracies introduced at $Y_{eq}(s)$ discretization (a process needed for discrete-time operation, usually software tools implement it automatically) which introduces extra phase at high frequencies [19]. From the Nyquist stability criterion, this extra phase tends to make the system unstable, so the sampling time reduction is needed to enhance the accuracy. Clearly, this is at the cost of increase the computational burden. Furthermore, if a good representation of the fundamental component behavior is wanted, more blocks are needed [$G_c(s)$, resonant/integral control action in $C(s)$, outer loops [4], etc.], which increases the order and complexity of the model too much.

B. Averaged Model of STATCOM with Active Filtering

The aim of this section is to show the average based modelling implementing active filtering for harmonic resonance mitigation [22]. Fig. 6 depicts a single-line representation of a STATCOM with active filtering included in a detailed WPP model, also implemented in PLECS. The WPP circuit presents potential harmonic voltage amplifications at some points due to resonances with the external grid impedance.

Fig. 7 shows the effect of a resonance between the external source and T2: a voltage harmonic amplification at the 5$^{th}$ harmonic component (1.58 multiplication factor) is likely.

Table II represents the STATCOM parameters and values employed in the simulation. Active filtering operation is based on a state feedback of the voltage at the point of connection [23], [24]. The 5$^{th}$ harmonic is obtained in real time by Discrete Fourier Transform (DFT) [25].

Fig. 8 shows simulated waveforms for the voltage at the point of STATCOM connection [$E_{pcc}(t)$] and STATCOM current [$I_c(t)$]. Fig. 8(a) shows the waveforms in the whole

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>External Grid</td>
<td></td>
</tr>
<tr>
<td>External Grid, Programmed background harmonics</td>
<td>0.0100 p.u.</td>
</tr>
<tr>
<td>Voltage level at STATCOM pcc</td>
<td>-3.3 kV rms</td>
</tr>
<tr>
<td>Rated dc-link voltage</td>
<td>U$_{dc}$ = 5250 V</td>
</tr>
<tr>
<td>Dc-link capacitance</td>
<td>C$_{dc}$ = 40 mF</td>
</tr>
<tr>
<td>Fundamental angular frequency</td>
<td>$\omega = 2\pi f_0$ rad/s</td>
</tr>
<tr>
<td>STATCOM Hardware</td>
<td></td>
</tr>
<tr>
<td>Dc-link voltage controller bandwidth</td>
<td>$\alpha_{dc} = 7$ Hz</td>
</tr>
<tr>
<td>Main Current controller bandwidth (fundamental component controller)</td>
<td>$\alpha_{cc} = 100$ Hz</td>
</tr>
<tr>
<td>Active damping gain (effective at the 5$^{th}$ harmonic)</td>
<td>$K_{ad} = -3$</td>
</tr>
</tbody>
</table>

Fig. 6. Simplified problem formulation: the STATCOM at T2 includes active filtering functionality to mitigate harmonic amplifications at the PCC voltage due to a series resonance.

Fig. 7. Voltage amplification factor from Grid to T2.

Table II

STATCOM Model Parameters
simulation time window of 2 seconds. It can be appreciated how, after an initial transient, the reactive current reference starts to increase in ramp up to $Q = 24$ MVA (the main STATCOM functionality). Then, the rated reactive current is reached. Subsequently, at 1.5 seconds the active filtering functionality is enabled. It can be appreciated how the current waveforms changes. Fig. 8(b) shows the detail of Fig. 8(a) at the moment of the activation of active filtering functionality: when active damping is activated, the 5th harmonic current jumps increases from 0 to 0.13 p.u (the improvement in the voltage waveform is not clear by time domain inspection, so frequency spectrum analysis, as shown below in Table III, is needed). This suggests that a highly distorted current at the output of the STATCOM is demanded to perform active filtering.

Fig. 9 shows the 5th harmonic at the STATCOM connection $[E_{pec5}(t)]$ when active damping is enabled. It can be seen how the 5th harmonic is attenuated by around a half.

Table III represents the steady-state values for the 5th harmonic voltage at different points of the WPP (see Fig. 6), both when the active filtering is disabled or enabled.

**TABLE III**

<table>
<thead>
<tr>
<th>Active Filtering</th>
<th>Background</th>
<th>T1</th>
<th>T2</th>
<th>T3</th>
<th>T4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage Amplitude of the 5th Harmonic Component</td>
<td>0.0160 p.u.</td>
<td>0.0145 p.u.</td>
<td>0.0105 p.u.</td>
<td>0.0086 p.u.</td>
<td>0.0140 p.u.</td>
</tr>
<tr>
<td>Active Filtering Enabled</td>
<td>0.0170 p.u.</td>
<td>0.0099 p.u.</td>
<td>0.0076 p.u.</td>
<td>0.0035 p.u.</td>
<td>0.0074 p.u.</td>
</tr>
</tbody>
</table>

Fig. 9 shows how the dc-link controller effectively regulates the dc-bus voltage (i.e., steady-state zero error is achieved and transitory disturbances rejected). It can be also appreciated the transient effects of reactive current changes and active filtering enabling: during the change in the reactive current reference, there is a drop in the dc-link voltage (disturbance effect), which is compensated when the reference reaches the final value; when the active filtering is enabled, a dc-link ripple corresponding with the resonance frequency in the synchronous reference frame arises (in this simulation, the 5th order harmonic is converted into a 6th order harmonic ripple in the dc-link). A high capacitance value seems recommended to minimize these effects.

Fig. 10 shows how the dc-link controller effectively regulates the dc-bus voltage (i.e., steady-state zero error is achieved and transitory disturbances rejected). It can be also appreciated the transient effects of reactive current changes and active filtering enabling: during the change in the reactive current reference, there is a drop in the dc-link voltage (disturbance effect), which is compensated when the reference reaches the final value; when the active filtering is enabled, a dc-link ripple corresponding with the resonance frequency in the synchronous reference frame arises (in this simulation, the 5th order harmonic is converted into a 6th order harmonic ripple in the dc-link). A high capacitance value seems recommended to minimize these effects.

Fig. 10 also shows the control signals. It has been checked that the STATCOM is working in linear zone, as the saturation limits at $[-1.15, 1.15]$ are not reached in steady-state. The dc-link capacitance value and a dc-link voltage reference have a decisive impact to keep the STATCOM working in linear zone. Ideally, both parameters should be high, but this
would be at the cost of size and cost of the capacitance and switching technology (e.g., maximum DC-link voltage is a key factor in IGBT selection).

V. CONCLUSION

This paper provides different methodologies for modelling wind turbines and STATCOM in large WPP harmonic resonance studies. An admittance based model is derived, which includes the effect of control and hardware filters. It is proved that this modelling strategy is specially suitable for frequency domain studies. It can be observed that the effect of control is to damp the response at low frequencies (below the current control bandwidth). The admittance expression is suitable to represent advanced control techniques (e.g., a voltage feedforward path) and sampling delays. A good knowledge of wind turbine VSC parameters, such as LCL filter or sampling frequency will help to provide accurate modelling. For studies in the time-domain, an average power based VSC model has been found as a more efficient alternative. As an example of its features, a STATCOM case study is provided. Simulation results show the active filtering functionality in the WPP circuit (ac-side), but also represents the dynamics in the VSC dc-side (i.e., dc-link capacitance and outer voltage dynamics, and amplitude modulation index).

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