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Harnefors, Lennart; Wang, Xiongfei; Yepes, Alejandro G.; Blaabjerg, Frede

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Passivity-Based Stability Assessment of Grid-Connected VSCs—An Overview

Lennart Harnefors, Senior Member, IEEE, Xiongfei Wang, Member, IEEE, Alejandro G. Yepes, Member, IEEE, and Frede Blaabjerg, Fellow, IEEE

Abstract—The interconnection stability of a grid-connected voltage-source converter (VSC) can be assessed by the passivity properties of the VSC input admittance. If critical grid resonances fall within regions where the input admittance acts passively, i.e., has nonnegative real part, then their destabilization is generally prevented. This paper presents an overview of passivity-based stability assessment, including techniques for space-vector modeling of VSCs whereby expressions for the input admittance can be derived. Design recommendations for minimizing the negative-real-part region are given as well.

Index Terms—Converter control, passivity, resonances, stabilization.

I. INTRODUCTION

THE PENETRATION of grid-connected converters, particularly voltage-source converters (VSCs), is currently rising rapidly [1]. As a consequence, the risk increases for destabilization of critical grid resonances [2]–[23]. These are resonances that are poorly damped and electrically located close to one VSC, or multiple VSCs, of high enough power rating in relation to the short-circuit ratio (SCR) of the grid.

Stability of a system comprising multiple grid-connected VSCs is generally difficult to analyze (e.g., using eigenvalues [13] or the Nyquist criterion [16]), particularly if the VSCs have different ratings and dynamic properties. On the other hand, the frequency-domain passivity theory offers an effective method for stability assessment [2]–[7].

Suppose that all VSCs, as well as all other power-system components, have a passive behavior, i.e., the real part of the input admittance (also called the conductance) is nonnegative for all frequencies. Then, the system is guaranteed to be stable regardless of the number of converters, because a network that consists solely of passive components—no matter how complex—is always stable.

However, the pure passivity of all components is impossible to obtain. VSCs have negative-conductance behavior in certain frequency ranges [2]. So do electrical machines (due to the induction generator effect [24]). Yet, if it can be ascertained that every grid-connected VSC has a nonnegative conductance in frequency regions where critical grid resonances appear—i.e., partial passivity—then it is unlikely that the VSCs will induce resonance destabilization. Grid codes based on this concept are enforced by several European administrations of electrified railways. It is typically required that nonnegative conductance above the fifth-harmonic frequency must be demonstrated for any new or retrofitted active-front-end rail vehicle to be approved [25], [26].

The main objective of this paper is to make a comprehensive overview of the passivity properties of the VSC input admittance. Important previously found results are highlighted. Modeling methods of three-phase VSCs for grid-interaction studies are reviewed, and design recommendations for minimizing the negative-real-part regions are given.

The stage is set in Section II, where a review of critical grid resonances is made, and the details of the assumed VSC control system are discussed. The passivity properties of the so-called inner input admittance, which results from just the current controller (CC) and the pulsewidth modulator (PWM), are considered in Section III. Requirements on the controller parameters and on the total time delay for obtaining passivity are presented. In Section IV, the consideration is extended to the total input admittance, which includes the impact of outer loops that feed into the CC, i.e., the phase-locked loop (PLL) and the direct-voltage controller (DVC). Here, the main novel result of this paper appears. It is shown how complex space vectors and complex transfer functions can be applied to assess the passivity properties of the total input admittance (even though the latter is dq imbalanced [27]). The analysis method has similarities to that suggested in [10], [15], and [17], but the impact of the DVC is included in addition to the impact of the PLL. Moreover, the usage of space vectors rather than phase quantities obviates the need for deriving separate positive- and negative-sequence expressions.

II. PRELIMINARIES

A. Critical Grid Resonances

Grid resonances that are vulnerable to destabilization by VSCs can broadly be classified into two categories.
1) Harmonic Resonances: Resonances in this category appear in the range from hundreds of hertz up to a few kilohertz. The first incident of harmonic resonance destabilization, as known to the authors, occurred in the Swiss single-phase rail grid in 1995 [28]–[31]. Harmonic resonances are caused by the inherent impedance characteristics of power lines and cables, often in conjunction with converter input filters, such as inductance–capacitance–inductance (LCL) filters.

2) Near-Synchronous Resonances: In this category, we find resonances in the range from the synchronous (fundamental) frequency $f_1$ up to roughly $2f_1$, which typically appear in very weak grids, i.e., with an SCR approaching 1 p.u. [32]. Subsynchronous resonances [33], i.e., below $f_1$, can be classified in this category as well.

B. Causes of Resonance Destabilization

The causes of negative-conductance behavior of a VSC (in turn, possibly leading to resonance destabilization) are as follows:

1) the total, i.e., computation-plus-PWM, time delay $T_d$;
2) the CC dynamics;
3) the dynamics of the outer controllers, i.e., the PLL and, provided that such are used, the DVC and the controller for the reactive power or the point-of-common-coupling (PCC)-voltage magnitude.

Causes 1) and 2) affect harmonic resonances, whereas causes 2) and 3) affect near-synchronous resonances.

C. System Model

In the following, the positive- and negative-sequence synchronous components are, for convenience, referred to as +1 and −1, respectively. Harmonics are referred to with their signed order in a similar fashion. Boldface letters are used to denote complex space vector and transfer functions that operate on complex space vectors. The derivative operator is denoted by $s = d/dt$ (which shall be considered as the complex Laplace variable, where appropriate). Vectors and transfer functions referred to the stationary $ab$ frame are denoted with the superscript $s$, whereas vectors and transfer functions referred to the synchronous $dq$ frame aligned with $+1$ are denoted without a superscript. We introduce $T_s = 1/f_s$ as the sampling period, and $\omega_1 = 2\pi f_1$ and $\omega_s = 2\pi f_s$ as the angular synchronous and angular sampling frequencies, respectively.

In the control system, the CC closes the innermost, and fastest, loop for the converter current $i^q$ (see Fig. 1). Over-modulation is assumed not to occur, and switching harmonics are disregarded, allowing the PWM process to be modeled as lumped with the computational time delay into the total time delay $T_d$. The converter is assumed to be equipped with an inductive input filter, with inductance $L$ and a negligible resistance. Hence, the converter-current dynamics are in the $ab$ frame governed by

$$i^{ab} = \frac{E^a - v^a}{sL}, \quad v^a = e^{-sT_d}v_{ref}^{ab}$$

where $E^a$ is the PCC voltage and $v_{ref}^{ab}$ is the reference vector to the PWM, by which the converter voltage $v^a$ is generated. The $dq$-frame correspondence is obtained simply by substituting $s \rightarrow s + jo_1$ [27]

$$i = \frac{E - v}{(s + jo_1)L}, \quad v = e^{-(s+j\omega_1)T_d}v_{ref}$$

The grid impedance $Z^s(s)$, which is assumed to be balanced (also known as symmetric [27]), adds the following relations in the $ab$ and $dq$ frames, respectively:

$$v^g_s - Z^s(s)i^s = E^s \quad v^g_s - Z^g(s)i^g = E^g$$

where $Z(s) = Z^s(s + jo_1)$ and $v^g_s$ is the stiff grid voltage. The PLL and the DVC feed into the CC with signals $\theta$ and $i^q_{dc}$, respectively (see Section II-E for details). From these signals, together with $i^q_{ref}$, the $ab$-frame converter-current reference is formed as $i_{ref} = e^{i\theta}(i^q_{ref} + ji^q_{ref})$. The DVC has as input the dc-link voltage $u_d$, which is measured across the dc-link capacitor (with capacitance $C_d$).

D. CC

The two CC options that most frequently are suggested in the literature are here reviewed.

1) $dq$-Frame CC: The CC is in this case given by

$$v_{ref} = -e^{j\omega_1 T_d}[F_c(s)(i_{ref} - i) + jo_1 Li]$$

where $F_c(s)$ is the controller transfer function and the angle-adjustment factor $e^{j\omega_1 T_d}$ compensates the angle-displacement factor $e^{-j\omega_1 T_d}$ in (2). For brevity, PCC-voltage feedforward [3] is not included, but a $dq$ decoupling term $jo_1 Li$ is added. This prioritizes control of +1 over −1.

A proportional (P)-plus-resonant (R) controller with reduced-order generalized integrators (ROGIs) [34] as R parts is a suitable choice

$$F_c(s) = a_c L \left(1 + \sum_h a_h e^{j\phi_h}/(s - j\omega_1)\right)$$

where $a_c$ is the ideal closed-loop-system bandwidth, $a_h$ (with dimension angular frequency) is the individual gain factor of the R part for the harmonic order $h$, and $\phi_h$ is the compensation angle of that R part [35], [36]. Characteristically, R parts are included for +1 and −1 as well as for balanced harmonics, i.e., orders −5, +7, −11, +13 . . . [34]. These translate to $h = 0, -2, \pm 6, \pm 12, \ldots$ in the $dq$ frame. The R part at $h = 0$ reduces to a pure integrator. Selecting $a_h \ll a_c$ is recommended [37].
Combining (2) and (4) yields

\[ \mathbf{i} = \mathbf{G}_{cl}(s)i_{ref} + \mathbf{Y}_i(s)\mathbf{E} \]  

(6)

where the inner closed-loop system and the inner input admittance, respectively, are given by

\[ \mathbf{G}_{cl}(s) = \frac{e^{-st}D_e \mathbf{F}_e(s)}{[s + j\omega_1(1 - e^{-st})]L + e^{-st}D_e \mathbf{F}_e(s)} \]  

(7)

\[ \mathbf{Y}_i(s) = \frac{1}{[s + j\omega_1(1 - e^{-st})]L + e^{-st}D_{eq}(s)}. \]  

(8)

By inner, it is meant that the outer control loops, i.e., the PLL and the DVC, are not yet considered. Owing to the \(dq\) decoupling and the angle-adjustment factor, \(\mathbf{G}_{cl}(0) = 1\) irrespective of \(\mathbf{F}_e(0)\).

**Remark 1:** In (5), each ROGI pair at \(h = \pm 6, \pm 12, \ldots\) can be merged into a second-order generalized integrator (SOGI) [35], [36] as

\[ \frac{a_h e^{j\phi_h}}{s - jh\omega_1} + \frac{a_h e^{-j\phi_h}}{s + jh\omega_1} = 2a_h \cos\phi_h - h_1 \sin\phi_h \frac{s \cos\phi_h - h_1 \sin\phi_h}{s^2 + (h_1)^2}. \]  

(9)

This reduces the total computational burden, since complex coefficients are avoided. If desired, the ROGI for \(-1\) (i.e., \(h = -2\)) can be replaced by an SOGI with \(h = 2\) according to the right-hand side of (9).

2) \(ab\)-Frame CC: An equivalent \(ab\)-frame implementation of control law (4) can be obtained simply by substituting \(s \rightarrow \tilde{s}\), where

\[ \tilde{s} = s - j\omega_1. \]  

(10)

We get

\[ \mathbf{v}_i^s = -e^{j\omega_1 T_d} \mathbf{F}_e(\tilde{s})[(\mathbf{v}_i^s)^* - \mathbf{i}] + j\omega_1 L\mathbf{i}^s. \]  

(11)

The correspondences to (7) and (8) too are obtained simply by substituting \(s \rightarrow \tilde{s}\)

\[ \mathbf{G}_{cl}(s) = \frac{e^{-st}D_e \mathbf{F}_e(s)}{(s - j\omega_1(1 - e^{-st})L + e^{-st}D_e \mathbf{F}_e(s)} \]  

(12)

\[ \mathbf{Y}_i(s) = \frac{1}{(s - j\omega_1(1 - e^{-st})L + e^{-st}D_{eq}(s)}. \]  

(13)

**Remark 2:** It should be observed that the angle-adjustment factor and the \(dq\) decoupling term remain in (11), which is not common practice in \(ab\)-frame control. However, both are useful in this case as well, as they for +1 compensate the static voltage drop \(j\omega_1 L\mathbf{i}^s\) across the filter inductor, thus giving prioritized control of +1. As a result, \(\mathbf{G}_{cl}(j\omega_1) = 1\) irrespective of \(\mathbf{F}_e(0)\).

**Remark 3:** Notice that, even for \(ab\)-frame implementation, controller (5) is designed as referred to the \(dq\) frame, i.e., with \(h = 0, -2, \pm 6, \ldots\) It is then transformed to the \(ab\) frame by the substitution \(s \rightarrow \tilde{s} = s - j\omega_1\).

**Remark 4:** For \(ab\)-frame control, the ROGIs for \(\pm 1\) can be merged into an SOGI according to (9). However, replacing the ROGIs for harmonics with SOGIs only adds to the computational burden, as the system order doubles for each controlled harmonic [34].

3) **Bandwidth Selection:** The following two assumptions allow a simplified stability analysis of the current control loop to be carried out: 1) The \(R\) parts have negligible impact (which is reasonable, given the aforementioned recommendation \(a_R \ll a_c\)). 2) The imperfect \(dq\) decoupling that results from the time delay can be neglected, i.e., \(1 - e^{-st} \approx 0\). Then, \(\mathbf{G}_{cl}(s) = \mathbf{G}_k(s)/[1 + \mathbf{G}_k(s)],\) where

\[ \mathbf{G}_k(s) = \frac{\alpha_c e^{-st}D_d}{s} \]  

(14)

is the open-loop transfer function. Since \(|\mathbf{G}_k(ja_c)| = 1, a_c\) is the crossover frequency. Thus, the phase margin is given by

\[ \phi_m = \pi + \arg \mathbf{G}_k(ja_c) = \frac{\pi}{2} - a_c T_d. \]  

(15)

With the total time delay expressed in the sampling period as \(T_d = n T_s = 2\pi n / \omega_1,\) a bandwidth selection recommendation can be obtained as

\[ a_c \leq \left(\frac{\pi}{2} - \phi_m\right) \frac{\omega_1}{2\pi n}. \]  

(16)

The selection recommendation \(a_c \leq \omega_1 / 10\) of [38] is obtained as a special case of (16), e.g., for \(\phi_m = \pi / 5 = 36^\circ\) and \(n = 1.5\).

**E. Outer Control Loops**

To save space, the loop that, via \(i_q^{\text{ref}}\), controls the reactive power or the PCC-voltage magnitude is disregarded, and a constant \(i_q^{\text{ref}}\) is considered (see Fig. 1).

1) **PLL:** The purpose of the PLL is to track the rotation of the PCC voltage vector, thereby aligning the \(dq\) frame (with angle \(\theta\) relative the \(ab\) frame) with the +1 component of \(\mathbf{E}\), whose magnitude is \(E_0\). The PLL uses the imaginary part of \(\mathbf{E} = e^{-j\theta} \mathbf{E}^s\) as input signal, which is fed to the PLL controller \(F_p(s)\). This is typically a P-integral controller, which can be expressed as

\[ F_p(s) = \frac{\alpha_p}{E_0} \left(1 + \frac{\alpha_p}{s}\right) \]  

(17)

where the normalization of the input signal is made by the division by \(E_0\), and where the gains (with dimension angular frequency) typically are selected as \(\alpha_p < \alpha_p \ll a_c\). To the PLL-controller output, \(\omega_1\) is added, and the sum signal is then integrated to form the transformation angle as

\[ \theta = \frac{1}{s} [F_p(s) \text{Im}[\mathbf{E}] + \omega_1]. \]  

(18)

The PLL thus forces \(\text{Im}[\mathbf{E}]\) to zero in the steady state (disturbances disregarded), leaving \(\Re[\mathbf{E}] = E_0\). Assuming power-invariant space-vector scaling or per unit values, the complex converter input power is given by \(\mathbf{S} = \mathbf{E}\mathbf{i}^s\) [39]. With \(\mathbf{E} = E_0\), we thus have

\[ \mathbf{S} = P + j Q = E_0(i_d - j i_q) \]  

(19)

which shows that \(i_d\) and \(-i_q\) are the active-power-producing and reactive-power-producing current components, respectively.
2) DVC: The purpose of the DVC is to make \( v_d \) track its reference \( v_d^{\text{ref}} \). With \( W_d = C_d v_d^2/2 \), the energy balance of the dc link can be expressed as
\[
dW_d \over dt = C_d v_d^2/2 = P - P_l
\]  
where \( P = \text{Re}(S) \) and \( P_l \) is the load power, including the converter losses (\( P_l < 0 \) for inverter operation). Since \( i_d \) is the active-power-producing current component, the following control law can be used:
\[
i_d^{\text{ref}} = F_d(s)(W_d^{\text{ref}} - W_d), \quad W_d^{\text{ref}} = C_d(v_d^{\text{ref}})^2/2.
\]  
This allows the DVC to be structurally similar to the PLL controller (17)
\[
F_d(s) = \frac{a_d}{L_0} \left( 1 + \frac{a_d}{s} \right)
\]  
and a similar parameter selection recommendation applies, i.e., \( a_d < a_d \ll a_c \) (a deviation of the value of \( C_d \) used in the control system from the actual value does not give a static control error, but effectively alters \( a_d \)).

III. PASSIVITY PROPERTIES OF THE INNER INPUT ADMITTANCE

In this section, the impact of the outer control loops is disregarded, reducing the control system to the CC and the PWM. Yet, the important findings can be made by analyzing this reduced system. Because this system is linear and balanced, such an analysis is relatively straightforward [3]. The total time delay plays an important role at harmonic frequencies. Therefore, the elaborate discussions concerning this parameter are first made.

A. Total Time Delay

In digital VSC control systems, particularly for two-level VSCs, it is useful to sample the converter current synchronously in between switchings, i.e., coinciding with the peaks of the triangular carrier signal for suboscillation PWM, as shown in Fig. 2. Thereby, switching harmonics are suppressed from the samples, often allowing antialiasing filtering to be avoided [35]. There are two variants of this principle.

1) Double-Update PWM: In this variant, samples are taken both at the positive and negative peaks, as shown in Fig. 2. The sampling frequency is twice the switching frequency. The computation time of the CC is generally a fraction of \( T_s \), but it is not negligible. For this reason, it is common practice to delay the update of the phase-voltage reference \( v_d^{\text{ref}} \) (given here without a specific phase notation) by one sampling interval, as shown in Fig. 2 (dotted arrows). The current sample taken at time \( t = kT_s \) updates \( v_d^{\text{ref}} \) at \( t = (k + 1)T_s \), and so on. Thereby, even short pulses, such as that at \( t = (k + 3)T_s \), can be generated without any error due to computation. Each update of \( v_d^{\text{ref}} \) affects just one switching event, which can occur minimum immediately and maximum \( T_s \) after the update. The average (and unavoidable) PWM time delay is \( 0.5T_s \), which results in the total time delay (on average) \( T_d = 1.5T_s \).

2) Single-Update PWM: In this variant, the sampling frequency is set equal to the switching frequency. Consequently, the value of \( T_s \) is in this case twice that in double-update PWM (given that the same switching frequency is used in both cases). Current sampling is made either at the positive or negative peaks of the carrier signal, as shown in Fig. 3. Provided that the CC computation time is shorter than \( 0.5T_s \), reference update can be delayed just until the next peak of the opposite sign (rather than the entire period \( T_s \)) [44]. Each update of \( v_d^{\text{ref}} \) affects two switching events, which occur symmetrically about the (in this example, negative) peak. The average PWM time delay taken over two such consecutive switching events is obviously \( 0.5T_s \), giving \( T_d = T_s \).

B. Passivity Properties for Different Total Time Delays

In [2] and [5], it is shown that the negative-real-part region caused by the total time delay begins at the critical frequency \( f_{\text{crit}} \) at which \( \text{cos} \omega T_d \) (for \( \omega = 2\pi f_{\text{crit}} \)) changes sign from positive to negative, i.e., for \( 2\pi f_{\text{crit}} T_s = \pi/2 \), giving
\[
f_{\text{crit}} = \frac{1}{4T_d} = \frac{f_s}{4} \frac{T_s}{T_d}
\]  
For double-update PWM and single-update PWM, i.e., respectively, with \( T_d = 1.5T_s \) and \( T_d = T_s \), we obtain \( f_{\text{crit}} = f_s/6 \) and \( f_{\text{crit}} = f_s/4 \), respectively. Let us verify this numerically. A normalized switching frequency of 100 p.u. is considered (with the synchronous frequency as base frequency). This is a reasonable value, accounting for, e.g., a 5-kHz switching frequency at \( f_s = 50 \text{ Hz} \). Consequently, \( \omega_s = 200 \text{ p.u.} \) for double-update PWM and \( \omega_s = 100 \text{ p.u.} \).
for single-update PWM. CC (11) is used, with $\alpha_c = 8$ p.u.
Equation (15) shows that this selection yields $\phi_m = 61^\circ$
for single-update PWM with $T_d = T_s$ and $\phi_m = 68^\circ$
for double-update PWM with $T_d = 1.5T_s$, which both are
generous values. R parts (ROGIs), with $\alpha_h = 0.2$ p.u. for all $h$,
are included for $+1, -1, -5, +7, -11, +13$ in the $\alpha\beta$ frame,
 i.e., for $h = 0, -2, \pm 6, \pm 12$ in (5). The R-part compensation
angles are selected as a compensation of the time delay at the
R-part frequency [4]

$$
\phi_h = h\omega_1 T_d.
$$

(24)

In Fig. 4, the real parts of the inner input admittance are shown in the frequency region up to the Nyquist frequency,
i.e., $-\omega_c/2 \leq \omega \leq \omega_c/2$ (since the continuous-time models
used here do not account for the effects of aliasing and PWM,
evaluation for frequencies above the Nyquist frequency is
meaningless). As the solid curves show, for neither one of the
PWM variants is a passive system obtained; negative-real-part
regions appear above the respective critical frequencies
predicted by (23). A proposal for eliminating the negative-real-part
region up to the Nyquist frequency for $T_d = 1.5T_s$
by a scheme based on PCC-voltage feedforward can be found
in [5]. The scheme can easily be adapted to the case $T_d = T_s$

If the total time delay can be brought down to $T_d = 0.5T_s$,
i.e., just the PWM time delay, then (23) gives
$f_{\text{crit}} = f_s/2$, i.e., the negative-real-part region is eliminated and a passive
inner input admittance up to the Nyquist frequency is obtained,
as verified in Fig. 4 (dashed curves). (This fact is hinted in
[40, eq. (6)], but it is not shown explicitly. It is implicitly
shown via the Nyquist criterion in [41] and [42].) In addition,
$\alpha_c$ can be increased, for a certain $\phi_m$, by 1.5/0.5 = 3 for
double-update PWM and by 1/0.5 = 2 for single-update
PWM, as shown by (16).

It is generally easier to reduce $T_d$ to (or close to) 0.5$T_s$
for single-update PWM than for double-update PWM [43]–[45].
This is because for single-update PWM, the sampling instant
can be shifted between the positive and negative peaks,
as shown in Fig. 5 (where the computational time delay is $\sim 0.2T_d$). When $v_{\text{ref}}$ suddenly decreases, the current-sampling instant is shifted from the negative to the positive
carrier peak. This allows the short positive pulse about
$t = (k + 1)T_s$ to be generated without timing error.

For double-update PWM, the sampling instants can be
shifted away from the peaks, but at the expense of a greatly
increased harmonic content of the current samples [40].

1) Example: Single-update PWM with $f_s = 10$ kHz and
$T_d = T_s$ is implemented in the control system for an
LCL-filter-equipped VSC operating with $f_1 = 50$ Hz. The resonant frequency is 2.1 kHz, i.e., below $f_{\text{crit}} = f_s/4 = 2.5$ kHz.
As can be observed in Fig. 6, the system is initially stable.
At the center of the displayed time interval, the interrupt for
current sampling is shifted, so that $T_d = 1.5T_s$ is obtained,
giving $f_{\text{crit}} = f_s/6 = 1.7$ kHz. The resonance now falls
within the negative-real-part region, and as a result, growing
oscillations commence.

C. Passivity Properties About the R-Part Frequencies

Fig. 7 shows the detail about $-11$. For the compensation-angle
selection (24), $\text{Re}\{\mathbf{Y}_i^1(\omega)(-11\omega)\}$ is a local minimum, so
a local negative-real-part region is avoided, as the solid curve
shows. This holds for all R-part frequencies for which
$\cos(\omega_1 T_d) > 0$ and $|\omega_1| < \omega_c/2$ [4].
Introducing a perturbation also in the \(dq\)-frame angle, as 
\[ \theta = \omega_1 t + \Delta \theta, \]
gives 
\[ E = e^{-j \theta} E^t = e^{-j \Delta \theta} (E_0 + \Delta E). \]
This relation can be linearized by approximating 
\[ e^{-j \Delta \theta} \approx 1 - j \Delta \theta \]
and by neglecting cross terms between the perturbation quantities, yielding
\[ E = E_0 + \Delta E - j E_0 \Delta \theta. \]  \hfill (26) 

Substitution of (26) in (18) results in
\[ \Delta \theta = \frac{F_p(s) \text{Im}[\Delta E] - E_0 F_p(s) \Delta \theta}{s + E_0 F_p(s) - G_p(s)}, \]  \hfill (27) 

If the integral term of (17) is neglected, then 
\[ G_p(s) = [a_p/(s + a_p)]/E_0, \]  i.e., \( a_p \) is the closed-loop PLL bandwidth. 

The \(dq\)- and \(ab\)-frame CCs (4) and (11) are equivalent under the design premises stated, concerning the current control loop only. However, they differ concerning their PLL impact [15]. For an \(ab\)-frame CC, the \(dq\)-frame reference \(i_{ref}^{ab} \) is transformed into the \(a\beta\) frame as
\[ i_{ref}^{ab} = e^{j \theta} i_{ref}^{dq} = e^{j(\theta + \Delta \theta)} (i_0 + \Delta i_{ref}). \]  \hfill (28) 

where \( \Delta i_{ref} \) is the perturbation about the mean value 
\[ i_0 = i_{d0} + j i_{q0}. \]
Since a constant \( i_{ref}^{dq} = i_{q0} \) is assumed, 
\[ \Delta i_{ref} = \Delta i_{dref}, \]
where \( \Delta i_{dref} \) is the perturbation impact from the DVC. Equation (28) can be linearized as 
\[ i_{ref}^{dq} \approx e^{j (\theta + \Delta \theta)} (i_0 + \Delta i_{ref}) = e^{j \theta} (i_0 + \Delta i_{ref}^{aq}). \]  \hfill (29) 

where
\[ \Delta i_{ref}^{aq} = \Delta i_{dref} + j G_p(s) i_0 \text{Im}[\Delta E]. \]  \hfill (30) 

The PLL thus acts as an added reference perturbation (in the \(q\)-direction only in case \( i_0 \) is real). The closed-loop impact of (30) can be determined by considering (6) for the perturbation quantities, i.e., 
\[ \Delta i = G_{cl}(s) \Delta i_{ref}^{aq} + Y_i(s) \Delta E. \]  \hfill (31) 

Before proceeding to include the DVC, let us discuss the impact just of the PLL by assuming \( \Delta i_{dref} = 0 \). Since (30) includes \( \text{Im}[\Delta E] \), it is obvious that the model no longer is balanced [27]. Yet, a complex space-vector model can be employed using the identity 
\[ \text{Im}[\Delta E] = (\Delta E - \Delta E^*)/(2j) \]
in (30), which gives
\[ \Delta i = Y_+(s) \Delta E + Y_-(s) \Delta E^*. \]  \hfill (32) 

where
\[ Y_+(s) = Y_i(s) - Y_-(s), \quad Y_-(s) = -\frac{G_{cl}(s) G_p(s) i_0}{2}. \]  \hfill (33) 

Equation (32) shows that, if \( \Delta E \) contains just one frequency component, e.g., \( \Delta E = \Delta E e^{j \omega t} \), then—because of the imbalance—two components appear in \( \Delta i \), as 
\[ \Delta i = Y_+(j \omega) \Delta E e^{j \omega t} - Y_-(j \omega) \Delta E^* e^{-j \omega t}. \]  \hfill (34) 

Component \( \Delta i_+ \), which may be called an image [27], is negative sequence in the \(dq\) frame, but as long as \( \omega < \omega_1 \), it is positive sequence in the \(a\beta\) frame; the components there appear as sideband components of \(+1\), at \( \omega_1 \pm \omega \).
Both components, in turn, affect $\Delta E$ via the negative feedback described by (3). If $Z(s)$ has a resonance at, or close to, $+\omega$, then $\Delta \alpha$ will be amplified, giving a large amplitude $|\Delta E_+|$ (initially exponentially growing if the resonance gets destabilized). Because the grid is assumed to be balanced, $|Z^*(-j\omega)| = |Z^*(j\omega)|$, whereas $|Z^-|$ is not zero for $\omega \neq 0$. Consequently, $\Delta \alpha$ is amplified much less by the resonance than $\Delta \alpha$. The dominant component of $\Delta E$ is still $\Delta E_+ e^{i\alpha_{\text{ref}}}$. This component may produce active power of nonzero mean with $\Delta \alpha$, according to $\text{Re} \{\Delta E_+ e^{i\alpha_{\text{ref}}} \Delta \alpha\} = \text{Re} \{\Delta \alpha_i\Delta \alpha^*_i\}$, whereas its interaction with $\Delta \alpha$ just produces active-power pulsations of the angular frequency $2\omega$. This motivates neglecting the impact of $\Delta \alpha_i$. Impact of the DVC revealed.

In addition, the per-phase analysis is used, which can be linearized as $e.g.,$ for subsynchronous torsional interaction [33] and analysis for interaction with an imbalanced grid, which is the case, degenerated cases the assumptions may not hold. Moreover, for interaction with an imbalanced grid, which is the case, for subsynchronous torsional interaction [33] and analysis of multiple-converter systems, a multivariable model, as in [2], must be used.

Similar conclusions are drawn in [10], [15], and [17], though using somewhat different motivations and a different linearization method. In addition, the per-phase analysis is used, which does not allow the existence of an image component to be revealed.

B. Impact of the DVC

We now proceed to determine the impact of the DVC. Combining (20) with (21) and the relation $P = \text{Re} \{Ei^{*}\} = \text{Re} \{Ei^{*}\}$ yields

$$i_d^{\text{ref}} = F_d(s) \left[ W_d^{\text{ref}} - \frac{\text{Re} \{(E_0 + \Delta E)^{*} (i_0 + \Delta i) - P_i\}}{s}\right]^{(35)}$$

which can be linearized as

$$\Delta i_d^{\text{ref}} = \frac{F_d(s)}{s} \text{Re} \{E_0 \Delta i + i_0 \Delta E^{*}\}. \quad (36)$$

Substitution of (31) in (36), noting that $\text{Re} \{i_0 \Delta E^{*}\} = \text{Re} \{i_0 \Delta E\}$, yields

$$\Delta i_d^{\text{ref}} = \frac{F_d(s)}{s} \text{Re} \{E_0 [G_c(s) \Delta i_d^{\text{ref}} + Y_i(s) \Delta E] + i_0^{\text{ref}} \Delta E\}. \quad (37)$$

This relation has, due to the real part of $\Delta i_d^{\text{ref}}$, $\Delta i_d^{\text{ref}}$ on both sides. To allow solving for $\Delta i_d^{\text{ref}}$, the approximation $G_c(s) \approx 1$ is made. Since $a_c > a_d$, frequency components outside the passband of $G_c(s)$ are well attenuated by $F_d(s)/s$, so neglecting the filtering effect of $G_c(s)$ is reasonable.

We thus obtain

$$\Delta i_d^{\text{ref}} \approx \frac{F_d(s)}{s} \text{Re} \{E_0 \Delta i^{\text{ref}} + [E_0 Y_i(s) + i_0^{\text{ref}}] \Delta E\} \quad (38)$$

where, from (30), $\text{Re} \{\Delta i_d^{\text{ref}}\} = \Delta i_d^{\text{ref}} - i_0 G_p(s) \text{Im} \{\Delta E\}$. For brevity, we shall neglect the term $-i_0 G_p(s) \text{Im} \{\Delta E\}$, which tends to have a small impact, particularly at high power factors. This allows (38) to be simplified to

$$\Delta i_d^{\text{ref}} = \frac{F_d(s)}{s + E_0 F_d(s)} \text{Re} \{E_0 Y_i(s) + i_0^{\text{ref}} \Delta E\}. \quad (39)$$

If the integral term of (22) is neglected, then $G_d(s) = ([a_d/(s + a_d)]/E_0$, i.e., $a_d$ is the bandwidth of the direct-voltage control loop. The complete relation between $\Delta E$ and $\Delta \alpha$, including PLL and DVC impact, can now be obtained by substituting (39) in (30) and using the identities $\text{Re} \{\Delta E\} = (\Delta E + \Delta E^*)/2$ and $\text{Im} \{\Delta E\} = (\Delta E - \Delta E^*)/(2j)$. The result is identical to (32), but with

$$Y_+(s) = Y_i(s) + \frac{G_c(s)}{2} \left\{ (G_p(s) k_0 - G_d(s) [i_0^{\text{ref}} + E_0 Y_i(s)]) \right\} \quad (40)$$

as $Y_-(s)$ is not used in the following, its expression is omitted.

Remark 5: As previously mentioned, the control loop for the PCC-voltage magnitude or the reactive power is disregarded in order to save space. However, inclusion in the analysis of its impact is straightforward. For example, a PCC-voltage-magnitude control law $i_d^{\text{ref}} = F_d(s)(E_0 - |E|)$ can be shown to add a term $-jG_c(s) F_d(s)/2$ to (40).

C. Impact of the PLL for a dq-Frame CC

A different impact of the PLL on the total input admittance is obtained for a dq-frame CC than for an $\alpha\beta$-frame CC [46]. This is because two coordinate transformations, of $\hat{u}$ into the $dq$ frame and of $v_{\text{ref}}$ into the $\alpha\beta$ frame, are used, thus adding two sources of PLL impact. Straightforward calculations show that this adds a term $-Y_{\beta}(s)G_p(s)/2$ to (40).

D. Examples of Passivity Properties Versus Resonance Destabilization

We shall now, by two examples, correlate numerically the passivity properties of $Y_+(s)$ given by (40) with the occurrences of resonance destabilization found by simulation in MATLAB. The grid impedance is selected as a parallel–series inductive–capacitive impedance

$$Z'(s) = s L_g || \left( s L_s + \frac{1}{s C_s} \right) = \frac{s L_s (s^2 L_g C_s + 1)}{s^2 (L_g + L_s) C_s + 1} \quad (41)$$

i.e., the angular resonant frequency in the $\alpha\beta$ frame is $\omega_{\text{res}} = 1/(s L_s + L_s) C_s^{1/2}$. $L_g = L = 0.1$ p.u. and $L_s = 1$ p.u. are selected, whereas $C_s$ is varied in order to obtain the desired $\omega_{\text{res}}$, as explained in the following.

In the control system—which uses an $\alpha\beta$-frame CC—an R part is included only for $+1$, i.e., for $h = 0$ in (5), with $a_h = 0.5$ p.u. The PLL and the DVC have the parameters $\alpha_p = a_d = 0.2$ p.u. and $\alpha_{\text{ref}} = a_d = 0.05$ p.u., except where noted otherwise. The grid voltage is adjusted so as to give $E_0 = 1$ p.u., the reactive-power exchange is zero, i.e., $i_0 = 0$, and the system is considered lossless. The last two assumptions yield $i_0 = P_I/E_0$.

1) Example 1: Inverter Operation: A power injection $P_I = -0.9$ p.u. into the dc link is considered. This scenario could account for, e.g., a photovoltaic inverter. To allow the PLL to quickly track variations in the PCC-voltage angle, $\alpha_p = 2$ p.u. is used in this example (see [10] for a similar selection). The angular resonant frequency $\omega_{\text{res}}$ is adjusted
until marginally stable operation is reached, i.e., a constant-amplitude oscillation occurs, as shown in Fig. 8(a). This correlates well with Fig. 8(b), where the real part of \( Y_s(s) = Y_s(s - j\omega_1) \) is shown: \( \omega_{res} = 1.38 \text{ p.u.} \) is located near the upper boundary of the negative-real-part region. Fig. 8(c) shows the discrete Fourier transform (DFT) modulus of \( \mathbf{i}_s \) in a logarithmic scale. The components \( |\mathbf{i}_s^+| \) and \( |\mathbf{i}_s^-| \), located symmetrically about \( \omega_1 = 1 \text{ p.u.} \), at \( \omega_{res} \) and \( 2\omega_1 - \omega_{res} \), respectively, can be observed. The corresponding components in \( \mathbf{E}_s \) are shown in Fig. 8(d). It can be noted that the lower sideband component has roughly 30 dB smaller amplitude than the upper sideband component, and therefore can be neglected, as discussed in Section IV-A.

The PLL gives a negative impact for inverter operation [2], which can be deduced by the multiplication by \( i_0 \) of \( G_p(s) \) in (40). The dashed curve in Fig. 8(b) shows that a much smaller negative-real-part region is obtained for inverter operation with the same parameter values. For inverter operation, \( \alpha_p \) should not be made larger than necessary to obtain acceptable dynamic performance, in order to minimize the negative-real-part region.

2) Example 2 (Rectifier Operation): A power draw of \( P_l = 0.9 \text{ p.u.} \) from the dc link is now considered. This scenario could account for, e.g., a back-to-back ac motor drive. To give sufficiently small variations in \( n_d \) for variations that may occur in \( P_l \) (though not included in the simulation), a larger \( \alpha_d = 0.5 \text{ p.u.} \) is this time used. The results shown in Fig. 9 are very similar to those in Fig. 8, although the negative-real-part region this time is slightly smaller. Consequently, a slightly lower \( \omega_{res} \) is needed to give marginally stable operation.

The DVC gives negative impact mainly for rectifier operation [2], which can be deduced by the multiplication by \(-i_0^*\) of \( G_d(s) \) in (40). The dashed curve in Fig. 9(b) shows that a much smaller negative-real-part region is obtained for inverter operation with the same parameter values. For rectifier operation, \( \alpha_d \) should not be made larger than necessary to obtain acceptable dynamic performance, in order to minimize the negative-real-part region.

Remark 6: It is interesting to note in (40) that, for \( G_p(s) = G_d(s) \) and a real \( \mathbf{i}_0 \), terms \( G_p(s)\mathbf{i}_0 - G_d(s)i_0^* \) cancel in (40). Thus, the identical selections of the PLL controller (17) and the DVC (22) remove the dependence of \( i_d0 \) from the input-admittance characteristics, giving the same passivity properties for both inverter and rectifier operations. For moderate gain selections, then very small negative-real-part regions are obtained, as shown in Fig. 10(a).

Remark 7: If a \( dq\)-frame CC is used, a widening of the negative-real-part region tends to result, as shown in Fig. 10(b). It is, therefore, generally preferable to use an \( \alpha\beta\)-frame CC.
V. CONCLUSION

An overview of methods for stability assessment based on the passivity properties of the VSC input admittance was presented. The modeling and analysis method in [10], [15], and [17] was generalized. Design recommendations for minimizing the negative-real-part regions were derived. These recommendations, which, in terms of passivity properties, clarify the key results of papers cited in the text, can be summarized as follows:

1) make the total time delay $T_d$ as small as possible. If it can be made equal just to the PWM time delay 0.5Ts, [40]–[42], then a positive real part of the inner input admittance is obtained up to the Nyquist frequency;

2) use proper selection of the R-part compensation angles $\phi_a$ according to (24), particularly if R parts are included for higher harmonic orders [4];

3) do not select the bandwidths of the outer loops, i.e., the PLL and the DVC, unnecessarily large, this particularly applies for the PLL in inverter operation and the DVC in rectifier operation [2], [11], [23];

4) use an $\alpha\beta$-frame CC to reduce the PLL impact [46].

REFERENCES


**Lennart Harnefors** (S’93–M’97–SM’07) was born in Eskilstuna, Sweden, in 1968. He received the M.Sc., Licentiate, and Ph.D. degrees in electrical engineering from the Royal Institute of Technology (KTH), Stockholm, Sweden, in 1993, 1995, and 1997, respectively, and the Docent degree in industrial automation from Lund University, Lund, Sweden, in 2000. He was with Mälardalen University, Västerås, Sweden, from 1994 to 2005, where he was appointed as a Professor in 2001. From 2001 to 2005, he was, in addition, a part-time Visiting Professor of Electrical Drives with the Chalmers University of Technology, Gothenburg, Sweden. Since 2005, he has been with ABB, where he is currently a Senior Principal Scientist with Corporate Research, Västerås. He is, in addition, a part-time Adjunct Professor of Power Electronics with KTH. His current research interests include analysis and control of power electronic systems, in particular, grid connected converters and ac drives.

**Xiongfei Wang** (S’10–M’13) received the B.Sc. degree in electrical engineering from Yanshan University, Qinhuangdao, China, in 2006, the M.Sc. degree from the Harbin Institute of Technology, Harbin, China, in 2008, and the Ph.D. degree from Aalborg University, Aalborg, Denmark, in 2013. He has been with Aalborg University since 2009, where he is currently an Assistant Professor with the Department of Energy Technology. His current research interests include grid converters for renewable energy systems and microgrids, harmonics analysis and control, passive and active filters, and stability of power electronic-based power systems.

Dr. Wang received the IEEE Power Electronics Transactions Second Prize Paper Award in 2015. He is an Associate Editor of the IEEE TRANSACTIONS ON INDUSTRY APPLICATIONS, and served as a Guest Associate Editor of the IEEE JOURNAL OF EMERGING AND SELECTED TOPICS IN POWER ELECTRONICS of the Special Issue on Harmonic Stability and Mitigation in Power Electronics Based Power Systems.

**Alejandro G. Yepes** (S’10–M’12) received the M.Sc. and Ph.D. degrees from the University of Vigo, Vigo, Spain, in 2009 and 2011, respectively. He has been with the Department of Electronics Technology, University of Vigo, since 2008. His current research interests include control of switching power converters.

**Frede Blaabjerg** (S’86–M’88–SM’97–F’03) was with ABB-Scandia, Randers, Denmark, from 1987 to 1988. From 1988 to 1992, he was a Ph.D. Student with Aalborg University, Aalborg, Denmark. He became an Assistant Professor in 1992, an Associate Professor in 1996, and a Full Professor of Power Electronics and Drives in 1998. His current research interests include power electronics and its applications, such as in wind turbines, PV systems, reliability, harmonics, and adjustable speed drives. Dr. Blaabjerg has received 15 IEEE Prize Paper Awards. He received the IEEE PELS Distinguished Service Award in 2009, the EPE-PEMC Council Award in 2010, the IEEE William E. Newell Power Electronics Award in 2014, and the Villum Kann Rasmussen Research Award in 2014. He was the Editor-in-Chief of the IEEE TRANSACTIONS ON POWER ELECTRONICS from 2006 to 2012. He was a Distinguished Lecturer of the IEEE Power Electronics Society from 2005 to 2007, and the IEEE Industry Applications Society from 2010 to 2011. He was nominated between the most 250 cited researchers in engineering in the world by Thomson Reuters in 2014.