Sideband Harmonic Instability of Paralleled Inverters with Asynchronous Carriers

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Abstract—Paralleled inverters with asynchronous carriers are increasingly emerging into renewable power plants and other power-electronic-based power systems. The sideband harmonics resulting from the pulse width modulation tend to be different from one inverter to another, which may trigger sideband harmonic resonances among the paralleled inverters. To address the sideband harmonic instability, the nonlinearity of the digital pulse width modulator is analyzed first in this paper based on the harmonic balance principle. It is revealed that the dynamic sideband components corresponding to a given small-signal perturbation can be far below the Nyquist frequency, and they may not be effectively attenuated by anti-aliasing filters. A multi-frequency impedance model is then developed for the inverter, which characterizes the dynamic coupling between the perturbed frequency component and the sideband frequency component. The impact of asynchronous carriers on the sideband harmonic instability of paralleled inverters can thus be analyzed, and the frequency and phase sequence of the resulting oscillation components are predicted. Experimental results of two paralleled inverters confirm the effectiveness of the developed model and the stability analysis.

Index Terms—Asynchronous carriers, grid-connected inverters, sideband harmonics, multi-frequency model.

I. INTRODUCTION

Over the past decade, renewable energy based distributed power generations have been continuously integrated into power grids [1]. Multi-parallel grid-connected inverters are increasingly found in renewable power plants and distribution networks. The dynamic coupling of paralleled inverters through the upstream grid impedance tends to bring in a wide frequency range of resonances, challenging stable operations of modern power systems [2].

Low-frequency resonances are often associated with the power and synchronization control loops of inverters, including the Phase-Locked Loop (PLL), DC-link voltage control and power control. It is reported that the PLL tends to make the inverter behave like a negative resistor at the point of coupling (PCC) [3], [4], which may lead to resonances or unexpected harmonics under the weak grid condition [5]. The stability impact of the DC-link voltage control is investigated in [6], considering the different grid conditions. It is revealed that the PLL and reactive power control loop can introduce a partial positive feedback into the dc-link voltage control in weak grids, which may destabilize the system [7]. Moreover, oscillation phenomena in the time scale of DC-link voltage control may take place among the paralleled inverters, which resembles the rotor dynamics of conventional Synchronous Generators (SGs) [8]. Apart from the low-frequency resonances, the harmonic frequency resonances may also be triggered by the interactions of the wideband control loops, e.g. current control loop, of paralleled inverters. Moreover, the passive components, such as the commonly used LCL-filters of inverters, reactive power compensation capacitors and parasitic capacitors of power cables, tend to introduce or magnify the harmonic resonances [9]-[11]. Such phenomena are increasingly reported in renewable power plants, electrical traction system [12]-[13], challenging the grid stability and power quality.

To effectively address the challenges above, the impedance-based approach has been recently developed in [14], [15], which not only provides an intuitive insight of the interactions among the paralleled inverters but also enables to reshape inverter control output impedance for stabilizing the power system [16]-[17]. The impedance model developed for the stability analysis is usually based on the averaged small-signal model, where the sideband harmonics generated by the digital modulator have been neglected by small ripple approximation [18]. However, this is not always true. By employing the high-order filters, such as LCL filter, LLCL filter, LTCL filter, etc., the resonance frequency of filters can be designed to be near or even beyond the Nyquist frequency for a cost-effective design without compromising the quality of the injected grid current [19]-[21]. In this case, the sideband components may impose a significant impact on the dynamics of the inverters. The sideband harmonic resonance may be triggered among the paralleled inverters when their carrier waveforms of modulation are not ideally synchronized. Such high-frequency resonances may induce higher power losses in the passive components which can make them overloaded. But this resonance cannot be characterized by the conventional impedance models. A crossover filter has been used in [22] to characterize the asynchronous-carrier-related resonance. However, this approach merely provides an intuitive explanation on the effect of sideband harmonics. It fails to mathematically reveal the underlying dynamic mechanism. In [23], a multi-frequency impedance matrix has been developed to reveal the sideband harmonic oscillations in dc nano grid. Yet, both the multi-frequency model and the impedance matrix are established at the dc steady-state operating point, which is not readily used for the paralleled inverters, due to the time-varying operating point with the sinusoidal fundamental waveform. In [24], multiple-frequency input admittance is derived to assess the passivity of the inverter above the Nyquist frequency. It only considers the sideband harmonics generated by sampling processes, while neglecting the sideband harmonics generated by the sinusoidal...
PWM modulator.

In this paper, a multi-frequency transfer function matrix is proposed for digital sinusoidal PWM modulator based on the harmonic balance principle, where the relationship between the perturbation frequency component and the sideband frequency component is established by injecting a perturbation along a given periodically time varying operation trajectory. Based on the transfer function matrix, a multi-frequency impedance model of the inverter is developed to accurately characterize the output behavior at the PCC including the sideband components. The dynamic impacts of asynchronous carriers on paralleled inverters can thus be mathematically revealed, and consequently, the characteristic of the sideband harmonic instability can be identified. Experimental results from two paralleled inverters confirm the theoretical analysis.

II. ACCURATE MODELING OF THE DIGITAL SINUSOIDAL PWM MODULATOR

Compared with the harmonic spectrum of PWM modulation in DC system, the harmonic spectrum of sinusoidal PWM modulation in AC system is much more complicated when the reference modulating waveform contains both the fundamental and perturbation components. Moreover, when the digital control is employed, the sampling and hold (S&H) procedure also introduces sideband components [24]. Therefore, when the sampling frequency is different from the switching frequency, the output of the digital sinusoidal PWM modulator will contain multiple sideband harmonics. Common practice in modeling the digital sinusoidal PWM modulator is to use Zero-Order Hold (ZOH), which can be expressed by:

\[ G(s) = \frac{1-e^{-Ts}}{Ts} \]  

(1)

where \( T_s \) is the sampling frequency.

However, the ZOH only takes the S&H procedure into consideration, which neglects the inherent nonlinearity of the sinusoidal PWM. As shown in Fig. 1, an obvious mismatch can be observed between the output waveform of ZOH and a digital sinusoidal PWM modulator. Hence, such a ZOH model is inadequate to capture the sideband component of the digital sinusoidal PWM modulator.

The harmonics in the output of the digital sinusoidal PWM modulator can be divided into two types. The first type is the static harmonics, which are constant and not affected by the perturbations, while the other is the dynamic harmonics which are changed along with perturbations. As for the static harmonics, the analytical methods have been well developed based on the double Fourier analysis [25]. However, the analysis of dynamic harmonics, which may have a significant influence on system dynamic performance and stability, are usually overlooked [22].

To fully capture the characteristics of dynamic harmonics, a small-signal perturbation component \( u_{sp} \) at the frequency \( \omega_p \) is intentionally added to the fundamental waveform \( U_{sp} \). The complex vectors of \( u_{sp} \) and \( U_{sp} \) in the frequency domain can be represented by:

\[ u_{sp}(\omega) = u_r(\omega) + j\mu_p(\omega) = M_p e^{j\theta_p} \delta(\omega - \omega_p) \]  

(2)

\[ U_{sp}(\omega) = U_r(\omega) + jU_p(\omega) = M_\theta e^{j\theta_\theta} \delta(\omega - \theta_\theta) \]  

(3)

where \( M_p \) and \( M_\theta \) are the modulation ratios of \( u_{sp} \) and \( U_{sp} \), respectively; \( \theta_p \) and \( \theta_\theta \) are initial phase angle of \( u_{sp} \) and \( U_{sp} \), respectively. Under small-signal assumption, we have \( M_p \ll M_\theta \).

Using one-dimensional spectral analysis method proposed in [27], the harmonic spectrum of inverter output voltages in the \( a\beta \) frame can be derived as (4)

\[ U_{sp}(\omega) = U_r(\omega) + jU_p(\omega) = M_p e^{j\theta_p} \delta(\omega - \omega_p) \]  

(4)

Fig. 1 Outputs of ZOH and the digital sinusoidal PWM modulator

Fig. 2 Digital sinusoidal PWM modulator with small-signal perturbation

frequency and sideband frequency based on the harmonic balance principle [26].

To simplify the analysis, all the variables are transferred to the \( a\beta \) frame and represented by the complex space vector. Thus, the three-phase system can be represented by single-phase system, provided that the three-phase components are balanced. As shown in Fig. 2, the angular frequency of the carrier waveform is \( \omega_c \), and the sampling frequency is \( \omega_s \). In this work, the double update of the duty cycle is adopted, i.e. the digital control system is sampled and updated twice for each switching cycle, \( \omega_s = 2\omega_c \) [22]. A small-signal perturbation component \( u_{sp} \) at the frequency \( \omega_p \) is intentionally added to the fundamental waveform \( U_{sp} \). The complex vectors of \( u_{sp} \) and \( U_{sp} \) in the frequency domain can be represented by:

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Using one-dimensional spectral analysis method proposed in [27], the harmonic spectrum of inverter output voltages in the \( a\beta \) frame can be derived as (4)
where \( \theta_c \) is the initial phase angle of the carrier, and \( m, n_0 \) and \( n_p \) are the index variables of the carrier, fundamental wave and small-signal perturbation, respectively. \( J_n \) is the \( n \) order of first kind Bessel function, which is expressed as

\[
J_n(x) = \frac{1}{\pi} e^{\pi x} \cos(n\pi - x \sin \pi) dx.
\]

According to the Fourier analysis given in (4), the harmonic spectra of the inverter output voltage in the three different cases are shown in Fig. 3. First, when the PWM reference contains only the sinusoidal fundamental waveform, as shown in Fig. 3(a), the dominant harmonics are around \( 0.5\omega_s \) and \( \omega_s \), which are static harmonics. Secondly, when the PWM reference contains only the small-signal perturbation \( \omega_s \), as shown in Fig. 3(b), the resulting sideband harmonic is at the frequency \( \omega_s - \omega_p \), which is the dynamic harmonic. Lastly, when the PWM reference contains both the sinusoidal fundamental waveform and the small-signal perturbation, as shown in Fig. 3(c), the static harmonics are the same with that in Fig. 3(a). However, the dynamic harmonics are different from that in Fig. 3(b). New sideband harmonic components are generated at the relatively low-frequency range, of which the frequencies \( \omega_{sb1} \) and \( \omega_{sb2} \) are also affected by the phase sequence of the perturbation.

In this paper, it is assumed that the dynamic harmonic components beyond the Nyquist frequency \( 0.5\omega_s \) are well attenuated by the power filter and the anti-aliasing filter. Only the lowest sideband harmonic at \( \omega_{sb} \) is taken into account when modeling the digital sinusoidal PWM modulator.

According to (2) and (4), the relationship between the perturbation and the corresponding dominant sideband components is derived based on the harmonic balance principle, which is illustrated by the multi-frequency model shown in Fig. 4. The dynamics of two frequency components, i.e., the perturbation frequency component and the sideband frequency component, have been taken into consideration. As for the positive sequence perturbation, i.e., \( \omega_p > 0 \), the dominant sideband component is \( \omega_{sb1} = \omega_p + \omega_s \). If \( s = j(\omega_p - \omega_s) \) is used as the Laplace variable at the perturbation frequency, \( s = j(\omega_p - \omega_s) \) is thereby used as Laplace variable at the sideband frequency. As for the negative sequence perturbation, i.e., \( \omega_p < 0 \), the dominant sideband component is \( \omega_{sb1} = \omega_p + \omega_s \), so \( s = j(\omega_p + \omega_s) \) is thereby used as Laplace variable of the sideband frequency.

In Fig. 4, \( G_d(s) \) represents the delay caused by the computation and ZOH, which is given by

\[
G_d(s) = e^{-1.5\pi\tau}.
\]
\[ G_i(s) = J_i \left( \frac{\pi}{2} \frac{i}{\omega} \right) M_0. \]  

\[ G_2(s) = \begin{cases} -J_i \left( \frac{\pi}{2} \frac{1 - j \omega_0}{\omega} \right) M_0 & (\omega_0 \geq 0) \\ -J_i \left( \frac{\pi}{2} \frac{1 + j \omega_0}{\omega} \right) M_0 & (\omega_0 < 0) \end{cases} \]  

It is worth noting that this multi-frequency model can be readily transformed from the \( \alpha \beta \)-frame to the \( dq \)-frame, simply by replacing the Laplace variables \( s \) and \( \tilde{s} \) with \( s + j \omega_0 \) and \( \tilde{s} + j \omega_0 \), respectively, according to the frequency translation between the \( \alpha \beta \) frame and \( dq \) frame [5].

### III. Multi-Frequency Impedance Modeling

To analyze the impact of asynchronous carriers on the stability of the paralleled inverters, the multi-frequency impedance model of the single grid-connected inverter will be firstly established, and then extended to the paralleled inverters.

#### A. Single Inverter

The control scheme of a three-phase grid-connected inverter with LCL filter is shown in Fig. 5, where \( v_{pcc,a}, v_{pcc,b}, \) and \( v_{pcc,c} \) are the three-phase grid voltages at the point of common coupling (PCC). In this paper, the inverter-side currents are controlled in the \( \alpha \beta \) frame, and the capacitor voltage \( v_c \) is fed forward to actively damp resonance peak of the LCL filter. \( G_i \) is the current regulator, and \( G_c \) is the feedforward function.

The corresponding control block diagram is shown in Fig. 6, from which the control output admittance considering the sideband effect can be derived as:

\[ Y_e = [Y_{anv} + Y_c] \times [Y_{anv} + Y_c + Y_{anv}]^{-1} \times Y_{anv}. \]  

where \( Y_c \) and \( Y_{anv} \) are the admittance matrices of capacitor \( C \) and inductor \( L_z \), which are expressed as \( Y_c = [sC \ 0 \ 0 \ sC] \), \( Y_{anv} = [1/sL_z \ 0 \ 0 1/sL_z] \), and \( Y_{anv} \) is the closed-loop admittance matrix of the L-inverter, which is expressed as (10) at the bottom of this page, where \( I \) is the identity matrix. Similarly, the grid impedance matrix is denoted by \[ Z_g = \begin{bmatrix} sL_z & 0 \\ 0 & 1/sL_z \end{bmatrix}. \]  

As seen, the dynamic behavior of the single inverter is represented by the two-input-two-output impedance matrix, which contains both the perturbation frequency component and the sideband frequency component, as well as the coupling effect between them.

#### B. Paralleled Inverters

Based on the multi-frequency impedance model of the single inverter, the simplified circuit model of paralleled inverters is depicted by the Fig. 7, and the corresponding control block diagram is shown in Fig. 8. The dynamics of inverters are coupled with each other through the upstream grid impedance \( Z_p \). Since the cross-coupling effect between the perturbation frequency component and the sideband frequency component can be depicted by the cross-coupling impedance term in the impedance matrix with the information of the phase angle of the carrier included, the dynamic interactions among the inverters with asynchronous carriers can be well depicted, which will be analyzed in detail in the next section.
III. STABILITY ANALYSIS AND EXPERIMENTAL VERIFICATION

All the impedances/admittances are represented by two-input-two-output matrices, which can accurately model the dynamics of paralleled inverters with consideration of sideband components determined by angles of carriers. Therefore, the sideband harmonic instability caused by the asynchronous carriers can be revealed. According to Fig. 8, the return ratio for the grid current of k# inverter \( L_{gk} \) can be expressed by:

\[
L_{gk} = \left( \sum_{i=1}^{N} Y_{i} + Z^{-1} \right)^{-1} \times Y_{ak}.
\]  

By plotting the Bode diagram of the eigenvalues of the return ratio \( L_{gk} \), the stability for the grid current of the k# inverter can be justified [28]. Due to the frequency coupling effect that described by the coupling terms of the inverter impedance matrix, stability problem can be aroused even if the system is designed both interactive and commonly stable. That is to say, even if the system is designed to be stable under both the stiff grid condition and the weak grid condition, the coupling effects between perturbation frequency domain and the sideband frequency domain can trigger additional stability frequency problem. This instability cannot be predicted by the averaged small signal model, which neglects the sideband components.

The experimental setup is shown in Fig. 9, where the weak grid is realized by connecting the inductors with the grid simulator, and control algorithms of the two inverters are implemented in one dSPACE so that carrier angles of two inverters can be adjusted flexibly. The anti-aliasing filter is intentionally added to filter out the high-frequency sideband components beyond the Nyquist frequency. The circuit parameters are shown in Table I.

Fig. 10 (a) shows Bode diagrams of eigenvalues of \( L_{g1} \) when the carriers of the inverters are synchronous, i.e., \( \theta_{c1} = \theta_{c2} \). Since the coupling terms of the modulator \( G_{2}(s) \) is different as for the negative sequence and positive sequence, both the negative-sequence and positive-sequence frequency responses of \( L_{g1} \) are presented. As can be seen, the phase responses of all eigenvalues are limited within \(-180^\circ\)~\(180^\circ\). Therefore, the system should be stable according to the Nyquist stability criterion. Fig. 10 (b) shows Bode diagrams of eigenvalues of \( L_{g1} \) when inverter 1# is asynchronous with inverter 2#, i.e., \( \theta_{c1} - \theta_{c2} = 180^\circ \). As can be seen, the negative crosses are generated around \(-3.4kHz\) and \(-2.7kHz\), which implies that the system is unstable and resonances are expected to be in the negative sequence.

Figs. 11 and 12 give the steady-state and dynamic experimental results, respectively. It is clear that the system is stable when all the inverters are synchronous, and the resonance between the inverters arises when the carriers of inverters are asynchronous. According to the FFT analysis, the resonance is mainly around 2.7kHz, while the resonance at the sideband frequency 3.4kHz is almost canceled due to phase-shift of the sideband component according to Fig. 3. So the experimental results confirm the effectiveness of

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**TABLE I**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Values</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{in} )</td>
<td>600 V</td>
</tr>
<tr>
<td>( V_{g} )</td>
<td>110 V</td>
</tr>
<tr>
<td>( f_{0} )</td>
<td>50 Hz</td>
</tr>
<tr>
<td>( f_{sw} )</td>
<td>6 kHz</td>
</tr>
<tr>
<td>( f_{s} )</td>
<td>12 kHz</td>
</tr>
<tr>
<td>( L_{1} )</td>
<td>1.5mH</td>
</tr>
<tr>
<td>( C )</td>
<td>5\mu F</td>
</tr>
<tr>
<td>( L_{2} )</td>
<td>1.5mH</td>
</tr>
<tr>
<td>( L_{g} )</td>
<td>1.5mH</td>
</tr>
</tbody>
</table>

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Fig. 7  Simplified circuit model of paralleled inverters

Fig. 8  Control block diagram of paralleled inverters

Fig. 9  Configuration of the experimental setup.
developed multi-frequency impedance model and the theoretical analysis.

To eliminate the sideband harmonic instability, a straightforward way is to make all the inverter carriers synchronized. This can be realized by the global synchronization method [29] through communication channels, such as RS485, RS232, power line carrier, or optical fiber. When the communication channels are not available, it is desirable to eliminate the sideband harmonic instability by incorporating the damping control in each inverter. Compared with the traditional harmonic instability, it is more challenging to damp the sideband harmonic instability which usually occurs beyond the Nyquist frequency. Basically, the damping control can be classified into the active type and passive type. The active damping can mimic the behavior of the resistor through the control, which does not introduce additional power loss [15]. However, the damping performance of this method can be degraded by the digital control delay, especially in the high-frequency range. On the other hand, the passive damping can provide effective damping over the full frequency range by using a real resistor [30]. Nevertheless, it may result in considerable power losses. Therefore, the combined active and passive damping method could be the practical solution to address the sideband harmonic instability issue.

Fig. 10  Bode diagram of eigenvalues under (a) synchronous condition (b) asynchronous condition

Fig. 11  Steady-state waveforms when carriers are (a) synchronous, (b) asynchronous.

Fig. 12  Dynamic waveforms when carriers are adjusted (a) from synchronous to asynchronous, (b) from asynchronous to synchronous.
For the paralleled inverters with asynchronous carriers, the sideband harmonics, produced by the pulse width modulation, tend to be different from one inverter to another, and can trigger the harmonic resonance and instability among the paralleled inverters. By investigating the harmonic spectrum of the digital sinusoidal PWM modulator, the multi-frequency impedance model is established to accurately describe the output behavior of inverter at the PCC point including the sideband components. The dynamic effect of the asynchronous carriers can thus be analyzed, and the frequency and phase sequence of the resonances can be predicted. The experimental results from two-paralleled inverters confirm the effectiveness of the developed model and corresponding stability analysis.

VI. REFERENCES


