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Unified Impedance Model of Grid-Connected Voltage-Source Converters

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Abstract—This paper proposes a unified impedance model of grid-connected voltage-source converters for analyzing dynamic influences of the phase-locked loop (PLL) and current control. The mathematical relations between the impedance models in the different domains are first explicitly revealed by means of complex transfer functions and complex space vectors. A stationary ($\alpha\beta$ -) frame impedance model is then proposed, which not only predicts the stability impact of the PLL, but also reveals its frequency coupling effect. Furthermore, the impedance shaping effects of the PLL on the current control in the rotating (dq -) frame and the stationary ($\alpha\beta$ -) frame are structurally compared. The frequency-domain case studies on a three-phase grid-connected converter are next presented, and subsequently validated in time-domain simulations and experimental tests. The close correlations between the measured results and theoretical analysis confirm the effectiveness of the stationary-frame impedance model.

Index Terms—Grid, impedance model, phase-locked loop (PLL), stability, voltage-source converters (VSCs).

I. INTRODUCTION

VOLTAGE-SOURCE converters (VSCs) are increasingly used in electric power grids for efficient energy consumption and renewable energy generation [1]. Compared to electrical machines, VSCs have the wider bandwidth control dynamics, ranging from the outer power control loops (sub-synchronous frequencies) to the inner current control loop (hundreds of hertz to kHz). The dynamic interactions among the power grid and VSCs tend to cause oscillations in a wide frequency range [2].

To address the stability challenges with the grid-connected VSCs, the impedance modeling and control methods have been developed for dynamic characterization and active stabilization of VSCs [3]–[10]. However, many of the research efforts were dedicated to the stability of the inner current control with *LCL*-filters [3]–[6], where the influences of current controller and time delay of the digital control system have been thoroughly

discussed. Only a few works have recently been reported to include the effects of the grid synchronization and the outer power (dc-link voltage) control loops [7]–[10], [16], [17]. Those control loops are usually designed with a lower bandwidth than the current control, which, together with the grid impedance, tend to result in the unexpected harmonics or resonances near the fundamental frequency [2].

A wide variety of grid synchronization schemes have been reported for grid-connected VSCs. The synchronous reference frame phase-locked loop (SRF-PLL), among other schemes, has widely been used with single-phase and three-phase VSCs [11]–[14]. In the SRF-PLL, the phase detection is realized by using the Park (dq -) transformation, which is highly nonlinear and time-variant, and hence significantly complicates the small-signal modeling of the SRF-PLL [7]. Moreover, only the q -axis component is regulated for the phase tracking, which, unlike the current control, leads to an asymmetric dq -frame model [15], and consequently brings in a frequency coupling effect in the phase domain [2], [16], [17].

Two impedance models have, thus, been developed to reveal the dynamic influence of the PLL. In the first approach, the impedance model was developed in the rotating dq -frame [7]–[9], where two dq -frames, which are aligned to the measured and actual grid phase, respectively, were built. Thus, the PLL dynamics can be accounted by linearizing the transformation between the two frames [7]. The dq -frame model effectively predicts the stability impact of the SRF-PLL by means of the generalized Nyquist stability criterion. However, it fails to reveal how the impedance characteristics of the grid and VSCs contribute to the frequencies of the induced oscillations, since both the VSC output impedance and grid impedance are nondiagonal transfer matrices in the dq -frame [18].

The second method is based on the principle of harmonic balance, where the harmonic linearization of sinusoidal signals is employed to formulate the impedance model directly in the stationary $\alpha\beta$ -frame [10]. Such a representation simplifies the model as a diagonal transfer matrix, which is composed by two single-input single-output transfer functions for modeling the VSC system in the positive-sequence and negative-sequence, and it is hence also named as the sequence-domain impedance model [17]. Compared to the dq -frame model, the $\alpha\beta$ -frame model facilitates the stability analysis of multiple-VSC-based systems, where the whole system can be readily modeled in a common frame, rather than using multiple dq -frames for multiple VSCs [19]. However, the $\alpha\beta$ -frame model in [10] neglects the frequency-coupling effect brought by the

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be represented by the following transfer functions:

$$Y_p(s) = \frac{\mathbf{I}}{\mathbf{V}_o} \Big|_{\mathbf{V}=0} = \frac{1}{Ls + R_L}, \quad Y_o(s) = \frac{\mathbf{I}}{\mathbf{V}} \Big|_{\mathbf{V}_o=0} = \frac{1}{Ls + R_L} \quad (1)$$

where the VSC output voltage is denoted by the complex space vector $\mathbf{V}_o = V_{o\alpha} + jV_{o\beta}$, in the $\alpha\beta$ -frame. R_L is the equivalent series resistance of the filter inductor. Then, based on the frequency translation between the $\alpha\beta$ -frame and dq -frame [15], the L -filter plant in the dq -frame can be derived as following:

$$\begin{aligned} \mathbf{I} &= Y_p(s)\mathbf{V}_o \\ \Rightarrow \mathbf{I}_{dq} &= e^{-j\theta} Y_p(s) e^{j\theta} \mathbf{V}_{o,dq} = Y_p(s + j\omega_1) \mathbf{V}_{o,dq} \\ \Rightarrow \mathbf{Y}_{p,dq}(s) &= Y_p(s + j\omega_1) \\ \mathbf{Y}_{o,dq}(s) &= Y_o(s + j\omega_1) = Y_p(s + j\omega_1) = \frac{1}{L(s + j\omega_1) + R_L} \end{aligned} \quad (2)$$

where $e^{-j\theta} = \cos(\theta) - j\sin(\theta)$ is the complex form of the Park transformation [7], and $e^{j\theta}$ is the inverse Park transformation. ω_1 is the grid fundamental frequency (i.e., line frequency).

$G_{c,dq}(s)$ is the PI current controller in the dq -frame, which is given by

$$G_{c,dq}(s) = k_p + \frac{k_i}{s}. \quad (3)$$

Following the frequency translation, $G_{c,dq}(s)$ is transformed in the $\alpha\beta$ -frame as

$$\mathbf{G}_c(s) = G_{c,dq}(s - j\omega_1) = k_p + \frac{k_i}{s - j\omega_1} \quad (4)$$

where the center frequency ω_1 is fixed in the $\alpha\beta$ -frame current control, yet it is equivalently adapted to the PLL dynamics in the dq -frame current control.

$G_{del}(s)$ is the time delay of the digital control system, which, in the worst case, includes one sampling period (T_s) of the computational delay and half-sampling period ($0.5T_s$) delay of the pulse width modulator [20]:

$$G_{del}(s) = e^{-1.5T_s s}. \quad (5)$$

The closed-loop response of the $\alpha\beta$ -frame current control can, thus, be derived as follows:

$$\mathbf{I} = \mathbf{G}_{cl}(s)\mathbf{I}_{ref} - \mathbf{Y}_{cl}(s)\mathbf{V} \quad (6)$$

$$\mathbf{G}_{cl}(s) = \frac{\mathbf{T}(s)}{1 + \mathbf{T}(s)}, \quad \mathbf{Y}_{cl}(s) = \frac{\mathbf{Y}_o(s)}{1 + \mathbf{T}(s)} \quad (7)$$

where $\mathbf{G}_{cl}(s)$ and $\mathbf{Y}_{cl}(s)$ are the closed-loop gain and control output admittance of the $\alpha\beta$ -frame current control, respectively. $\mathbf{T}(s)$ denotes the open-loop gain of the $\alpha\beta$ -frame current control, which is given by

$$\mathbf{T}(s) = \mathbf{G}_c(s)G_{del}(s)Y_p(s). \quad (8)$$

Similarly, the open-loop gain of the dq -frame current control can be derived as

$$\mathbf{T}_{dq}(s) = G_{c,dq}(s)G_{del}(s)\mathbf{Y}_{p,dq}(s). \quad (9)$$

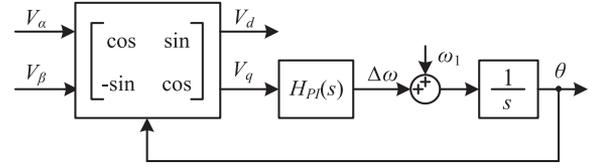


Fig. 3. Block diagram of the SRF-PLL.

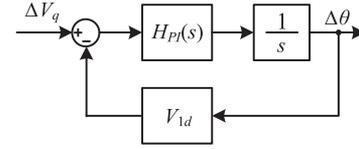


Fig. 4. Block diagram of the small-signal model of the SRF-PLL.

The closed-loop response of the current loop in the dq -frame is expressed as

$$\mathbf{I}_{dq} = \mathbf{G}_{cl,dq}(s)\mathbf{I}_{ref,dq} - \mathbf{Y}_{cl,dq}(s)\mathbf{V}_{dq} \quad (10)$$

$$\mathbf{G}_{cl,dq}(s) = \frac{\mathbf{T}_{dq}(s)}{1 + \mathbf{T}_{dq}(s)}, \quad \mathbf{Y}_{cl,dq}(s) = \frac{\mathbf{Y}_{o,dq}(s)}{1 + \mathbf{T}_{dq}(s)}. \quad (11)$$

C. PLL Model

Fig. 3 depicts the block diagram of the SRF-PLL adopted in this work, where the dq -transformation is used for the phase detection, and the q -axis voltage is regulated by a PI controller for the phase tracking [11].

Given a perturbation on the PCC voltage in the dq -frame $\Delta\mathbf{V}_{dq}$, the PCC voltage in the $\alpha\beta$ -frame can be expressed as

$$\mathbf{V} = (V_{1d} + \Delta\mathbf{V}_{dq})e^{j\omega_1 t} \quad (12)$$

where V_{1d} is the steady-state PCC voltage aligned to the d -axis, i.e., the steady-state PCC voltage vector $\mathbf{V}_{1,dq} = V_{1d} + j0$. The response of the detected phase θ corresponding to the voltage perturbation can be given as

$$\theta = \omega_1 t + \Delta\theta. \quad (13)$$

The q -axis voltage at the output of the dq -transformation is, thus, derived as follows:

$$\begin{aligned} \mathbf{V}_{dq} &= \mathbf{V}e^{-j\theta} = (V_{1d} + \Delta\mathbf{V}_{dq})e^{-j\Delta\theta} \\ &\approx (V_{1d} + \Delta\mathbf{V}_{dq})(1 - j\Delta\theta) \Rightarrow V_q = \Delta V_q - \Delta\theta V_{1d}. \end{aligned} \quad (14)$$

Then, considering the PI controller, the detected phase variation $\Delta\theta$ is given by

$$\Delta\theta = \frac{H_{PI}(s)}{s} V_q. \quad (15)$$

Substituting (15) into (14), the small-signal model of the SRF-PLL can, thus, be established, which, as shown in Fig. 4, can be derived as [2]

$$\Delta\theta = H_{PLL}(s)\Delta V_q, \quad H_{PLL}(s) = \frac{H_{PI}(s)}{s + H_{PI}(s)V_{1d}} \quad (16)$$

which illustrates that SRF-PLL is linearized as a second-order dynamic system.

III. UNIFIED IMPEDANCE MODEL WITH PLL EFFECT

This section elaborates first the dq -frame impedance model built upon the dq -frame current control with the PLL dynamics [2], [7]–[9]. Then, a unifying approach to the impedance models in the different domains is proposed by using complex space vectors and complex transfer functions. It further reveals that the transformation matrix introduced in [17] merely transforms a dq -frame impedance matrix of real space vectors into another dq -frame impedance matrix based on complex space vectors.

A. DQ-Frame Impedance Model

Similar to (14), the dq -transformations used within the dq -frame current control can also be linearized as follows:

$$\begin{aligned} \mathbf{I}_{dq} &= \mathbf{I}e^{-j\theta} = (\mathbf{I}_{1,dq} + \Delta\mathbf{I}_{dq})e^{-j\Delta\theta} \\ &\approx (\mathbf{I}_{1,dq} + \Delta\mathbf{I}_{dq})(1 - j\Delta\theta) \\ \Rightarrow \mathbf{I}_{dq} &= \mathbf{I}_{1,dq} + \Delta\mathbf{I}_{dq} - \Delta\mathbf{I}_{PLL,dq} \\ \Delta\mathbf{I}_{PLL,dq} &= j\Delta\theta\mathbf{I}_{1,dq} \end{aligned} \quad (17)$$

$$\begin{aligned} \mathbf{V}_c &= \mathbf{V}_{c,dq}e^{j\theta} = (\mathbf{V}_{c1,dq} + \Delta\mathbf{V}_{c,dq})e^{j(\omega_1 t + \Delta\theta)} \\ &\approx (\mathbf{V}_{c1,dq} + \Delta\mathbf{V}_{c,dq})(1 + j\Delta\theta)e^{j\omega_1 t} \\ \Rightarrow \mathbf{V}_{c,dq} &= \mathbf{V}_{c1,dq} + \Delta\mathbf{V}_{c,dq} + \Delta\mathbf{V}_{PLL,dq} \\ \Delta\mathbf{V}_{PLL,dq} &= j\Delta\theta\mathbf{V}_{c1,dq} \end{aligned} \quad (18)$$

where $\mathbf{I}_{1,dq} = I_{1d} + jI_{1q}$ is the steady-state VSC current. $\mathbf{V}_{c1,dq} = V_{c1d} + jV_{c1q}$ is the steady-state modulating voltage. $\Delta\mathbf{I}_{PLL,dq}$ and $\Delta\mathbf{V}_{PLL,dq}$ denote the dynamic effects of the PLL on the VSC current and the voltage reference of the modulator, respectively.

Substituting (16) into (17) and (18) leads to

$$\begin{aligned} \Delta\mathbf{I}_{PLL,dq} &= jH_{PLL}(s)\mathbf{I}_{1,dq}\Delta V_q \\ \Delta\mathbf{V}_{PLL,dq} &= jH_{PLL}(s)\mathbf{V}_{c1,dq}\Delta V_q \end{aligned} \quad (19)$$

where the input is merely the q -axis voltage, other than space vectors in (10), which are derived for the current loop only. Hence, asymmetric transfer matrices with the cross-couplings between the d - and q -axis components are inevitable when the dynamics of PLL are included [7]–[9], [15].

Fig. 5 illustrates the block diagram of transfer matrices for the dq -frame current control with the PLL effect. The transfer functions in Fig. 2(b) are represented by the symmetric transfer matrices, which are denoted with the additional superscript “ m ,” i.e., the transfer functions for the current controller $G_{c,dq}(s)$, the time delay $G_{del}(s)$, and the L -filter plant $Y_{p,dq}(s)$ are changed, respectively, as

$$G_{c,dq}(s) \rightarrow G_{c,dq}^m(s) = \begin{bmatrix} G_{c,dq}(s) & 0 \\ 0 & G_{c,dq}(s) \end{bmatrix}$$

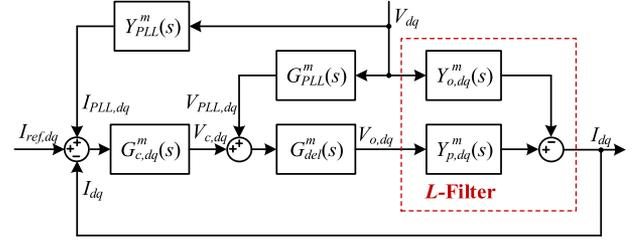


Fig. 5. Block diagram of transfer matrices for the dq -frame current control with the small-signal dynamics of the PLL.

$$G_{del}(s) \rightarrow G_{del}^m(s) = \begin{bmatrix} G_{del}(s) & 0 \\ 0 & G_{del}(s) \end{bmatrix} \quad (20)$$

$$Y_{p,dq}(s) \rightarrow Y_{p,dq}^m(s) = \frac{1}{L(s^2 + \omega_1^2)} \begin{bmatrix} s & \omega_1 \\ -\omega_1 & s \end{bmatrix} = Y_{o,dq}^m(s). \quad (21)$$

Based on (19), the PLL effects are modeled by two asymmetric transfer matrices, $Y_{PLL}^m(s)$ and $G_{PLL}^m(s)$, which are given by

$$\begin{aligned} I_{PLL,dq} &= \begin{bmatrix} I_{PLL,d} \\ I_{PLL,q} \end{bmatrix} = \underbrace{\begin{bmatrix} 0 & -H_{PLL}(s)I_{1q} \\ 0 & H_{PLL}(s)I_{1d} \end{bmatrix}}_{Y_{PLL}^m(s)} \begin{bmatrix} V_d \\ V_q \end{bmatrix} \\ V_{PLL,dq} &= \begin{bmatrix} V_{PLL,d} \\ V_{PLL,q} \end{bmatrix} = \underbrace{\begin{bmatrix} 0 & -H_{PLL}(s)V_{c1q} \\ 0 & H_{PLL}(s)V_{c1d} \end{bmatrix}}_{G_{PLL}^m(s)} \begin{bmatrix} V_d \\ V_q \end{bmatrix} \end{aligned} \quad (22)$$

where the symbol “ Δ ” is omitted for simplicity. By shifting the output of $G_{PLL}^m(s)$ to the output of $Y_{p,dq}^m(s)$ in Fig. 5, it can be seen that the open-loop output admittance is reshaped by the PLL effect on the modulating voltage $G_{PLL}^m(s)$, which is then changed as $Y_{to,dq}^m(s)$, and given by

$$Y_{to,dq}^m(s) = Y_{o,dq}^m(s) - Y_{p,dq}^m(s)G_{del}^m(s)G_{PLL}^m(s). \quad (23)$$

However, the open-loop gain of the current control loop is kept the same as (9) [21].

Including then the PLL effect on the VSC current $Y_{PLL}^m(s)$, the modified closed-loop current response is derived as

$$\begin{aligned} I_{dq} &= \begin{bmatrix} I_d \\ I_q \end{bmatrix} = G_{cl,dq}^m(s) \begin{bmatrix} I_{ref,d} \\ I_{ref,q} \end{bmatrix} \\ &\quad - \underbrace{\left\{ [I^m + T_{dq}^m(s)]^{-1} Y_{to,dq}^m(s) - G_{cl,dq}^m(s) Y_{PLL}^m(s) \right\}}_{Y_{tel,dq}^m(s)} \begin{bmatrix} V_d \\ V_q \end{bmatrix} \end{aligned} \quad (24)$$

where I^m is the unitary diagonal matrix, $T_{cl,dq}^m(s)$ and $G_{cl,dq}^m(s)$, are symmetric transfer matrices for the open-loop and closed-loop gains of the current loop, respectively, which are

$$\begin{aligned} I^m &= \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}, \quad T_{dq}^m(s) = Y_{p,dq}^m(s)G_{del}^m(s)G_{c,dq}^m(s) \\ G_{cl,dq}^m(s) &= [I^m + T_{dq}^m(s)]^{-1} T_{dq}^m(s). \end{aligned} \quad (25)$$

$Y_{\text{cl,dq}}^m(s)$ is the modified closed-loop control output admittance matrix with the PLL dynamics.

B. Unifying Approach

Basically, a second-order transfer matrix based on real space vectors can be transformed as two complex transfer functions for the corresponding complex space vector and its conjugate [15]. This complex equivalence leads to a unifying approach to the dq -frame impedance model [7]–[9] and the modified sequence-domain impedance model [17].

For illustration, a generic dq -frame impedance matrix is used first to reveal the mathematical relations between the models in the different domains. Given a general dq -frame impedance matrix $Z_{\text{dq}}^m(s)$, which is expressed by

$$V_{\text{dq}} = \begin{bmatrix} V_d \\ V_q \end{bmatrix} = Z_{\text{dq}}^m(s) \begin{bmatrix} I_d \\ I_q \end{bmatrix}, \quad Z_{\text{dq}}^m(s) = \begin{bmatrix} Z_{dd}(s) & Z_{dq}(s) \\ Z_{qd}(s) & Z_{qq}(s) \end{bmatrix} \quad (26)$$

its complex equivalent can be derived in the following [2], [15]:

$$\mathbf{V}_{\text{dq}} = \mathbf{Z}_{+, \text{dq}}(s) \mathbf{I}_{\text{dq}} + \mathbf{Z}_{-, \text{dq}}(s) \mathbf{I}_{\text{dq}}^* \quad (27)$$

$$\mathbf{Z}_{+, \text{dq}}(s) = \frac{Z_{dd}(s) + Z_{qq}(s)}{2} + j \frac{Z_{qd}(s) - Z_{dq}(s)}{2}$$

$$\mathbf{Z}_{-, \text{dq}}(s) = \frac{Z_{dd}(s) - Z_{qq}(s)}{2} + j \frac{Z_{qd}(s) + Z_{dq}(s)}{2} \quad (28)$$

where \mathbf{I}_{dq}^* is the complex conjugate of \mathbf{I}_{dq} . $\mathbf{Z}_{+, \text{dq}}(s)$ and $\mathbf{Z}_{-, \text{dq}}(s)$ are the equivalent complex transfer functions. If the impedance matrix is symmetric, i.e., $Z_d(s) = Z_{dd}(s) = Z_{qq}(s)$ and $Z_q(s) = Z_{qd}(s) = -Z_{dq}(s)$ [15], its complex equivalent will be simplified as a single complex transfer function $\mathbf{Z}_{\text{dq}}(s)$, which is given by

$$\mathbf{V}_{\text{dq}} = \mathbf{Z}_{\text{dq}}(s) \mathbf{I}_{\text{dq}}, \quad \mathbf{Z}_{\text{dq}}(s) = Z_d(s) + jZ_q(s). \quad (29)$$

However, if the impedance matrix is asymmetric, the conjugate of the complex space vector will be required, as expressed in (27). The asymmetric impedance matrix based on real space vectors consequently results in a double-frequency model with complex space vectors.

To capture the frequency-coupled dynamics of the double-frequency model, a complex transfer function matrix $\mathbf{Z}_{\pm, \text{dq}}^m(s)$ is proposed in this work, which is given by

$$\begin{bmatrix} \mathbf{V}_{\text{dq}} \\ \mathbf{V}_{\text{dq}}^* \end{bmatrix} = \underbrace{\begin{bmatrix} \mathbf{Z}_{+, \text{dq}}(s) & \mathbf{Z}_{-, \text{dq}}(s) \\ \mathbf{Z}_{-, \text{dq}}^*(s) & \mathbf{Z}_{+, \text{dq}}^*(s) \end{bmatrix}}_{\mathbf{Z}_{\pm, \text{dq}}^m(s)} \begin{bmatrix} \mathbf{I}_{\text{dq}} \\ \mathbf{I}_{\text{dq}}^* \end{bmatrix} \quad (30)$$

where $\mathbf{Z}_{+, \text{dq}}^*(s)$ and $\mathbf{Z}_{-, \text{dq}}^*(s)$ are the complex conjugates of the complex transfer functions $\mathbf{Z}_{+, \text{dq}}(s)$ and $\mathbf{Z}_{-, \text{dq}}(s)$, respectively. The complex transfer matrix is the essential idea of the unifying approach.

In the modified sequence-domain impedance model [17], a linear transformation matrix A_Z is introduced to transform the impedance matrix $Z_m dq(s)$ into the sequence-domain. However, it is worth noting that the resulting impedance matrix in [17] is also a complex transfer matrix based on complex space vectors,

and it is actually equal to the impedance matrix $\mathbf{Z}_{\pm, \text{dq}}^m(s)$ derived in (30), i.e.,

$$\mathbf{Z}_{\text{pn}}^m(s) = A_Z Z_{dq}^m(s) A_Z^{-1} = \mathbf{Z}_{\pm, \text{dq}}^m(s). \quad (31)$$

Hence, the transformed impedance matrix in [17] is still in the dq -frame, other than in the sequence-domain or in the $\alpha\beta$ -frame. Moreover, the complex equivalence given in (27) reveals the mathematical relations between the dq -frame model and the modified sequence-domain model in [17]. Yet, the frequency translation from the dq -frame to $\alpha\beta$ -frame is still overlooked in (31), and thus the frequency coupling effect revealed in [2] and [16] is not explicitly identified.

IV. STATIONARY-FRAME IMPEDANCE MODEL

This section introduces first the proposed impedance model in the $\alpha\beta$ -frame, and then presents a structural comparison of the impedance shaping effects of the PLL on the dq -frame and $\alpha\beta$ -frame current control.

A. Proposed $\alpha\beta$ -Frame Impedance Model

Considering the frequency translation of complex transfer functions [15], the complex transfer matrix in (30) and (31) can be further transformed into the $\alpha\beta$ -frame, which is elaborated in the following:

$$e^{-j\theta} \mathbf{V} = \mathbf{Z}_{+, \text{dq}}(s) e^{-j\theta} \mathbf{I} + \mathbf{Z}_{-, \text{dq}}(s) e^{j\theta} \mathbf{I}^*$$

$$\Rightarrow \mathbf{V} = e^{j\theta} \mathbf{Z}_{+, \text{dq}}(s) e^{-j\theta} \mathbf{I} + e^{j\theta} \mathbf{Z}_{-, \text{dq}}(s) e^{j\theta} \mathbf{I}^*$$

$$= \mathbf{Z}_{+, \text{dq}}(s - j\omega_1) \mathbf{I} + \mathbf{Z}_{-, \text{dq}}(s - j\omega_1) e^{j2\theta} \mathbf{I}^* \quad (32)$$

$$e^{j\theta} \mathbf{V}^* = \mathbf{Z}_{-, \text{dq}}^*(s) e^{-j\theta} \mathbf{I} + \mathbf{Z}_{+, \text{dq}}^*(s) e^{j\theta} \mathbf{I}^*$$

$$\Rightarrow e^{j2\theta} \mathbf{V}^* = e^{j\theta} \mathbf{Z}_{-, \text{dq}}^*(s) e^{-j\theta} \mathbf{I} + e^{j\theta} \mathbf{Z}_{+, \text{dq}}^*(s) e^{j\theta} \mathbf{I}^*$$

$$= \mathbf{Z}_{-, \text{dq}}^*(s - j\omega_1) \mathbf{I} + \mathbf{Z}_{+, \text{dq}}^*(s - j\omega_1) e^{j2\theta} \mathbf{I}^* \quad (33)$$

$$\begin{bmatrix} \mathbf{V} \\ e^{j2\theta} \mathbf{V}^* \end{bmatrix} = \underbrace{\begin{bmatrix} \mathbf{Z}_{+, \text{dq}}(s - j\omega_1) & \mathbf{Z}_{-, \text{dq}}(s - j\omega_1) \\ \mathbf{Z}_{-, \text{dq}}^*(s - j\omega_1) & \mathbf{Z}_{+, \text{dq}}^*(s - j\omega_1) \end{bmatrix}}_{\mathbf{Z}_{\pm}^m(s)} \begin{bmatrix} \mathbf{I} \\ e^{j2\theta} \mathbf{I}^* \end{bmatrix} \quad (34)$$

where \mathbf{I}^* is the complex conjugate of \mathbf{I} in the $\alpha\beta$ -frame, $\mathbf{Z}_{\pm}^m(s)$ is the complex transfer matrix in the $\alpha\beta$ -frame. The positive-frequency and negative-frequency of complex space vectors in the $\alpha\beta$ -frame imply that the positive-sequence and negative-sequence components of three-phase systems. For a given vector at the frequency ω , a frequency-coupled vector at the frequency $2\omega_1 - \omega$ is yielded from (34). Hence, only when the frequency ω is above $2\omega_1$, there is a coupling between the positive-sequence and negative-sequence components. Otherwise, only the positive-sequence components at two different frequencies will be brought by the asymmetric transfer matrix.

Next is to apply this equivalence to the VSC control output admittance including the PLL dynamics, i.e., to transform the admittance matrix given in (24) from the dq -frame to the

$\alpha\beta$ -frame. In (19), the q -axis voltage V_q is replaced by the complex space vectors as follows:

$$V_q = \frac{\mathbf{V}_{dq} - \mathbf{V}_{dq}^*}{2j} \quad (35)$$

which consequently leads to

$$\mathbf{I}_{PLL,dq} = \underbrace{\frac{H_{PLL}(s)\mathbf{I}_{1,dq}}{2}}_{\mathbf{Y}_{PLL}(s)} (\mathbf{V}_{dq} - \mathbf{V}_{dq}^*) \quad (36)$$

$$\mathbf{V}_{PLL,dq} = \underbrace{\frac{H_{PLL}(s)\mathbf{V}_{c1,dq}}{2}}_{\mathbf{G}_{PLL}(s)} (\mathbf{V}_{dq} - \mathbf{V}_{dq}^*) \quad (37)$$

where $\mathbf{Y}_{PLL}(s)$ and $\mathbf{G}_{PLL}(s)$ are the equivalent complex transfer functions of asymmetric transfer matrices $Y_{PLL}^m(s)$ and $Y_{PLL}^m(s)$ given in (22), respectively.

The modified open-loop output admittance is formulated by using complex transfer functions and complex space vectors, which is given by

$$\begin{aligned} \mathbf{I}_{dq}|_{\mathbf{V}_{o,dq}=0} &= \underbrace{[\mathbf{Y}_{o,dq}(s) - G_d(s)\mathbf{Y}_{p,dq}(s)\mathbf{G}_{PLL}(s)]}_{\mathbf{Y}_{to+,dq}(s)} \mathbf{V}_{dq} \\ &+ \underbrace{G_d(s)\mathbf{Y}_{p,dq}(s)\mathbf{G}_{PLL}(s)}_{\mathbf{Y}_{to-,dq}(s)} \mathbf{V}_{dq}^* \end{aligned} \quad (38)$$

where $\mathbf{Y}_{to+,dq}(s)$ and $\mathbf{Y}_{to-,dq}(s)$ are the complex transfer functions of the modified open-loop output admittance. Thus, the transfer matrix given in (23) is equivalent to two complex transfer functions. Then taking (36) into account, the modified closed-loop control output admittance can be derived by

$$\begin{aligned} \mathbf{I}_{dq}|_{\mathbf{I}_{ref,dq}=0} &= \underbrace{\left[-\mathbf{G}_{c1,dq}(s)\mathbf{Y}_{PLL}(s) + \frac{\mathbf{Y}_{to+,dq}(s)}{1 + \mathbf{T}_{dq}(s)}\right]}_{\mathbf{Y}_{tc1+,dq}(s)} \mathbf{V}_{dq} \\ &+ \underbrace{\left[\mathbf{G}_{c1,dq}(s)\mathbf{Y}_{PLL}(s) + \frac{\mathbf{Y}_{to-,dq}(s)}{1 + \mathbf{T}_{dq}(s)}\right]}_{\mathbf{Y}_{tc1-,dq}(s)} \mathbf{V}_{dq}^* \end{aligned} \quad (39)$$

where $\mathbf{Y}_{tc1+,dq}(s)$ and $\mathbf{Y}_{tc1-,dq}(s)$ are complex transfer functions of the modified closed-loop control output admittance.

Following the same procedure as (30), the complex transfer matrix of the modified closed-loop control output admittance can be given, in the dq -frame, as

$$\begin{bmatrix} \mathbf{I}_{dq} \\ \mathbf{I}_{dq}^* \end{bmatrix} = \underbrace{\begin{bmatrix} \mathbf{Y}_{tc1+,dq}(s) & \mathbf{Y}_{tc1-,dq}(s) \\ \mathbf{Y}_{tc1-,dq}^*(s) & \mathbf{Y}_{tc1+,dq}^*(s) \end{bmatrix}}_{\mathbf{Y}_{tc1\pm,dq}^m(s)} \begin{bmatrix} \mathbf{V}_{dq} \\ \mathbf{V}_{dq}^* \end{bmatrix}. \quad (40)$$

Finally, applying the frequency translation formulated in (32) and (33), the dq -frame complex transfer matrix is transformed

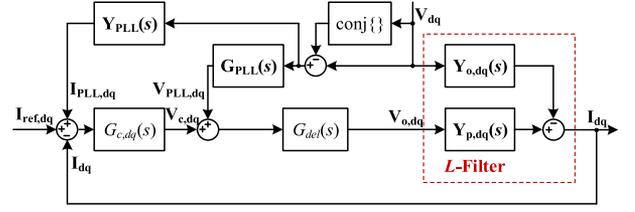


Fig. 6. Block diagram of complex transfer functions of the dq -frame current control with the PLL effect.

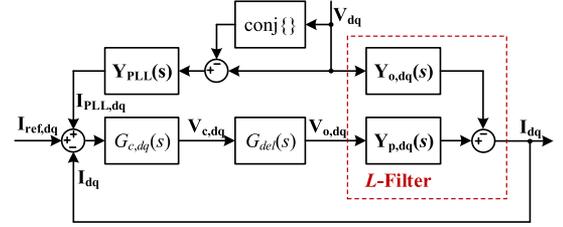


Fig. 7. Block diagram of complex transfer functions of the $\alpha\beta$ -frame current control with the PLL effect.

into the $\alpha\beta$ -frame, which is expressed by

$$\begin{aligned} \begin{bmatrix} \mathbf{I} \\ e^{j2\theta}\mathbf{I}^* \end{bmatrix} &= \underbrace{\begin{bmatrix} \mathbf{Y}_{tc1+,dq}(s-j\omega_1) & \mathbf{Y}_{tc1-,dq}(s-j\omega_1) \\ \mathbf{Y}_{tc1-,dq}^*(s-j\omega_1) & \mathbf{Y}_{tc1+,dq}^*(s-j\omega_1) \end{bmatrix}}_{\mathbf{Y}_{tc1\pm}^m(s)} \\ &\times \begin{bmatrix} \mathbf{V} \\ e^{j2\theta}\mathbf{V}^* \end{bmatrix} \end{aligned} \quad (41)$$

where $\mathbf{Y}_{tc1\pm}^m(s)$ is the $\alpha\beta$ -frame VSC control output admittance matrix including the PLL dynamics.

B. Impedance Shaping Effect of PLL

Fig. 6 shows the equivalent block diagram of Fig. 5, where, according to (36) and (37), complex space vectors and complex transfer functions are used to replace the real space vectors and transfer matrices in Fig. 5. It is shown that the PLL dynamics introduce the frequency-coupling dynamics with the complex conjugate of the PCC voltage. Also, the PLL dynamics modify the inputs of both the current controller and the modulator in the dq -frame current control, which consequently shapes both the open-loop output admittance, see (38), and the closed-loop control output admittance.

Fig. 7 illustrates the block diagram of the $\alpha\beta$ -frame current control loop with the PLL dynamics, where the current control loop is transformed into the dq -frame, following the frequency translation given in (2) and (4). Similar to (17), the PLL effect on the current reference can be derived as

$$\begin{aligned} \mathbf{I}_{ref} &= \mathbf{I}_{ref,dq} e^{j\theta} = (\mathbf{I}_{ref1,dq} + \Delta\mathbf{I}_{dq}) e^{j(\omega_1 t + \Delta\theta)} \\ &\approx (\mathbf{I}_{1,dq} + \Delta\mathbf{I}_{dq})(1 + j\Delta\theta) e^{j\omega_1 t} \\ &\Rightarrow \mathbf{I}_{ref,dq} = \mathbf{I}_{ref1,dq} + \Delta\mathbf{I}_{dq} + \Delta\mathbf{I}_{PLL,dq} \\ \Delta\mathbf{I}_{PLL,dq} &= j\Delta\theta\mathbf{I}_{ref1,dq} = j\Delta\theta\mathbf{I}_{1,dq} \end{aligned} \quad (42)$$

TABLE II
CURRENT CONTROLLER AND PLL PARAMETERS

Symbol	Controller Parameter	Value
k_p	Proportional gain of current controller	16 Ω
k_i	Integral gain of current controller	600 Ω/s
k_{p_PLL}	PLL-proportional gain ($f_{PLL} = 20$ Hz)	1.08 rad/s
	PLL-proportional gain ($f_{PLL} = 175$ Hz)	9.51 rad/s
	PLL-proportional gain ($f_{PLL} = 330$ Hz)	18.07 rad/s
k_{i_PLL}	PLL-integral gain ($f_{PLL} = 20$ Hz)	99.75 rad/s ²
	PLL-integral gain ($f_{PLL} = 175$ Hz)	7675 rad/s ²
	PLL-integral gain ($f_{PLL} = 330$ Hz)	27 708 rad/s ²

where $\mathbf{I}_{ref1,dq}$ is the current reference at the steady-state, which is equal to $\mathbf{I}_{1,dq}$. Thus, the same effect on the input of the current controller as the dq -frame current control is brought by the PLL. However, differing from Fig. 6, the PLL dynamics have no effect on the input of the modulator and the open-loop output admittance. This difference is because the center frequency of the R controller, i.e., the grid fundamental frequency ω_1 , is fixed in the $\alpha\beta$ -frame current control scheme shown in Fig. 2(a). In contrast, the I controller in the dq -frame leads to an equivalent R controller with the center frequency varied with the dynamics of the PLL [2], [8].

V. IMPEDANCE-BASED STABILITY ANALYSIS

The generalized Nyquist stability criterion is widely applied to the asymmetric impedance matrices for the system stability assessment [22], [23]. Similar to the dq -frame impedance model, the proposed $\alpha\beta$ -frame impedance model is asymmetric, which requires using the generalized Nyquist stability criterion for the converter-grid interaction analysis. Considering the grid impedance, the impedance ratio is given by

$$\mathbf{L}^m(s) = \mathbf{Z}_g^m(s) \mathbf{Y}_{tcl\pm}^m(s) \quad (43)$$

where $\mathbf{Z}_g^m(s)$ is the grid impedance matrix. The system stability can then be predicted based on the frequency responses of the eigenvalues of the impedance ratio, which are derived by

$$\det[\lambda \mathbf{I}^m - \mathbf{Z}_g^m(s) \mathbf{Y}_{tcl\pm}^m(s)] = 0. \quad (44)$$

Since the grid impedance is assumed to be balanced, the grid impedance matrix turns as a diagonal matrix for the complex space vectors. Corresponding to the dq -frame complex transfer matrix of the VSC control output admittance, which is given by (40), the grid impedance matrix can be expressed as

$$\begin{aligned} Z_g(s) &= \frac{L_g s + R_{Lg}}{(L_g s + R_{Lg})C_g s + 1} \\ \Rightarrow \mathbf{Z}_{g,dq}(s) &= \frac{L_g(s + j\omega_1) + R_{Lg}}{[L_g(s + j\omega_1) + R_{Lg}]C_g(s + j\omega_1) + 1} \end{aligned} \quad (45)$$

$$\begin{bmatrix} \mathbf{V}_{dq} \\ \mathbf{V}_{dq}^* \end{bmatrix} = \begin{bmatrix} \mathbf{V}_{g,dq} \\ \mathbf{V}_{g,dq}^* \end{bmatrix} - \underbrace{\begin{bmatrix} \mathbf{Z}_{g,dq}(s) & 0 \\ 0 & \mathbf{Z}_{g,dq}^*(s) \end{bmatrix}}_{\mathbf{Z}_{g,dq}^m(s)} \begin{bmatrix} \mathbf{I}_{dq} \\ \mathbf{I}_{dq}^* \end{bmatrix} \quad (46)$$

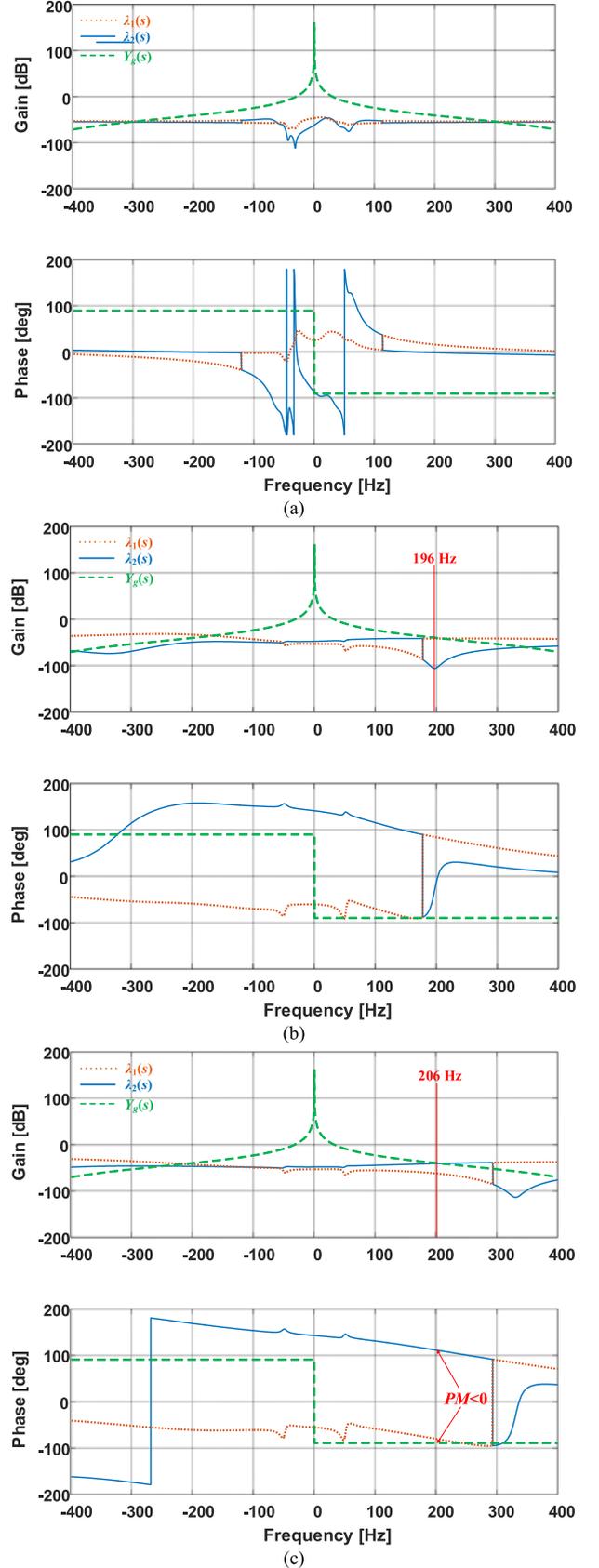


Fig. 8. Frequency responses for the eigenvalues of $\mathbf{Y}(s)$ and the grid impedance when using the dq -frame current control. (a) $f_{PLL} = 20$ Hz. (b) $f_{PLL} = 175$ Hz. (c) $f_{PLL} = 330$ Hz.

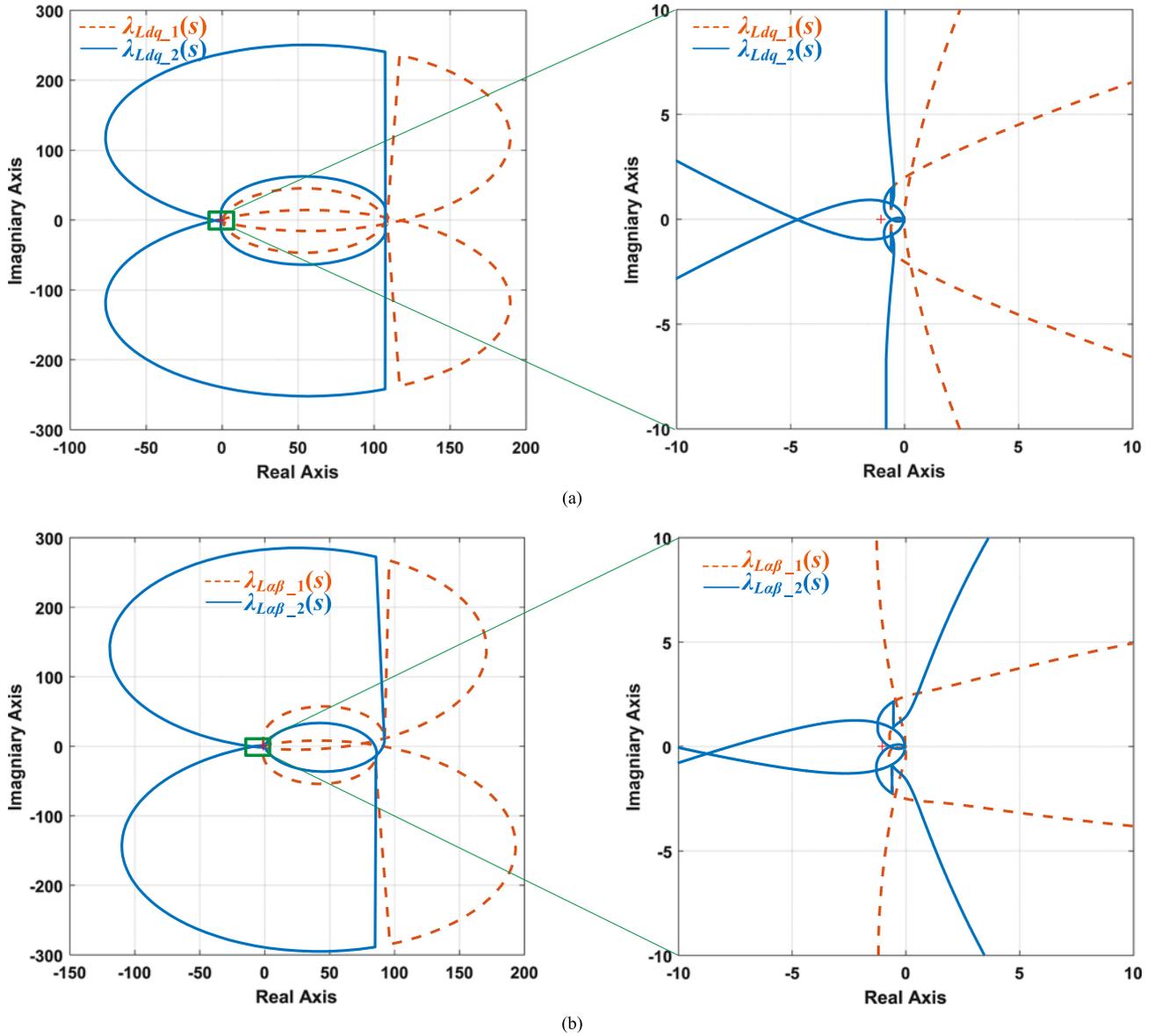


Fig. 9. Nyquist diagrams for the eigenvalues of the impedance ratios derived in different domains. (a) dq -frame impedance model. (b) $\alpha\beta$ -frame impedance model.

where R_{L_g} is the equivalent series resistance of grid inductance L_g . The complex conjugate of $\mathbf{Z}_{g,dq}(s)$ is given by

$$\mathbf{Z}_{g,dq}^*(s) = \frac{L_g(s - j\omega_1) + R_{L_g}}{[L_g(s - j\omega_1) + R_{L_g}]C_g(s - j\omega_1) + 1} \quad (47)$$

which can then be transformed into the $\alpha\beta$ -frame following the frequency translation given in (32) and (33), and it is given by

$$\begin{aligned} \mathbf{Z}_{g,dq}^m(s) \rightarrow \mathbf{Z}_{g,\alpha\beta}^m(s) &= \begin{bmatrix} \mathbf{Z}_{g,dq}(s - j\omega_1) & 0 \\ 0 & \mathbf{Z}_{g,dq}^*(s - j\omega_1) \end{bmatrix} \\ &= \begin{bmatrix} Z_g(s) & 0 \\ 0 & Z_g(s - j2\omega_1) \end{bmatrix}. \end{aligned} \quad (48)$$

With the diagonal grid impedance matrix, the eigenvalues of the impedance ratio can be approximated as

$$\det[\lambda I^m - \mathbf{Z}_{g,\alpha\beta}^m(s) \mathbf{Y}_{\text{tcl}\pm}^m(s)] \approx Z_g(s) \cdot \det[\lambda I^m - \mathbf{Y}_{\text{tcl}\pm}^m(s)]. \quad (49)$$

Instead of plotting the Nyquist diagrams for the eigenvalues of (44), the converter-grid interactions can be analyzed by plotting the frequency responses for the eigenvalues of $\mathbf{Y}_{\text{tcl}\pm}^m(s)$ against the grid admittance, i.e., $Y_g(s) = 1/Z_g(s)$. The system stability will be dependent on the phase difference at the frequencies where their magnitude responses intersect, where the phase difference out of 180° indicates an unstable system with a negative phase margin [18].

Table II provides the controller parameters used in this work. Three sets of SRF-PLL parameters with the different

control bandwidth f_{PLL} are evaluated. The control bandwidth of the SRF-PLL is determined by the settling time, T_{set} , of the second-order system given by (19), i.e., $f_{PLL} \approx 1/T_{set}$, where the damping ratio is chosen as 0.707, and the parameters of the PI controller can be found based on the settling time [14].

First, considering the cases with the dq -frame current control, the frequency responses for the eigenvalues of $\mathbf{Y}_{\text{tcl}\pm}^m(s)$ are plotted against the grid admittance. With the PLL bandwidth of 20 Hz, $f_{PLL} = 20$ Hz, the phase differences at the frequencies where $|\lambda_1(j\omega)| = |Y_g(j\omega)|$ or $|\lambda_2(j\omega)| = |Y_g(j\omega)|$ are within 180° , as shown in Fig. 8(a), which imply a stable response of the VSC system. However, as the PLL bandwidth increases, the phase differences are increased.

Fig. 8(b) plots the frequency responses for $f_{PLL} = 175$ Hz, where the phase difference at their magnitude intersection frequency 196 Hz is close to 180° . This implies that the system is marginally stable in response with the resonant or unexpected harmonic component nearby 196 Hz, and another frequency-coupled component nearby $2\omega_1 - \omega$, i.e., -96 Hz (negative-sequence), due to the frequency-coupling dynamics. When the PLL bandwidth is increased up to 330 Hz, the system becomes unstable with the phase difference out of 180° at the magnitude-interaction frequency, as shown in Fig. 8(c).

Fig. 9 then compares the Nyquist diagrams of the eigenvalues of the impedance ratio derived from the dq -frame impedance model and the $\alpha\beta$ -frame impedance model, where the case of the dq -frame current control with the PLL bandwidth of 330 Hz is considered. The same instability implication can be observed from the two Nyquist diagrams. Yet, differing from the dq -frame impedance model, the Nyquist diagrams of the $\alpha\beta$ -frame impedance model are asymmetric between the positive- and negative-frequencies. This difference is brought by the frequency translation from the dq -frame to the $\alpha\beta$ -frame. It is worth noting that the dq -frame complex transfer matrix, which is given in (40), leads to the same Nyquist diagrams as the dq -frame impedance matrix of real space vectors. This equivalence has been explicitly elaborated in [17].

Subsequently, Fig. 10 depicts the frequency responses for the cases with the $\alpha\beta$ -frame current control. Differing from the dq -frame current control, the VSC is kept stable with all three sets of PLL parameters. The differences are due to the use of a fixed-frequency R controller in the $\alpha\beta$ -frame, which, as illustrated in Fig. 7, equivalently avoids the PLL effect on the modulating voltage, and it is hence more robust against the dynamic impact of the PLL than the dq -frame current control.

However, similar to the dq -frame current control, the system phase margin is also reduced, i.e., the phase difference at the intersection frequencies of the magnitude responses is getting closer to 180° , as the bandwidth of the PLL increases. Fig. 10(c) shows that a marginally stable operation with the under-damped resonance may be resulted at the intersection frequency (270 Hz) of the magnitude responses, and the frequency-coupling harmonic at 170 Hz, yet in the negative-sequence, may arise.

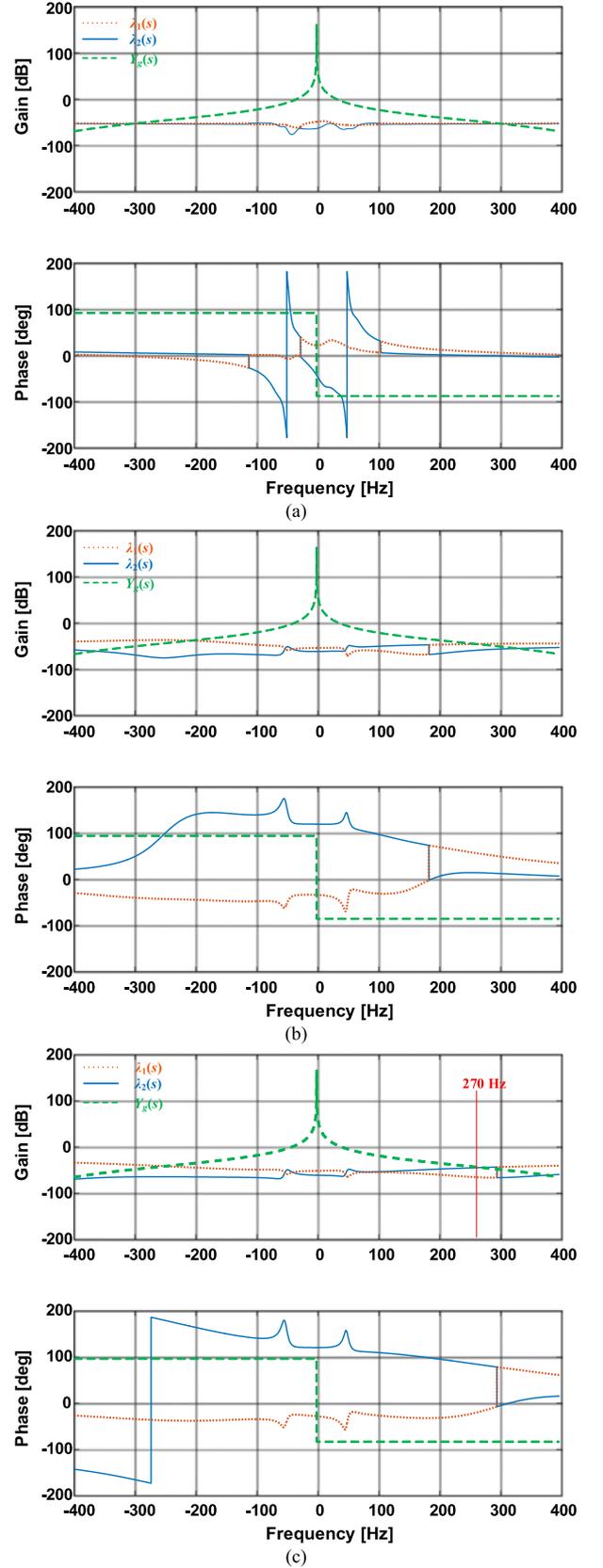


Fig. 10. Frequency responses for the eigenvalues of $\mathbf{Y}(s)$ and the grid impedance when using the $\alpha\beta$ -frame current control. (a) $f_{PLL} = 20$ Hz. (b) $f_{PLL} = 175$ Hz. (c) $f_{PLL} = 330$ Hz.

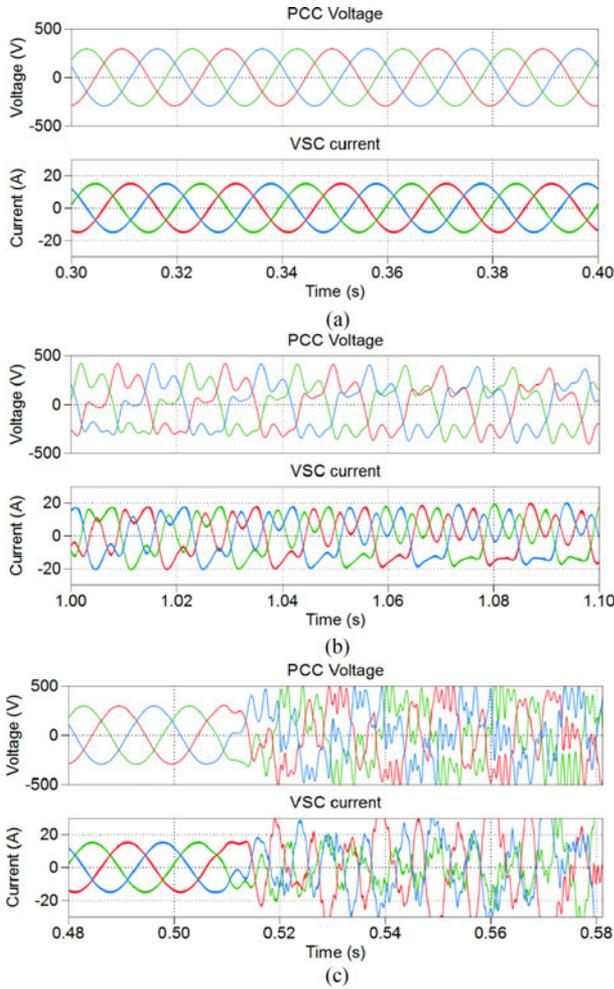


Fig. 11. Simulated PCC voltage and VSC current for the dq -frame current control. (a) $f_{PLL} = 20$ Hz. (b) $f_{PLL} = 175$ Hz. (c) $f_{PLL} = 330$ Hz.

VI. SIMULATIONS AND EXPERIMENTAL RESULTS

A. Simulation Results

To verify the effectiveness of the impedance-based analysis, the time-domain simulations based on the MATLAB/Simulink and PLECS blockset are performed. The parameters presented in Tables I and II are adopted.

Fig. 11 shows the simulated PCC voltage (line-to-line) and VSC current for the dq -frame current control. Three cases corresponding to the three different sets of PLL parameters are compared. A stable response is observed in Fig. 11(a) when $f_{PLL} = 20$ Hz, which agrees with the frequency-domain analysis in Fig. 8(a). Fig. 11(b) depicts the simulated waveforms for the case of $f_{PLL} = 175$ Hz, where the harmonic distortions confirm the stability implication of Fig. 8(b). Fig. 12 further illustrates the harmonic spectra analysis of Fig. 11(b), where the two main harmonic components correlate closely with the frequency identified in Fig. 8(b), and phase sequence obtained in the harmonic spectra verifies the frequency coupling effect, i.e., $2\omega_1 - \omega$. Fig. 11(c) shows the step change of the PLL bandwidth from 20 to 330 Hz at the time instant of 0.5 s. It is clear

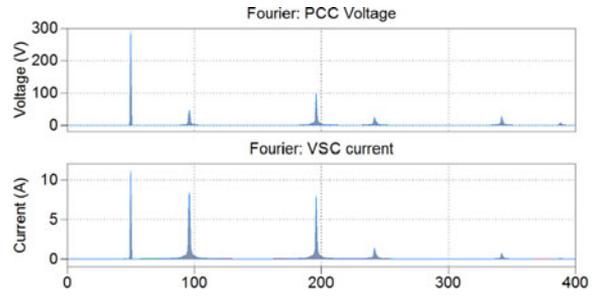


Fig. 12. Harmonic spectra analysis for the simulated waveforms in Fig. 11(b).

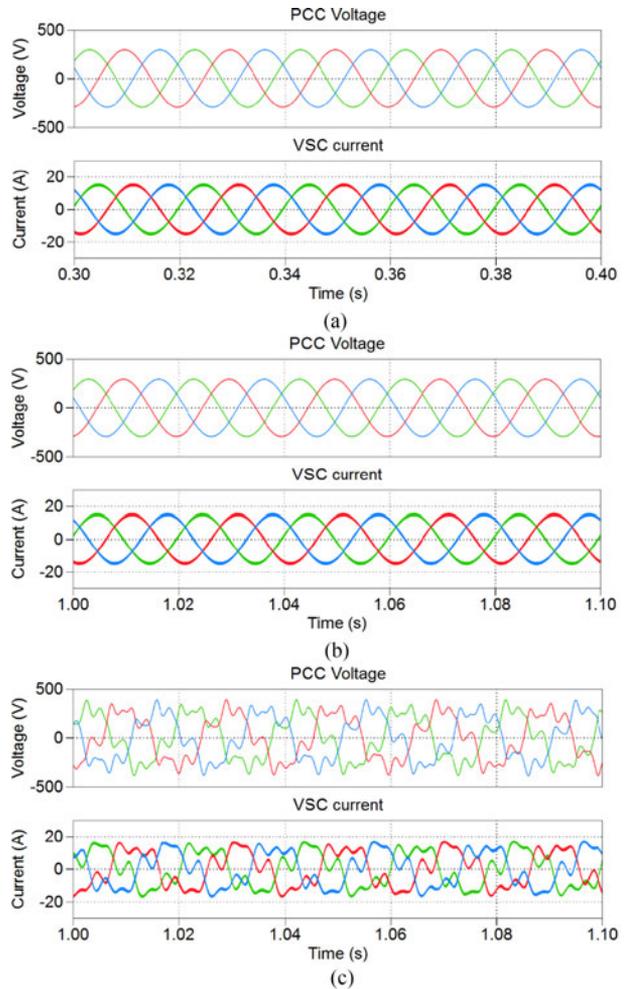


Fig. 13. Simulated PCC voltage and VSC current for the $\alpha\beta$ -frame current control. (a) $f_{PLL} = 20$ Hz. (b) $f_{PLL} = 175$ Hz. (c) $f_{PLL} = 330$ Hz.

that the system becomes unstable when the PLL bandwidth is increased.

Fig. 13 shows the simulated results for the $\alpha\beta$ -frame current control. It is clear that the system is kept stable in all cases, which confirms the stability analysis in Fig. 10. The harmonic distortion shown in Fig. 13(c) verifies the limited phase margin predicted in Fig. 10(c). The harmonic spectra of Fig. 13(c) are shown in Fig. 14, where the two harmonic components confirm the frequency identified in Fig. 10(c) and the coupled harmonic distortion.

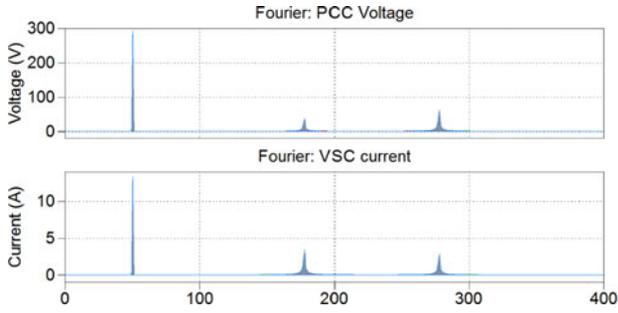
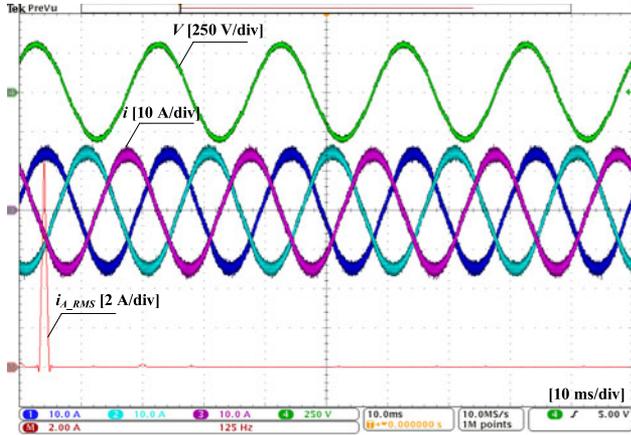
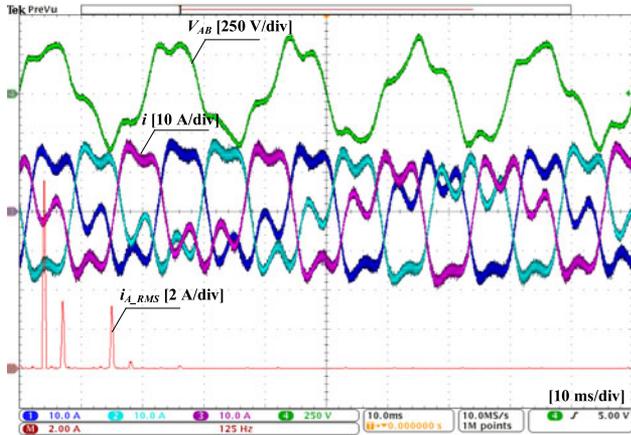


Fig. 14. Harmonic spectra analysis for the simulated waveforms in Fig. 13(c).



(a)

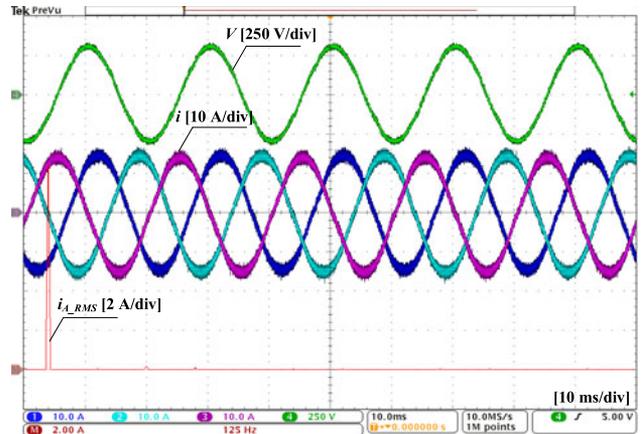


(b)

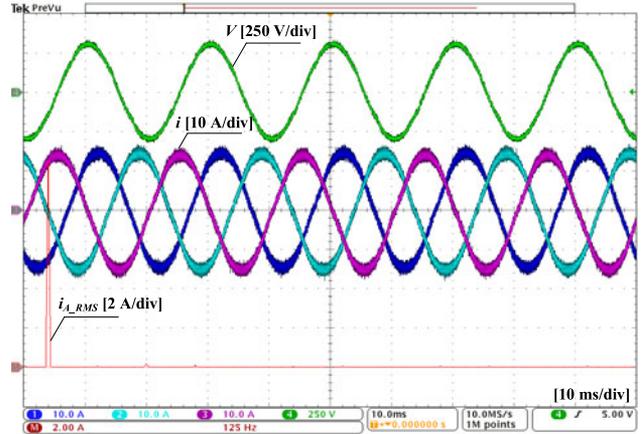
Fig. 15. Measured PCC voltage and VSC current for the dq -frame current control. (a) $f_{PLL} = 20$ Hz. (b) $f_{PLL} = 175$ Hz.

B. Experimental Results

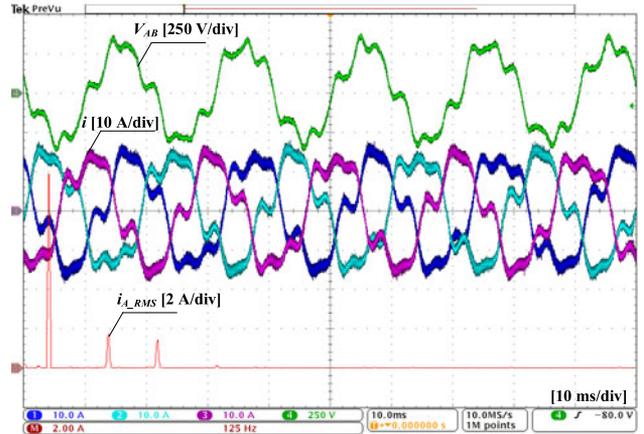
To further verify the simulation results, the experiments on a laboratory test setup are carried out. All the parameters used in the frequency-domain analysis and time-domain simulations are tested in experiments. A frequency converter is used as the grid-connected VSC. The control system is implemented in the DS1007 dSPACE system, where the DS5101 digital waveform output board is used for generating the switching pulses, and the DS2004 high-speed A/D board is used for the voltage and current measurements. A constant V dc voltage supply is used at



(a)



(b)



(c)

Fig. 16. Measured PCC voltage and VSC current for the $\alpha\beta$ -frame current control. (a) $f_{PLL} = 20$ Hz. (b) $f_{PLL} = 175$ Hz. (c) $f_{PLL} = 330$ Hz.

the dc-side, and Chroma grid simulator is used to generate the grid voltage.

Fig. 15 shows the measured line-to-line PCC voltage (V_{AB}) and VSC currents for the dq -frame current control. Two sets of PLL parameters, i.e., $f_{PLL} = 20$ Hz and 175 Hz, are tested, since the unstable case with $f_{PLL} = 330$ Hz tripped the test system. The harmonic spectra of the phase-A current are shown.

It is clear that the voltage and current are distorted when the PLL bandwidth is increased to $f_{PLL} = 175$ Hz, which closely correlate with the simulation results in Fig. 11(a) and (b).

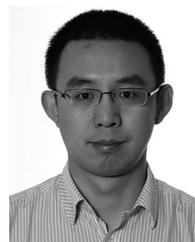
Fig. 16 shows the measured line-to-line PCC voltage (V_{AB}) and VSC currents for the $\alpha\beta$ -frame current control. Three sets of PLL parameters presented in Table II are tested for comparison. It is clear that the harmonic spectra of the phase-A current in Fig. 16(c) match with the simulation result in Fig. 14 and the frequency identified in Fig. 10(c).

VII. CONCLUSION

This paper has discussed a unifying approach to the impedance models of VSCs with the PLL dynamics included. The mathematical relations between the models built in the different domains have been explicitly revealed. A complex equivalence of the asymmetric dq -frame impedance matrix has been introduced based on complex space vectors and complex transfer functions. Based on the complex equivalence, a stationary-frame ($\alpha\beta$ -frame) impedance model has further been developed. Frequency-domain analysis on the influence of different PLL bandwidths has been performed with the developed impedance model, and validated in time domain simulations and experiments.

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