Neurons in our Brains vs ANN How different are they?

Subham Sahoo Vice-Leader, Reliability of Power Electronic Converters (ReliaPEC Group) Assistant Professor, AAU Energy Aalborg University, Denmark



A A L B O R G U N I V E R S I T Y

AI-POWER

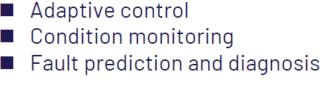


AALBORG University









Degradation prediction

01: **5** tailored AI tools

Design optimization



UCRC

xtel

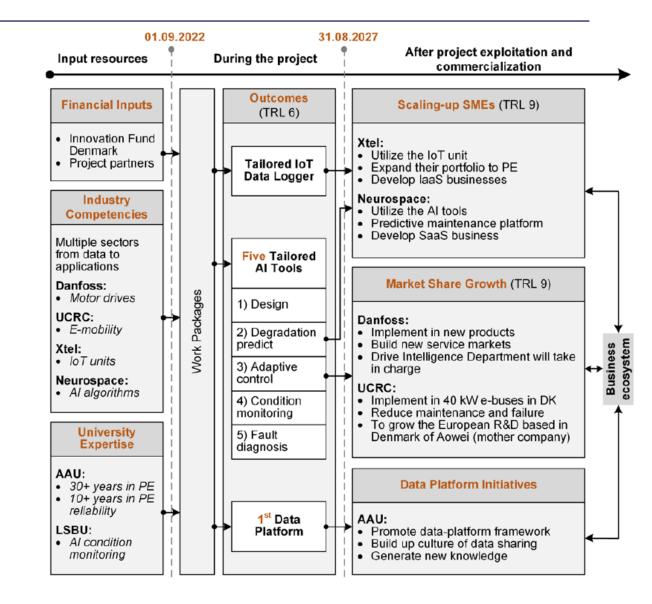
02: 1/3 Reduction

- Design optimization time
- Operating energy loss
- Maintenance cost

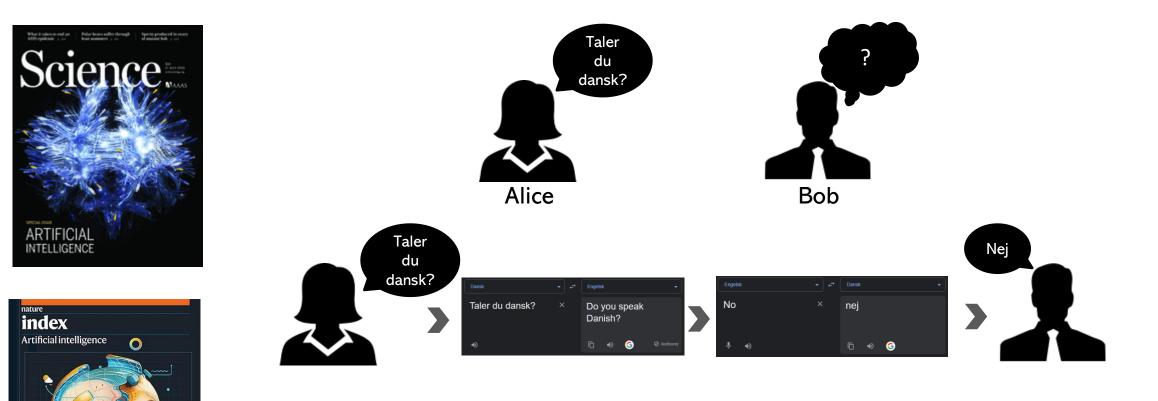
03: The world's **1**st power electronics data platform

Innovation Fund Denmark

More updates: https://www.ipower.ai/



Machine Learning Today

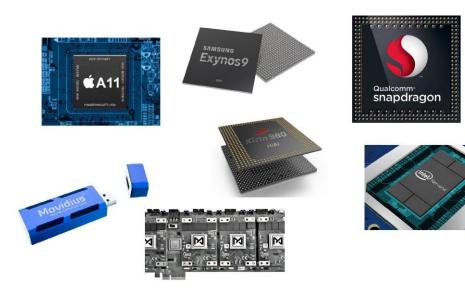


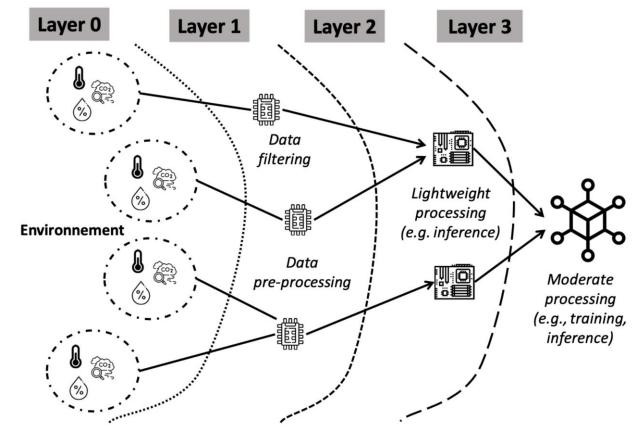
Breakthroughs in ML using (deep) Articial Neural Networks (ANNs) have come at the expense of massive memory, energy, and time requirements

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Machine Learning at Edge

- A solution is mobile edge or cloud computing: offline load computations to an edge or cloud server
- Another solution is to scale down energy and memory requirements of ANNs via tailored hardware implementations for mobile devices
 - Active field with established players and start-ups
 - Trade-offs between accuracy and complexity
 - Mostly limited to inference





The conventional inference cycle

Source: Lucas Wisnieweski, "Hardware Solutions for Low-Power Smart Edge Computing, "Journ. Low Power Electron. Appl., 2022

Beyond ANN

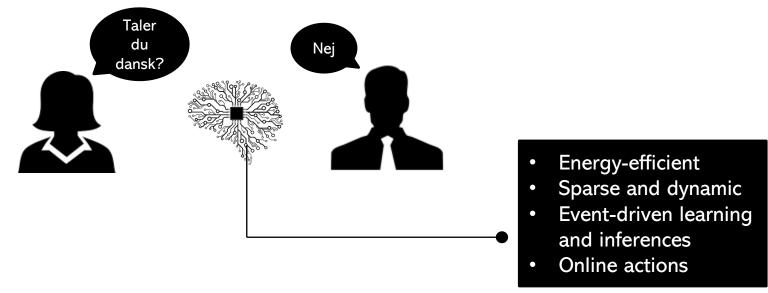


13 Million Watts 5600 sq. ft. & 340 tons 10¹⁰ ops/J 20 Watts 2 sq. ft. & 1.4 Kg 10¹⁵ ops/J

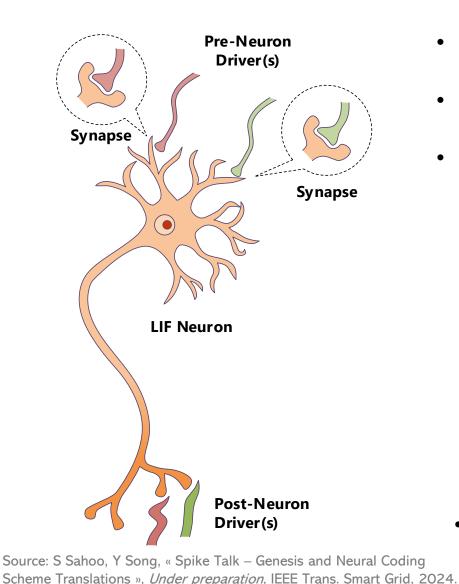


Source: https://www.olcf.ornl.gov, Google Images

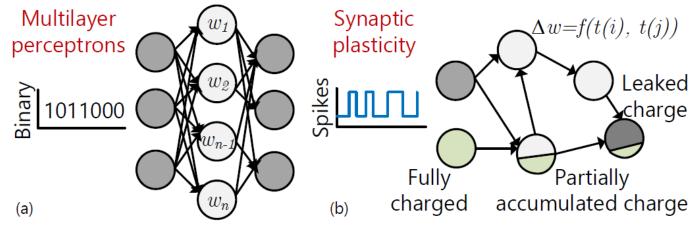
A sustainable fast-track process



► Actually..



- Neurons in the brain sense, process, and communicate over time using sparse binary signals (spikes or action potentials).
- This results in a dynamic, sparse, and event-driven learning and inference.
- Spiking signals minimize energy per bit.

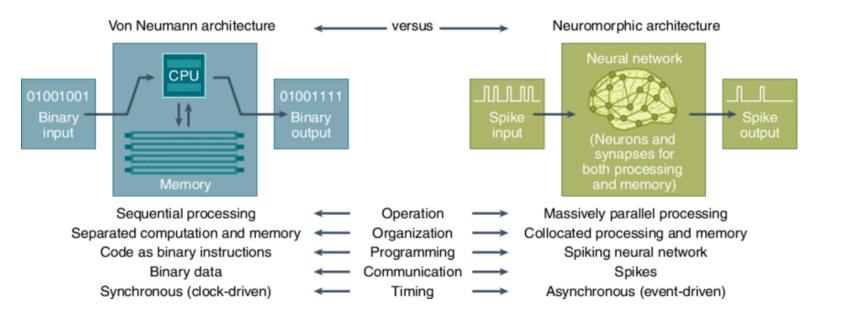


Spiking Neural Networks (SNNs)

• Biologically plausible neurons have a notion of charge and memory, that is reverse engineered to be implemented using memristors

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Processing Alternatives Today

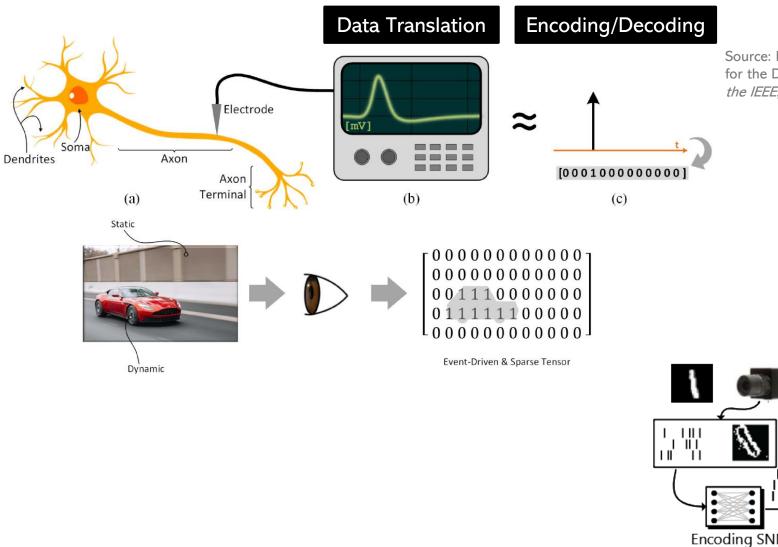


Proof-of-concept and commercial hardware implementations of SNNs have demonstrated significant energy savings as compared to ANNs

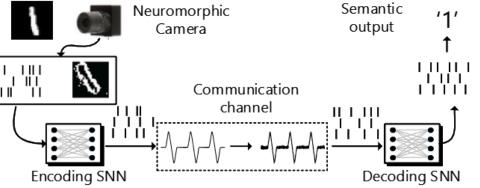




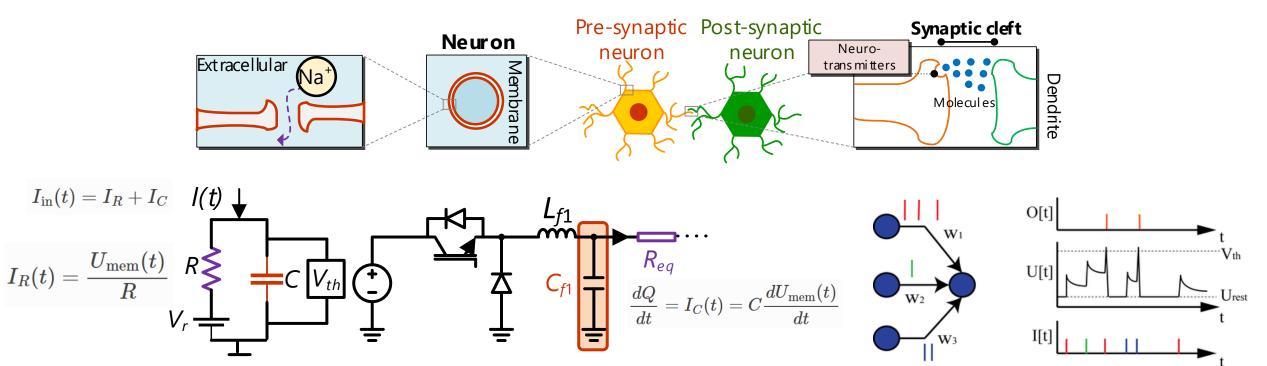
Simple Examples



Source: Frenkel et al.: Bottom-Up and Top-Down Approaches for the Design of Neuromorphic Processing Systems, *Proc. Of the IEEE*, 2023.



Biologically Plausible Neurons



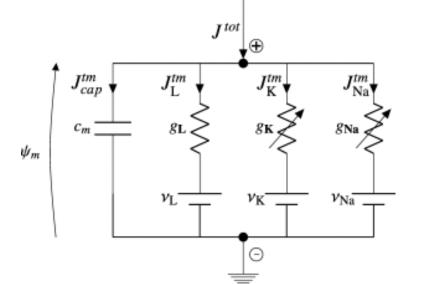
Leaky Integrate-and-Fire (LIF) Neuron

$$RCrac{dU_{
m mem}(t)}{dt} = -U_{
m mem}(t) + RI_{
m in}(t)$$

Source: S Sahoo, Y Song, « Spike Talk – Genesis and Neural Coding Scheme Translations », *Under preparation*, IEEE Trans. Smart Grid, 2024. SUBHAM SAHOO, **ReliaPEC** GROUP, AAU ENERGY, AALBORG UNIVERSITY

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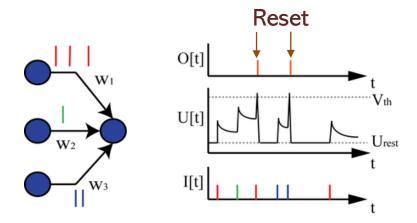
Complicated Neuron Models and Reset Mechanism



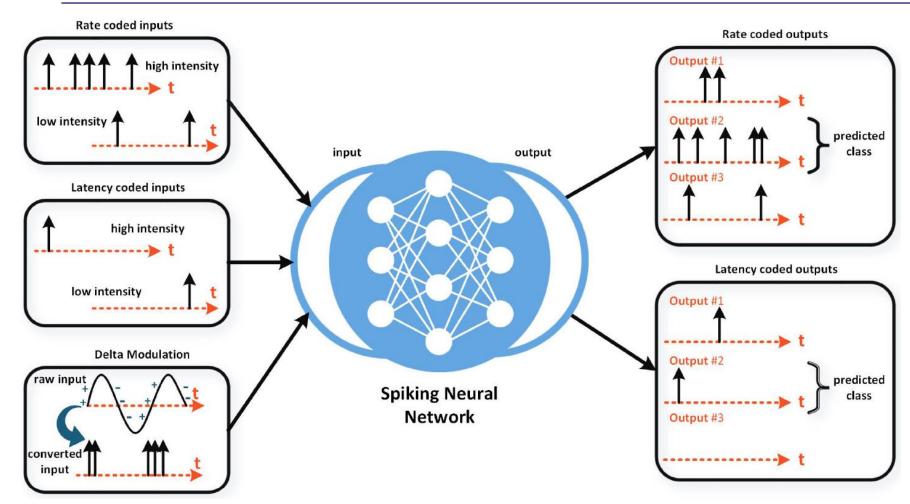
$$\sum_k I_k = g_{
m Na} \, m^3 h \, \left(u - E_{
m Na}
ight) + g_{
m K} \, n^4 \, \left(u - E_{
m K}
ight) + g_L \, \left(u - E_L
ight) \, ,$$

- Complicated (more accurate) model
- Poor computational efficiency deployment issues with most hardware accelerators

Source: Wulfram Gerstner, Werner M. Kistler, Richard Naud, « Neuronal Dynamics: From Single Neurons to Networks and Models of Cognition », 2014.



Neural Coding Schemes



How to translate real-valued information into spikes?

- Input data to an SNN may be converted into a firing rate, a firing time, or the data can be delta modulated.
- The network itself may be trained to enable the correct class to have the highest firing rate or to fire first, among many other encoding strategies.

→ t

Spike Timing Dependent Plasticity (STDP)

$$\Delta W = \begin{cases} A_{+}e^{(t_{pre} - t_{post})/\tau_{+}} & (t_{post} > t_{pre}) \\ -A_{-}e^{-(t_{pre} - t_{post})/\tau_{-}} & (t_{post} < t_{pre}) \end{cases}$$

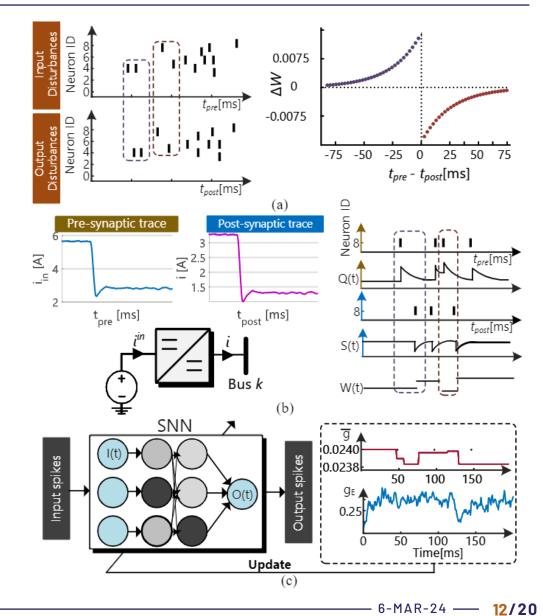
• The online weight update policy and its biological plausibility is explained by the Hebbian Principle: *"neurons that fire together wire together".*

 $\Delta W(t_{pre}) = S(t_{pre})W(t_{pre})$ $\Delta W(t_{post}) = Q(t_{post})W(t_{post})$

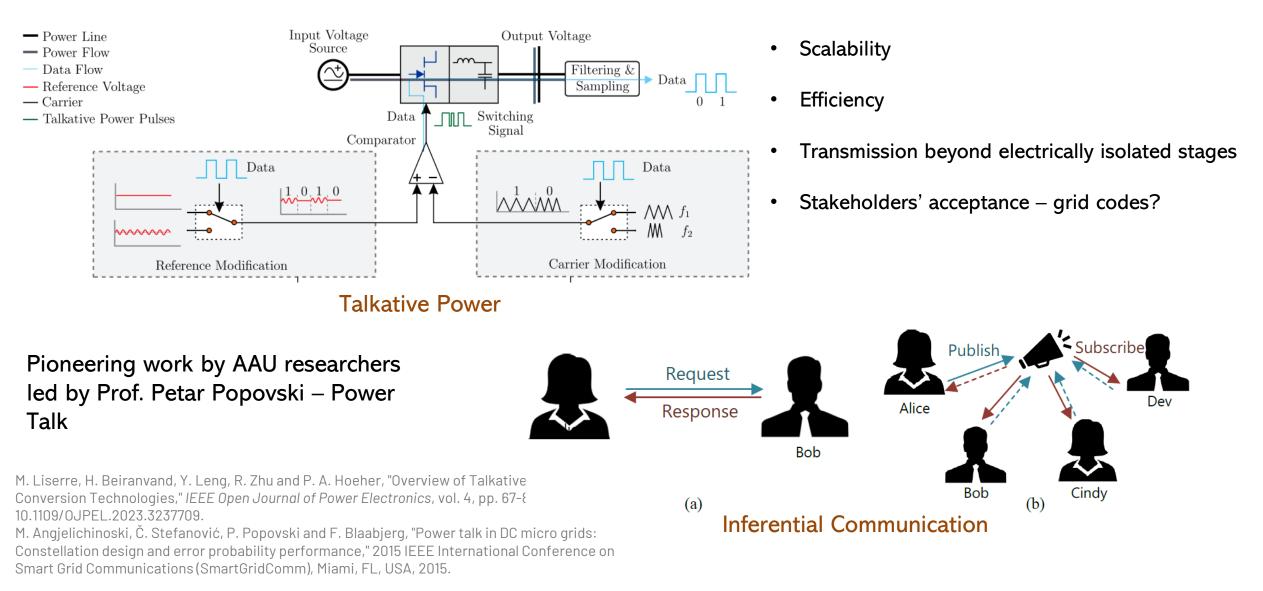
- If a pre-synaptic neuron fires just before a postsynaptic neuron, the connection between them is strengthened, often known as long-term potentiation (LTP).
- Otherwise, the connection is weakened, often known as long-term depression (LTD) of the same synapse.

Source: X Diao, Y Song, S Sahoo, Y Li, « Neuromorphic Event-Driven Semantic Communication in Microgrids» *IEEE Trans. Smart Grid*, 2024.

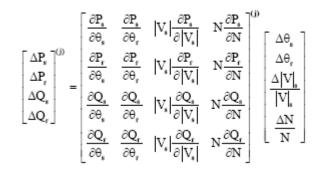
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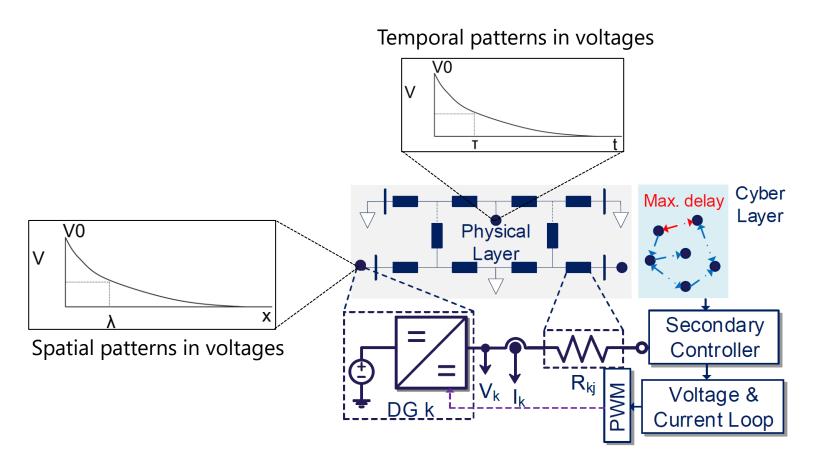
Information Embedded in Power

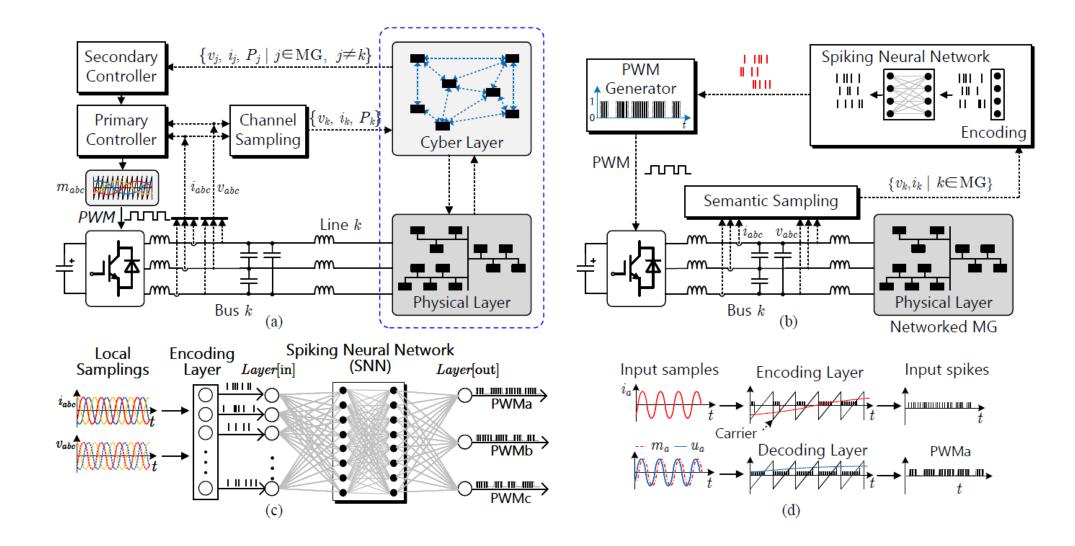


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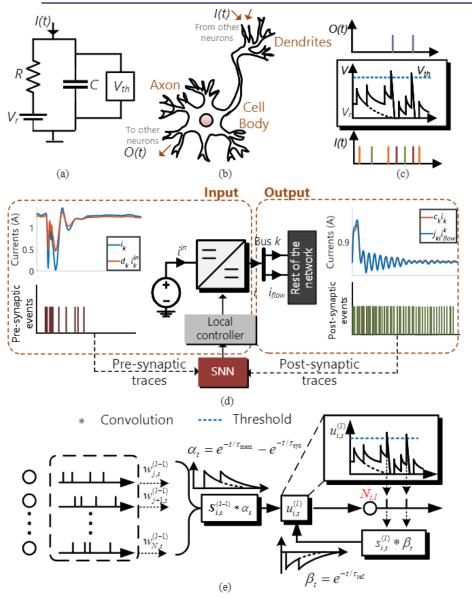


Spatio-temporal pattern exploration





Spiking Neuron as Energy Source

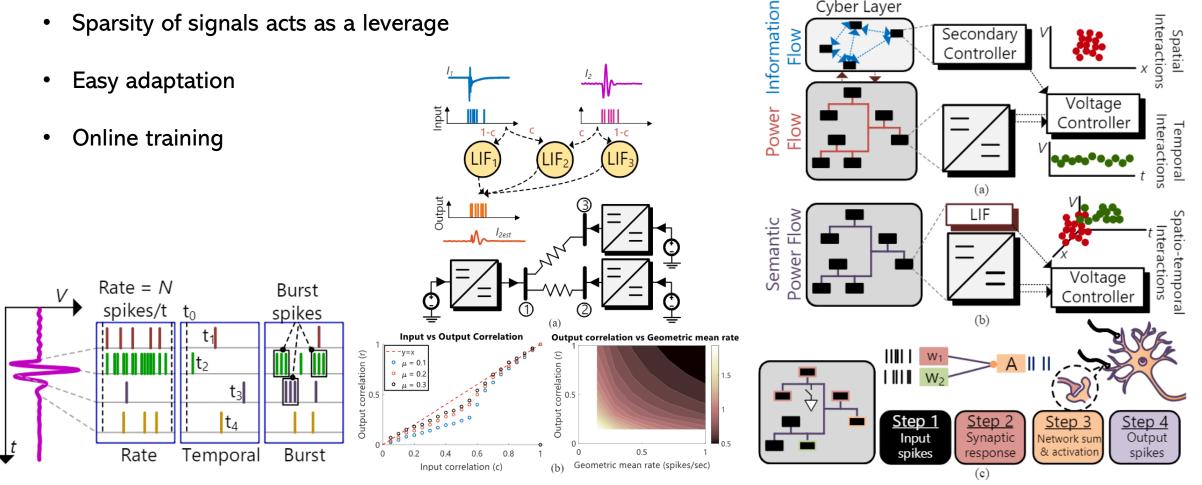


- We model each source as a LIF neuron
- It can respond to both input and output disturbances
- Only the remote sources will respond to a given disturbance based on the voltage fluctuations and its spatial decay
- STDP to change the conductance of the modeled neuron and change power generation
- Multi-agent networked control, adaptation, protection, flexibility is possible with minimal energy consumption per inference

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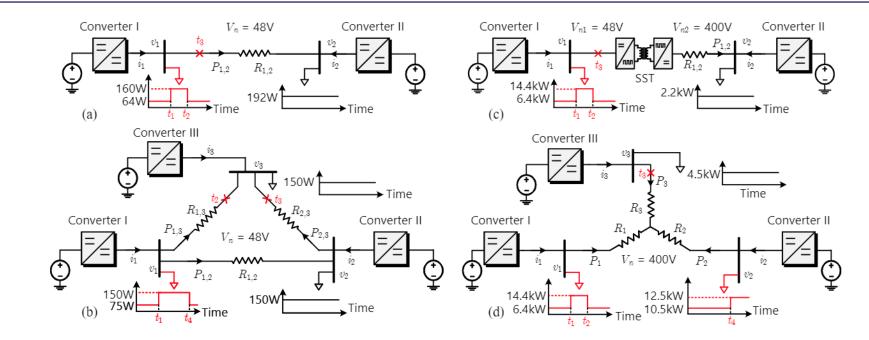
Spike Talk

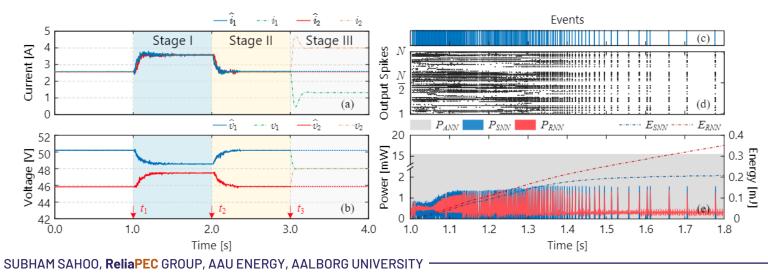
- No communication no exogeneous path arrival for ٠ attackers
- Sparsity of signals acts as a leverage •



Cyber Layer

Performance Evaluation





Source: X Diao, Y Song, S Sahoo, Y Li, « Neuromorphic Event-Driven Semantic Communication in Microgrids» IEEE Trans. Smart Grid, 2024.

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Closing Remarks

- Accuracy and Versatility Big Questions Going Forward
- Innovations in Neuromorphic Hardware
- Cost Big Limitation
- From pJ of computation concerns to kW of energy loss how will this neural implant be projected by energy stakeholders?

Open Course on Neuromorphic Computing in Power Electronics (certification to be provided by IEEE PELS)

- 8 video lectures computational neuroscience, coding schemes, power electronic integration
- Notes, exercises
- FAQs



Prof. Steven Low





Prof. Caroline Uhler





Yubo Song

Xiaoguang Diao



► Key References

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Contact Subham Sahoo Relia**PEC** Group <u>sssa@energy.aau.dk</u> +45 9940 7088

