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Frequency Scanning-Based Contributions Identification of Current Control Loop and PLL on DQ Impedance Characteristics of Three-Phase Grid-Connected Inverter

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Abstract—Complex space vector and complex transfer function theory is used to theoretically establish the dq impedance model of current-controlled grid-connected inverter (GCI) with considering phase-locked loop (PLL) dynamics, where contributions of current control loop and PLL on dq impedance characteristics are clearly identified. On its basis, this paper presents a frequency scanning-based black box identification method of the contributions of current control loop and PLL on dq-domain impedance characteristics of the GCI. Three-phase small-signal voltage vector perturbation which is aligned with point of common coupling (PCC) voltage vector is injected into PCC in series way, so that grid current responses which are only determined by the dynamics of current control loop are excited. In addition, parallel current injection to excite PCC voltage responses is theoretically proved to be not applicable for extracting the dynamics of current control loop and PLL. The effectiveness of the presented dq-domain impedance characteristics division method is verified by both frequency scanning results and time-domain simulation results obtained in Matlab/Simulink environment.

Index Terms—Current control loop, dq-domain impedance, frequency scanning, grid-connected inverter, impedance characteristics division, phase-locked loop.

I. INTRODUCTION

Voltage source grid-connected inverters (GCIs) have widely been utilized as the interface between the renewable energies, e.g., wind power and solar power, and utility grid [1]. However, the interactions between the control loops of the GCIs, e.g., current control loop and phase-locked loop (PLL), and time-varying grid impedance may lead to instability phenomena in wide frequency ranges [2]–[4]. Specifically, low-frequency instability phenomena can be induced under weak grid conditions, if PLL controller parameters are not properly tuned [5]. Recently, the impedance-based stability criteria (IBSC) has been proposed to assess the stability issues of three-phase AC power systems [6]. On its basis, the dq-domain IBSC is developed to deal with the impedance coupling effects

which are caused by PLL and outer power control loop [7], [8].

The dq-domain impedance characteristic of the GCI is required for the dq-domain IBSC, which can be theoretically derived if the internal circuit and controller parameters are known in advance [7]. However, the information is sometimes confidential, due to the industry secrecy and intellectual property protection [9], [10]. Recently, the frequency scanning techniques have been developed to deal with the black issue [11]–[23]. The dq-domain impedance model is obtained in [11] by injecting an unbalanced line-to-line current between two lines of the AC power system. In [12], an algorithm is constructed to allow for the measurement of the dq-domain impedance model via injection on multiple axes which are synchronized to the three phase network via a variable bandwidth PLL. Based on the identification of transfer function models from the response of the GCI under a set of passive load step tests, a dq-domain impedance model identification algorithm is proposed in [13]. In addition, the dq-domain impedance model is measured in [14], [15] by injecting three-phase small voltage perturbation in series way or injecting three-phase small current perturbation in parallel way, whereas the dq-domain impedance model of the three-phase AC system is measured in [16], [17] by injecting single-phase small voltage perturbation. In [18], a single-phase perturbation is injected into the inverter system during two scanning cycles, based on which the self-impedance and accompanying-impedance which are employed to characterize the frequency coupling effect of the GCI can be obtained in the sequence-domain. In [19], another three-phase inverter is paralleled with the GCI under test to generate the injected voltage or current signals. In addition, an adaptive frequency injection method is proposed in [20], where smaller frequency interval of injection signals is used in the region containing peaks and valleys and larger frequency interval is used in the region approximating to straight line. Therefore, the accuracy, reliability and efficiency of the traditional frequency scanning methods can

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be improved. However, these frequency scanning-based dq-domain impedance measurement methods presented in [11]–[20] require advanced and dedicated equipments, and have limited real-time applicability.

In [21], [22], orthogonal binary sequences which have wide bandwidths in nature are injected into the control loop of the GCI to simultaneously measure the d-axis and q-axis impedance components of the dq-domain impedance models of the GCI and the grid. In [23], the orthogonal binary sequences are further injected into a multiple-converter-based power system. Applying this method, all the impedances in the system can be simultaneously measured during one measurement cycle, so that the overall measurement time can be significantly reduced. However, these existing works can only reveal the overall effects of all control loops on the dq-domain impedance models. To the best of the authors' knowledge, no works have been reported on how to identify the contributions of different control loops on dq-domain impedance characteristics based on impedance measurement.

Complex space vectors and complex transfer functions-based modeling method of the GCI is originally proposed in [24], and has been extended to dq impedance modeling in [25], [26]. Specifically, current-controlled GCI and power-controlled GCI are investigated in [25] and [26], respectively. Complex space vectors and complex transfer function theory facilitates the observation of the effects of different control loops on dq-domain impedance feature. Based on the findings, this paper improves the conventional dq-domain impedance measurement techniques, and presents a frequency scanning-based black box identification method of contributions of current control loop and PLL on dq-domain impedance characteristics. The method presented in this paper is able to split the measured dq-domain impedance feature into two parts which are dominated by current control loop and PLL, respectively. In this case, the contributions of different control loops on system stability can be quantitatively assessed even if no detailed circuit and controller parameters are known.

II. EFFECTS OF CURRENT CONTROL LOOP AND PLL ON DQ-DOMAIN IMPEDANCE MODEL

In this section, the system configuration of the studied power system is first depicted. On its basis, the dq impedance models of a current-controlled three-phase LCL-filtered GCI without and with consideration of PLL are modeled using complex space vectors and complex transfer functions, respectively.

A. System Description

Fig. 1 shows the circuit and control diagram of a three-phase LCL-filtered voltage source GCI with current control loop and PLL. The LCL filter (L_{f1} , C_f and L_{f2}) is used to attenuate the high-frequency switching harmonics. The phase angle θ_g of voltage at point of common coupling (PCC) V_{PCC} is detected and synchronized with phase angle of injected current i_g of the GCI by the synchronous reference frame (SRF)-PLL, of which the detailed structure is shown in the red dotted box of Fig. 1. In addition, the grid-side current i_g is controlled in dq

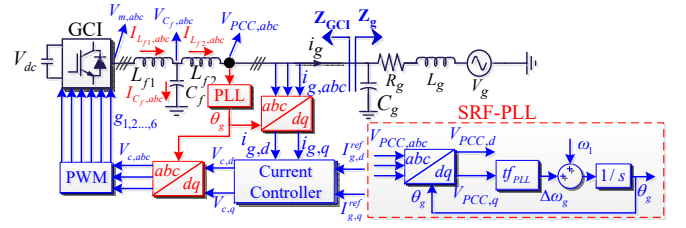


Fig. 1. Simplified one-line diagram of a three-phase LCL-filtered voltage source GCI with current control loop and PLL.

reference frame using proportional-integral (PI) controller. The grid impedance is emulated as an inductor L_g with parasitic resistor R_g which is paralleled with a capacitor C_g .

The dq impedance model of the GCI with consideration of current control loop and PLL has been established in [7], [25] using two-dimensional transfer matrices. In this paper, the dq impedance model will be established using complex space vectors and complex transfer functions. The italic letters are used to denote the real space vectors, e.g., $x_{dq} = [x_d, x_q]^T$ and $x_{\alpha\beta} = [x_\alpha, x_\beta]^T$. In addition, the bold letters are used to denote the complex space vectors, e.g., $\mathbf{x}_{dq} = x_d + jx_q$ and $\mathbf{x}_{\alpha\beta} = x_\alpha + jx_\beta$ for electrical parameters in dq-domain and $\alpha\beta$ -domain, respectively, and complex transfer functions, e.g., $\mathbf{Y}_{dq} = Y_d + jY_q$ and $\mathbf{Y}_{\alpha\beta} = Y_\alpha + jY_\beta$ for admittance of the GCI in dq-domain and $\alpha\beta$ -domain, respectively.

A linear continuous-time system with input signal and output signal as real space vectors $u_{dq} = [u_d, u_q]^T$ and $y_{dq} = [y_d, y_q]^T$ can be described as follows [24].

$$y_{dq} = \begin{bmatrix} G_{dd}(s) & G_{dq}(s) \\ G_{qd}(s) & G_{qq}(s) \end{bmatrix} u_{dq} = G^m(s) u_{dq} \quad (1)$$

where $G^m(s)$ is the transfer matrix between u_{dq} and y_{dq} . The superscript m indicates that the symbol is a matrix. For a symmetric three-phase AC system, i.e., $G_{dd}(s) = G_{qq}(s) = G_d(s)$ and $G_{qd}(s) = -G_{dq}(s) = G_q(s)$, (1) can be reformulated as (2) by representing $G(s)$ in the form of complex transfer function.

$$\mathbf{y}_{dq} = y_d + jy_q = \mathbf{G}(s) \mathbf{u}_{dq} = [G_d(s) + jG_q(s)](u_d + ju_q) \quad (2)$$

On the other hand, only one complex transfer function $\mathbf{G}(s)$ is not sufficient for modeling an unsymmetric system. Instead, two complex transfer functions should be involved, shown as follows [24].

$$\mathbf{y}_{dq} = \mathbf{G}_+(s) \mathbf{u}_{dq} + \mathbf{G}_-(s) \mathbf{u}_{dq}^* \quad (3)$$

where superscript $*$ denotes conjugation, and

$$\begin{aligned} \mathbf{G}_+(s) &= \frac{G_{dd}(s) + G_{qq}(s)}{2} + j \frac{G_{dq}(s) + G_{qd}(s)}{2} \\ \mathbf{G}_-(s) &= \frac{G_{dd}(s) - G_{qq}(s)}{2} + j \frac{G_{dq}(s) - G_{qd}(s)}{2} \end{aligned} \quad (4)$$

Based on (3) and (4), the following equation holds.

$$\begin{bmatrix} \mathbf{y}_{dq} \\ \mathbf{y}_{dq}^* \end{bmatrix} = \begin{bmatrix} \mathbf{G}_+(s) & \mathbf{G}_-(s) \\ \mathbf{G}_-^*(s) & \mathbf{G}_+^*(s) \end{bmatrix} \begin{bmatrix} \mathbf{u}_{dq} \\ \mathbf{u}_{dq}^* \end{bmatrix} \quad (5)$$

B. Effect of Current Control Loop on DQ Impedance Model

According to (1) and (2), the inductor and capacitor can be represented as complex space vectors based on their dq impedance models, shown as follows.

$$\begin{aligned} Z_{dq}^m(L) &= \begin{bmatrix} sL & -\omega_1 L \\ \omega_1 L & sL \end{bmatrix} \Leftrightarrow \mathbf{Z}_{dq}(\mathbf{L}) = (s + j\omega_1)L \\ Y_{dq}^m(L) &= \frac{1}{L(s^2 + \omega_1^2)} \begin{bmatrix} s & \omega_1 \\ -\omega_1 & s \end{bmatrix} \Leftrightarrow \mathbf{Y}_{dq}(\mathbf{L}) = \frac{1}{(s + j\omega_1)L} \\ Z_{dq}^m(C) &= \frac{1}{C(s^2 + \omega_1^2)} \begin{bmatrix} s & \omega_1 \\ -\omega_1 & s \end{bmatrix} \Leftrightarrow \mathbf{Z}_{dq}(\mathbf{C}) = \frac{1}{(s + j\omega_1)C} \\ Y_{dq}^m(C) &= \begin{bmatrix} sC & -\omega_1 C \\ \omega_1 C & sC \end{bmatrix} \Leftrightarrow \mathbf{Y}_{dq}(\mathbf{C}) = (s + j\omega_1)C \end{aligned}$$

where ω_1 is the fundamental angular frequency of the grid voltage V_g . The dynamic equations of the LCL filter can be expressed as follows.

$$\begin{aligned} \mathbf{V}_{m,dq} - \mathbf{V}_{C_f,dq} &= \mathbf{Z}_{dq}(\mathbf{L}_{f1})\mathbf{I}_{L_{f1},dq} \\ \mathbf{V}_{C_f,dq} &= \mathbf{Z}_{dq}(\mathbf{C}_f)\mathbf{I}_{C_f,dq} \\ \mathbf{V}_{C_f,dq} - \mathbf{V}_{PCC,dq} &= \mathbf{Z}_{dq}(\mathbf{L}_{f2})\mathbf{I}_{L_{f2},dq} \end{aligned} \quad (7)$$

where $\mathbf{V}_{m,dq} = V_{m,d} + jV_{m,q}$, $\mathbf{V}_{C_f,dq} = V_{C_f,d} + jV_{C_f,q}$ and $\mathbf{V}_{PCC,dq} = V_{PCC,d} + jV_{PCC,q}$ are complex space vector representations of $V_{m,abc}$, $V_{C_f,abc}$ and $V_{PCC,abc}$ in Fig. 1, respectively. $\mathbf{Z}_{dq}(\mathbf{L}_{f1})$, $\mathbf{Z}_{dq}(\mathbf{L}_{f2})$ and $\mathbf{Z}_{dq}(\mathbf{C}_f)$ are complex space vector representations of dq impedance models of L_{f1} , L_{f2} and C_f , respectively, which can be obtained by (6). In addition, $\mathbf{I}_{L_{f1},dq} = I_{L_{f1},d} + jI_{L_{f1},q}$, $\mathbf{I}_{C_f,dq} = I_{C_f,d} + jI_{C_f,q}$ and $\mathbf{I}_{L_{f2},dq} = \mathbf{I}_{g,dq} = I_{L_{f2},d} + jI_{L_{f2},q}$ are the complex space vector representations of $I_{L_{f1},abc}$, $I_{C_f,abc}$ and $I_{L_{f2},abc}$ in Fig. 1, respectively.

According to (7), \mathbf{Y}_{LCLl} and \mathbf{Y}_{LCLr} are defined as follows.

$$\begin{aligned} \mathbf{Y}_{LCLl} &= \frac{\mathbf{I}_{g,dq}}{\mathbf{V}_{m,dq}} \Big|_{\mathbf{V}_{PCC,dq}=0} \\ &= \frac{\mathbf{Z}_{dq}(\mathbf{C}_f)}{[\mathbf{Z}_{dq}(\mathbf{C}_f) // \mathbf{Z}_{dq}(\mathbf{L}_{f2}) + \mathbf{Z}_{dq}(\mathbf{L}_{f1})](\mathbf{Z}_{dq}(\mathbf{C}_f) + \mathbf{Z}_{dq}(\mathbf{L}_{f2}))} \\ \mathbf{Y}_{LCLr} &= \frac{\mathbf{I}_{g,dq}}{\mathbf{V}_{PCC,dq}} \Big|_{\mathbf{V}_{m,dq}=0} = \frac{1}{\mathbf{Z}_{dq}(\mathbf{C}_f) // \mathbf{Z}_{dq}(\mathbf{L}_{f1}) + \mathbf{Z}_{dq}(\mathbf{L}_{f2})} \end{aligned}$$

The control block diagram of the one-line diagram of the GCI in Fig. 1 without considering PLL dynamics can be established as Fig. 2, where the PLL-related control blocks are marked as red. The complex space vector representation of the terminal voltage of the GCI $\mathbf{V}_{m,dq}$ can be calculated based on the current control loop, shown as follows.

$$\mathbf{V}_{m,dq} = (\mathbf{I}_{g,dq}^{\text{ref}} - \mathbf{I}_{g,dq})\mathbf{G}_{c,dq}\mathbf{G}_{del} \quad (9)$$

where $\mathbf{I}_{g,dq}^{\text{ref}} = I_{g,d}^{\text{ref}} + jI_{g,q}^{\text{ref}}$ and $\mathbf{I}_{g,dq} = I_{g,d} + jI_{g,q}$ are complex space vector representations of current references and actual grid current in dq-domain, respectively. In addition, $\mathbf{G}_{c,dq} = K_{cp} + K_{ci}/s$ and $\mathbf{G}_{del} = e^{-1.5sT_s}$ are the complex space vector representations of current controller and time delay which consists of one sampling period for computation and a half sampling period for PWM modulation [7]. $\mathbf{G}_{c,dq}$

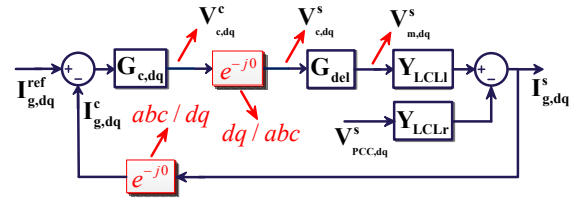


Fig. 2. Control block diagram of three-phase GCI in Fig. 1 using complex space vectors and complex transfer functions without considering PLL dynamics.

and \mathbf{G}_{del} can be calculated from the corresponding dq-domain models in (10) based on (1) and (2).

$$\begin{aligned} \mathbf{G}_{c,dq}^m &= \begin{bmatrix} G_{c,dq} & 0 \\ 0 & G_{c,dq} \end{bmatrix} = \begin{bmatrix} K_{cp} + \frac{K_{ci}}{s} & 0 \\ 0 & K_{cp} + \frac{K_{ci}}{s} \end{bmatrix} \\ \mathbf{G}_{del}^m &= \begin{bmatrix} G_{del} & 0 \\ 0 & G_{del} \end{bmatrix} = \begin{bmatrix} e^{-1.5sT_s} & 0 \\ 0 & e^{-1.5sT_s} \end{bmatrix} \end{aligned} \quad (10)$$

By combining (7) and (9), the closed-loop system response $\mathbf{I}_{g,dq}$ can be derived as follows.

$$\mathbf{I}_{g,dq} = \mathbf{G}_{cl,dq}\mathbf{I}_{g,dq}^{\text{ref}} + \mathbf{Y}_{cl,dq}\mathbf{V}_{PCC,dq} \quad (11)$$

where $\mathbf{G}_{cl,dq}$ and $\mathbf{Y}_{cl,dq}$ are the complex transfer function representations of the closed-loop gain and the closed-loop output admittance, respectively. The detailed expressions are shown as follows.

$$\mathbf{G}_{cl,dq} = \frac{\mathbf{T}_{dq}}{1 + \mathbf{T}_{dq}} \quad \mathbf{Y}_{cl,dq} = \frac{-\mathbf{Y}_{LCLr}}{1 + \mathbf{T}_{dq}} \quad (12)$$

where $\mathbf{T}_{dq} = \mathbf{G}_{c,dq}\mathbf{G}_{del}\mathbf{Y}_{LCLl}$ is the complex transfer function representation of the open loop gain in dq-domain.

According to the relationship between complex transfer function and transfer matrix shown in (1) and (2), $\mathbf{Y}_{cl,dq}$ can be represented in form of two-dimensional transfer matrix $Y_{cl,dq}^m$, shown as follows.

$$\mathbf{Y}_{cl,dq}^m = \begin{bmatrix} \frac{\mathbf{Y}_{LCLr}(s-j\omega_1)}{1 + \mathbf{G}_{c,dq}\mathbf{G}_{del}\mathbf{Y}_{LCLl}(s-j\omega_1)} & 0 \\ 0 & \frac{\mathbf{Y}_{LCLr}(s-j\omega_1)}{1 + \mathbf{G}_{c,dq}\mathbf{G}_{del}\mathbf{Y}_{LCLl}(s-j\omega_1)} \end{bmatrix} \quad (13)$$

It can be seen from (13) that the d -axis and q -axis admittances are the same, and no coupling exists between them.

C. Effect of PLL on DQ Impedance Model

Due to the limited response speed of the PLL, the detected phase angle of the PCC voltage is not the same as the real phase angle when the steady-state PCC voltage is perturbed. To linearize the SRF-PLL in Fig. 1, a small perturbation is added to the PCC voltage, shown as

$$\begin{aligned} \mathbf{V}_{PCC,dq}^c &= \mathbf{V}_{PCC,dq}^s \rightarrow \\ \mathbf{V}_{PCC,dq}^c + \Delta\mathbf{V}_{PCC,dq}^c &= e^{-j\Delta\theta_g}(\mathbf{V}_{PCC,dq}^s + \Delta\mathbf{V}_{PCC,dq}^s) \end{aligned} \quad (14)$$

where the superscripts c and s indicate that the symbols are represented in controller reference frame and system reference frame, respectively [7]. $\mathbf{V}_{PCC,dq}^c$ and $\mathbf{V}_{PCC,dq}^s = V_{PCC,1d}$ are complex space vector representations of steady-state PCC

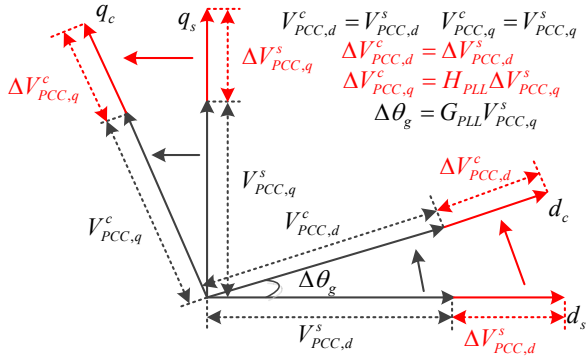


Fig. 3. Vector diagrams of PCC voltage in system and controller reference frames.

voltage in dq-domain in controller and system reference frames, respectively. In addition, Δ denotes small-signal perturbation.

Since $e^{-j\Delta\theta_g} \approx 1 - j\Delta\theta_g$ under small perturbation condition. The following small-signal relationship can be derived.

$$\begin{aligned} \Delta \mathbf{V}_{PCC,dq}^c &= \Delta \mathbf{V}_{PCC,dq}^s \underbrace{-j\Delta\theta_g \mathbf{V}_{PCC,1d}}_{\text{PLL dynamics}} \rightarrow \\ \Delta V_{PCC,d}^c &= \Delta V_{PCC,d}^s \\ \Delta V_{PCC,q}^c &= \Delta V_{PCC,q}^s \underbrace{-\Delta\theta_g V_{PCC,1d}}_{\text{PLL dynamics}} \end{aligned} \quad (15)$$

It can be seen from (15) that the q-axis dynamic is influenced by PLL. By substituting $\Delta\theta_g = \Delta V_{PCC,q}^s t f_{PLL}/s$ into (15), (15) can be reformulated as follows.

$$\begin{aligned} \Delta V_{PCC,d}^c &= \Delta V_{PCC,d}^s \\ \Delta V_{PCC,q}^c &= \frac{s}{s + t f_{PLL} V_{PCC,1d}} \Delta V_{PCC,q}^s = H_{PLL} \Delta V_{PCC,q}^s \\ \Delta\theta_g &= \frac{t f_{PLL}}{t f_{PLL} V_{PCC,1d} + s} \Delta V_{PCC,q}^s = G_{PLL} \Delta V_{PCC,q}^s \end{aligned} \quad (16)$$

Based on (16), the vector diagrams of PCC voltage in system and controller reference frames under a small perturbation are shown in Fig. 3.

Similar with the linearization process of the abc/dq transformation of PLL in (14), the other abc/dq and dq/abc transformations in Fig. 2(a) can also be linearized, shown as follows.

$$\begin{aligned} \mathbf{I}_{g,dq}^c &= \mathbf{I}_{g,dq}^s \rightarrow \\ \mathbf{I}_{g,dq}^c + \Delta \mathbf{I}_{g,dq}^c &= e^{-j\Delta\theta_g} (\mathbf{I}_{g,dq}^s + \Delta \mathbf{I}_{g,dq}^s) \\ \Delta \mathbf{I}_{g,dq}^c &= \Delta \mathbf{I}_{g,dq}^s \underbrace{-j\Delta\theta_g \mathbf{I}_{g,dq}^s}_{\text{PLL dynamics}} \end{aligned} \quad (17)$$

$$\begin{aligned} \mathbf{V}_{c,dq}^s &= \mathbf{V}_{c,dq}^c \rightarrow \\ \mathbf{V}_{c,dq}^s + \Delta \mathbf{V}_{c,dq}^s &= e^{j\Delta\theta_g} (\mathbf{V}_{c,dq}^c + \Delta \mathbf{V}_{c,dq}^c) \\ \Delta \mathbf{V}_{c,dq}^s &= \Delta \mathbf{V}_{c,dq}^c \underbrace{+j\Delta\theta_g \mathbf{V}_{c,dq}^c}_{\text{PLL dynamics}} \end{aligned} \quad (18)$$

By substituting (16) into (17) and (18), the effects of PLL dynamics on abc/dq and dq/abc transformations can be derived as follows.

$$\begin{aligned} \Delta \mathbf{I}_{g,dq}^{c,s} &= \Delta \mathbf{I}_{g,dq}^c - \Delta \mathbf{I}_{g,dq}^s = -jG_{PLL} \Delta V_{PCC,q}^s \mathbf{I}_{g,dq}^s \\ \Delta \mathbf{V}_{c,dq}^{s,c} &= \Delta \mathbf{V}_{c,dq}^s - \Delta \mathbf{V}_{c,dq}^c = jG_{PLL} \Delta V_{PCC,q}^s \mathbf{V}_{c,dq}^c \end{aligned} \quad (19)$$

To link $\Delta \mathbf{I}_{g,dq}^{c,s}$ and $\Delta \mathbf{V}_{c,dq}^{s,c}$ with $\Delta \mathbf{V}_{PCC,dq}^s$, $\Delta V_{PCC,q}^s$ should be represented as follows.

$$\Delta V_{PCC,q}^s = \frac{\Delta \mathbf{V}_{PCC,dq}^s - \Delta \mathbf{V}_{PCC,dq}^{s*}}{2j} \quad (20)$$

By substituting (20) into (19), (19) can be reformulated as follows.

$$\begin{aligned} \Delta \mathbf{I}_{g,dq}^{c,s} &= -\frac{G_{PLL} \mathbf{I}_{g,dq}^s}{2} (\Delta \mathbf{V}_{PCC,dq}^s - \Delta \mathbf{V}_{PCC,dq}^{s*}) \\ \Delta \mathbf{V}_{c,dq}^{s,c} &= \frac{G_{PLL} \mathbf{V}_{c,dq}^c}{2} (\Delta \mathbf{V}_{PCC,dq}^s - \Delta \mathbf{V}_{PCC,dq}^{s*}) \end{aligned} \quad (21)$$

Based on (21), control block diagram of the GCI with consideration of PLL dynamics using complex space vectors and complex transfer functions can be established, as shown in Fig. 4. Similar with (11), the closed-loop system response $\mathbf{I}_{g,dq}$ can be derived as follows.

$$\mathbf{I}_{g,dq}^s = \mathbf{G}_{cl,dq}^{PLL} \mathbf{I}_{g,dq}^{ref} + \mathbf{Y}_{cl,dq1}^{PLL} \mathbf{V}_{PCC,dq}^s + \mathbf{Y}_{cl,dq2}^{PLL} \mathbf{V}_{PCC,dq}^{s*} \quad (22)$$

where $\mathbf{G}_{cl,dq}^{PLL}$ and $\mathbf{Y}_{cl,dq}^{PLL}$ are the complex transfer function representations of the closed-loop gain and the closed-loop output admittance with consideration of PLL dynamics, respectively. The detailed expressions are shown as follows.

$$\begin{aligned} \mathbf{G}_{cl,dq}^{PLL} &= \frac{\mathbf{T}_{dq}}{1 + \mathbf{T}_{dq}} = \mathbf{G}_{cl,dq} \\ \mathbf{Y}_{cl,dq1}^{PLL} &= \frac{-\mathbf{Y}_{LCLr} + (G_{PLL} \mathbf{V}_{c,dq}^c / 2 - G_{PLL} \mathbf{I}_{g,dq}^s G_{c,dq} / 2) G_{del} \mathbf{Y}_{LCLl}}{1 + \mathbf{T}_{dq}} \\ &= \mathbf{Y}_{cl,dq} - \underbrace{\left(\frac{G_{PLL} \mathbf{I}_{g,dq}^s G_{cl,dq}}{2} - \frac{G_{PLL} \mathbf{V}_{c,dq}^c G_{del} \mathbf{Y}_{LCLl}}{2(1 + \mathbf{T}_{dq})} \right)}_{\text{PLL dynamics}} \\ \mathbf{Y}_{cl,dq2}^{PLL} &= \frac{-(G_{PLL} \mathbf{V}_{c,dq}^c / 2 - G_{PLL} \mathbf{I}_{g,dq}^s G_{c,dq} / 2) G_{del} \mathbf{Y}_{LCLl}}{1 + \mathbf{T}_{dq}} \\ &= \underbrace{\frac{G_{PLL} \mathbf{I}_{g,dq}^s G_{cl,dq}}{2} - \frac{G_{PLL} \mathbf{V}_{c,dq}^c G_{del} \mathbf{Y}_{LCLl}}{2(1 + \mathbf{T}_{dq})}}_{\text{PLL dynamics}} \end{aligned} \quad (23)$$

It can be seen that $\mathbf{Y}_{cl,dq1}^{PLL}$ is affected by dynamics of both current control loop and PLL, whereas $\mathbf{Y}_{cl,dq2}^{PLL}$ is only affected by PLL dynamics. This finding paves the way of the proposed frequency scanning-based contributions identification method of current control loop and PLL on dq impedance characteristics.

According to (5), (22) can be reformulated as follows.

$$\begin{aligned} \begin{bmatrix} \mathbf{I}_{g,dq}^s \\ \mathbf{I}_{g,dq}^{s*} \end{bmatrix} &= \begin{bmatrix} \mathbf{G}_{cl,dq}^{PLL} & 0 \\ 0 & \mathbf{G}_{cl,dq}^{PLL*} \end{bmatrix} \begin{bmatrix} \mathbf{I}_{g,dq}^{ref} \\ \mathbf{I}_{g,dq}^{ref*} \end{bmatrix} \dots \\ &+ \begin{bmatrix} \mathbf{Y}_{cl,dq1}^{PLL} & \mathbf{Y}_{cl,dq2}^{PLL} \\ \mathbf{Y}_{cl,dq2}^{PLL*} & \mathbf{Y}_{cl,dq1}^{PLL*} \end{bmatrix} \begin{bmatrix} \mathbf{V}_{PCC,dq}^s \\ \mathbf{V}_{PCC,dq}^{s*} \end{bmatrix} \end{aligned} \quad (24)$$

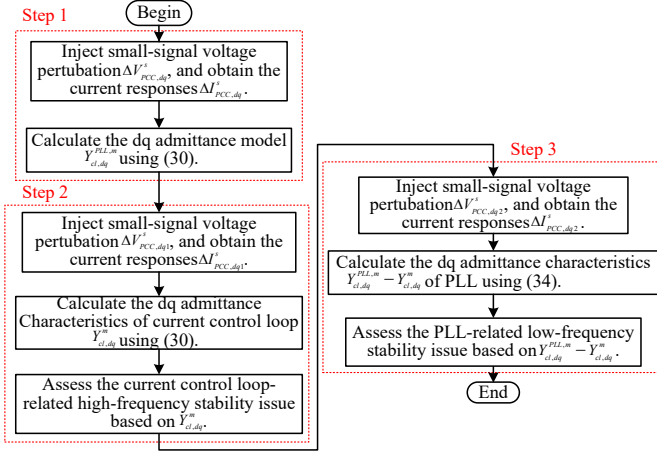


Fig. 6. Flowchart of the proposed frequency scanning-based contributions identification method of current control loop and PLL on dq impedance characteristics.

The impedance characteristics of the PLL can be obtained from (33), shown as

$$\begin{aligned} \mathbf{Y}_{cl,dq2}^{PLL} - \mathbf{Y}_{cl,dq2}^{PLL*} &= j \left(\frac{\Delta I_{g,q1}^s}{\Delta V_{PCC,d1}^s} + \frac{\Delta I_{g,d2}^s}{\Delta V_{PCC,q2}^s} \right) \\ \mathbf{Y}_{cl,dq2}^{PLL} + \mathbf{Y}_{cl,dq2}^{PLL*} &= \frac{\Delta I_{g,d1}^s}{\Delta V_{PCC,d1}^s} - \frac{\Delta I_{g,q2}^s}{\Delta V_{PCC,q2}^s} \end{aligned} \quad (34)$$

Note that if small-signal current perturbation is injected into the GCI system in parallel way, the PCC voltage responses will be affected by dynamics of both current control loop and PLL no matter what $\Delta V_{PCC,d}^s$ and $\Delta V_{PCC,q}^s$ are, i.e., the impedance characteristics of the two control loops cannot be partitioned.

The flowchart of the proposed frequency scanning-based contributions identification method of current control loop and PLL on dq impedance model is shown in Fig. 6. In step 1, small-signal voltage perturbation $\Delta V_{PCC,dq}^s$ is injected into PCC in series way, and grid current responses $\Delta I_{PCC,dq}^s$ is obtained. On its basis, the dq admittance model is calculated using (30). In step 2, small-signal voltage perturbation $\Delta V_{PCC,dq}^s = [\Delta V_{PCC,d1}^s, 0]$ is injected into PCC in series way, and grid current responses $\Delta I_{PCC,dq1}^s$ is obtained, based on which the impedance characteristic of current control loop $\mathbf{Y}_{cl,dq}^m$ is calculated using (32). The calculated $\mathbf{Y}_{cl,dq}^m$ can then be used to assess the high-frequency stability issue. In step 3, small-signal voltage perturbation $\Delta V_{PCC,dq}^s = [0, \Delta V_{PCC,q2}^s]$ is injected into PCC in series way, and grid current responses $\Delta I_{PCC,dq2}^s$ is obtained, based on which the impedance characteristic of PLL is calculated using (34). The calculated dq impedance characteristics of PLL can then be used to assess the low-frequency stability issue.

IV. SIMULATION VERIFICATION

In this section, the effectiveness of the proposed dq-domain impedance characteristics division method is verified by the frequency scanning results and time-domain simulation results in Matlab/Simulink.

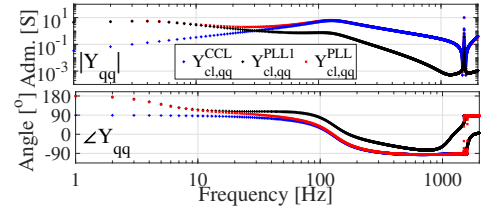


Fig. 7. Measured Bode diagrams of the q-axis admittance components in step 1, step 2 and step 3 of Fig. 6.

The circuit and controller parameters of the GCI in Fig. 1 are shown in Table I. Since the q-axis impedance component of the dq impedance model plays an important role in stability assessment, the following frequency scanning results will focus on the q-axis impedance characteristics.

TABLE I
CIRCUIT AND CONTROLLER PARAMETERS OF THE GCI

Parameter	Value
DC-link voltage V_{dc}	1150 V
Grid fundamental frequency f_1	50 Hz
Inverter side filter inductance L_{f1}	263 μ H
Grid side filter inductance L_{f2}	200 μ H
Filter capacitance C_f	40 μ F
Switching frequency f_{swit}	2.5 kHz
Sampling frequency f_{samp}	2.5 kHz
Grid Vrms (phase-to-phase) V_g	575 V
Proportional gain of current controller k_{pi}	5.4908e-04
Integral gain of current controller k_{ii}	0.3295
Proportional gain of PLL k_{ppll}	40
Integral gain of PLL k_{ipll}	800
D-axis current reference $I_{g,d}^{ref}$	2.0 kA
Integral gain of PLL $I_{g,q}^{ref}$	0

The measured Bode diagrams of the q-axis admittance component in step 1, step 2 and step 3 of Fig. 6 are shown as $\mathbf{Y}_{cl,qq}^{PLL}$, $\mathbf{Y}_{cl,qq}^{CCL}$ and $\mathbf{Y}_{cl,qq}^{PLL1}$ in Fig. 7, respectively. It can be seen that the high-frequency admittance characteristic and low-frequency admittance characteristic can be extracted in step 2 and step 3, respectively, which verifies the effectiveness of the proposed impedance features division method in Section III-B.

Next, the extracted high-frequency admittance characteristic $\mathbf{Y}_{cl,qq}^{CCL}$ is used to assess the stability issue related with current control loop. Fig. 8(a) shows the Bode diagrams of $\mathbf{Y}_{cl,qq}^{CCL}$ and q-axis admittance component of a 2 km transmission line $\mathbf{Y}_{cl,qq}^{TL2}$. It can be seen that the phase angle difference at the magnitude interaction point 799 Hz is $86.92^\circ - (-95.43^\circ) = 182.35^\circ$, which indicates that the system is unstable at 799 Hz. Fig. 8(b) shows the time-domain simulation results of three-phase grid currents, and the FFT is shown in Fig. 8(c). It can be seen that the three-phase grid currents oscillate at 780 Hz, which agree with the theoretical analysis results in Fig. 8(a).

Next, the extracted low-frequency admittance characteristic $\mathbf{Y}_{cl,qq}^{PLL1}$ is used to assess the stability issue related with PLL. Fig. 9(a) shows the Bode diagrams of $\mathbf{Y}_{cl,qq}^{PLL1}$ and q-axis admittance component of a 21 km transmission line $\mathbf{Y}_{cl,qq}^{TL1}$. It can be seen that the phase angle difference at the magnitude

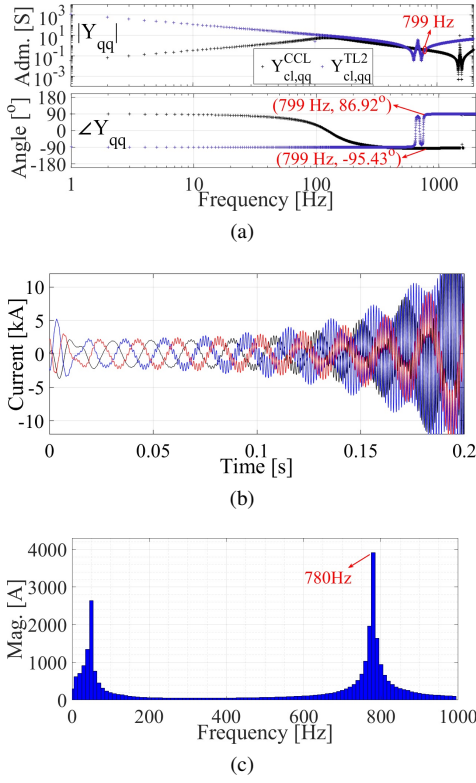


Fig. 8. High-frequency stability analysis based on the extracted admittance characteristics of current control loop. (a) Bode diagrams of $Y_{cl,qq}^{CCL}$ and $Y_{cl,qq}^{TL2}$. (b) Time-domain simulation results of grid currents. (c) FFT of grid currents.

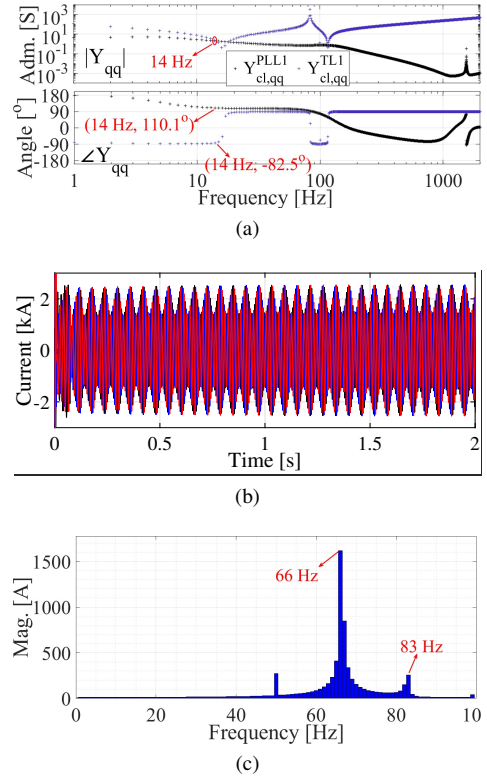


Fig. 9. Low-frequency stability analysis based on the extracted admittance characteristics of PLL. (a) Bode diagrams of $Y_{cl,qq}^{PLL1}$ and $Y_{cl,qq}^{TL1}$. (b) Time-domain simulation results of grid currents. (c) FFT of grid currents.

interaction point 14 Hz is $110.1^\circ - (-82.5^\circ) = 192.6^\circ$, which indicates that the system is unstable at 14 Hz. Fig. 9(b) shows the time-domain simulation results of three-phase grid currents, and the FFT is shown in Fig. 9(c). It can be seen that the three-phase grid currents mainly oscillate at 66 Hz ($50+16=66$ Hz), which agree with the theoretical analysis results in Fig. 9(a).

V. CONCLUSION

This paper presents a frequency scanning-based black box identification method of the contributions of current control loop and PLL on dq-domain impedance characteristics of the GCI without knowing internal circuit and controller parameters. Small-signal three-phase voltage perturbation is injected to excite the grid current responses for extracting the dq-domain impedance characteristics of current control loop and PLL. The theoretical analysis results, frequency scanning results and time-domain simulation results obtained in Matlab/Simulink environment verify the effectiveness of the presented dq-domain impedance characteristics division method. The frequency scanning-based contributions identification of other control loops, e.g., outer power control loop and dc-link voltage control, will be investigated in a future work.

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REFERENCES

- [1] F. Blaabjerg, Z. Chen, and S. B. Kjaer, "Power electronics as efficient interface in dispersed power generation systems," *IEEE Trans. Power Electron.*, vol. 19, no. 5, pp. 1184–1194, Sep. 2004.
- [2] X. Wang and F. Blaabjerg, "Harmonic stability in power electronic-based power systems: Concept, modeling, and analysis," *IEEE Trans. Smart Grid*, vol. 10, no. 3, pp. 2858–2870, May 2019.
- [3] W. Zhou, Y. Wang, and Z. Chen, "Impedance-decoupled modelling method of multi-port transmission network in inverter-fed power plant," *IEEE Trans. Ind. Appl.*, vol. 56, no. 1, pp. 611–621, Jan.-Feb. 2020.
- [4] W. Zhou, Y. Wang, D. Liu, and Z. Chen, "Optimization of active and reactive power dispatch among multi-paralleled grid-connected inverters considering low-frequency stability," in *Proc. IEEE 2019 45th Annual Conference of the IEEE Industrial Electronics Society (IES)*, pp. 6158–6165.
- [5] D. Dong, B. Wen, D. Boroyevich, P. Mattavelli, and Y. Xue, "Analysis of phase-locked loop low-frequency stability in three-phase grid-connected power converters considering impedance interactions," *IEEE Trans. Ind. Electron.*, vol. 62, no. 1, pp. 310–321, Jan. 2015.
- [6] J. Sun, "Impedance-based stability criterion for grid-connected inverters," *IEEE Trans. Power Electron.*, vol. 26, no. 11, pp. 3075–3078, Nov. 2011.
- [7] B. Wen, D. Boroyevich, R. Burgos, P. Mattavelli, and Z. Shen, "Analysis of DQ small-signal impedance of grid-tied inverters," *IEEE Trans. Power Electron.*, vol. 31, no. 1, pp. 675–687, Jan. 2016.

- [8] W. Zhou, Y. Wang, R. E. Torres-Olguin, D. Liu, and Z. Chen, "A modified dq impedance model of three-phase grid-connected inverter-grid system considering coupling between inverter and grid," in *Proc. IEEE 2020 35th Applied Power Electronics Conference and Exposition (APEC)*, pp. 1–8.
- [9] W. Zhou, Y. Wang, and Z. Chen, "A gray-box parameters identification method of voltage source converter using vector fitting algorithm," in *Proc. IEEE 2019 10th International Conference on Power Electronics and ECCE Asia (ICPE 2019-ECCE Asia)*, pp. 2948–2955.
- [10] W. Zhou, Y. Wang, P. Cai, and Z. Chen, "A gray-box impedance reshaping method of grid-connected inverter for resonance damping," in *Proc. IEEE 2019 10th International Conference on Power Electronics and ECCE Asia (ICPE 2019-ECCE Asia)*, pp. 2660–2667.
- [11] J. Huang, K. A. Corzine, and M. Belkhat, "Small-signal impedance measurement of power-electronics-based AC power systems using line-to-line current injection," *IEEE Trans. Power Electron.*, vol. 24, no. 2, pp. 445–455, Feb. 2009.
- [12] G. Francis, R. Burgos, D. Boroyevich, F. Wang, and K. Karimi, "An algorithm and implementation system for measuring impedance in the DQ domain," in *Proc. IEEE 2011 IEEE Energy Conversion Congress and Exposition*, pp. 3221–3228.
- [13] V. Valdivia, A. Lázaro, A. Barrado, P. Zumel, C. Fernández, and M. Sanz, "Impedance identification procedure of three-phase balanced voltage source inverters based on transient response measurements," *IEEE Trans. Power Electron.*, vol. 26, no. 12, pp. 3810–3816, Dec. 2011.
- [14] Z. Shen, M. Jaksic, P. Mattavelli, D. Boroyevich, J. Verhulst, and M. Belkhat, "Design and implementation of three-phase AC impedance measurement unit (IMU) with series and shunt injection," in *Proc. IEEE 2013 Twenty-Eighth Annual IEEE Applied Power Electronics Conference and Exposition (APEC)*, pp. 2674–2681.
- [15] A. Rygg and M. Molinas, "Apparent impedance analysis: A small-signal method for stability analysis of power electronic-based systems," *IEEE J. Emerg. Sel. Top. Power Electron.*, vol. 5, no. 4, pp. 1474–1486, Dec. 2017.
- [16] M. Jaksic, D. Boroyevich, R. Burgos, Z. Shen, I. Cvetkovic, and P. Mattavelli, "Modular interleaved single-phase series voltage injection converter used in small-signal dq impedance identification," in *Proc. IEEE 2014 Energy Conversion Congress and Exposition (ECCE)*, pp. 3036–3045.
- [17] Y. Tang, R. Burgos, B. Wen, D. Boroyevich, J. Verhulst, D. Vrtachnik, and M. Belkhat, "A novel DQ impedance measurement method in three-phase balanced systems," in *Proc. IEEE 2019 20th Workshop on Control and Modeling for Power Electronics (COMPEL)*, pp. 1–5.
- [18] Y. Zhang, X. Du, Y. Shi, C. Zeng, J. Liu, and H.-M. Tai, "Impedance scanning method of grid-tied converters under nonzero grid impedance condition," in *Proc. IEEE 2019 10th International Symposium on Power Electronics for Distributed Generation Systems (PEDG)*, pp. 728–733.
- [19] W. Cao, Y. Ma, X. Zhang, and F. Wang, "Sequence impedance measurement of three-phase inverters using a parallel structure," in *Proc. IEEE 2015 Applied Power Electronics Conference and Exposition (APEC)*, pp. 3031–3038.
- [20] X. Yue, Z. Fang, F. Wang, Z. Zhang, and H. Shi, "A novel adaptive frequency injection method for power electronic system impedance measurement," *IEEE Trans. Power Electron.*, vol. 29, no. 12, pp. 6700–6711, Dec. 2014.
- [21] T. Roinila, T. Messo, and E. Santi, "MIMO-identification techniques for rapid impedance-based stability assessment of three-phase systems in DQ domain," *IEEE Trans. Power Electron.*, vol. 33, no. 5, pp. 4015–4022, May 2018.
- [22] A. Riccobono, M. Mirz, and A. Monti, "Noninvasive online parametric identification of three-phase AC power impedances to assess the stability of grid-tied power electronic inverters in LV networks," *IEEE J. Emerg. Sel. Top. Power Electron.*, vol. 6, no. 2, pp. 629–647, Jun. 2018.
- [23] T. Roinila, H. Abdollahi, S. Arrua, and E. Santi, "Real-time stability analysis and control of multiconverter systems by using MIMO-identification techniques," *IEEE Trans. Power Electron.*, vol. 34, no. 4, pp. 3948–3957, Apr. 2019.
- [24] L. Harnefors, "Modeling of three-phase dynamic systems using complex transfer functions and transfer matrices," *IEEE Trans. Ind. Electron.*, vol. 54, no. 4, pp. 2239–2248, Aug. 2007.
- [25] X. Wang, L. Harnefors, and F. Blaabjerg, "Unified impedance model of grid-connected voltage-source converters," *IEEE Trans. Power Electron.*, vol. 33, no. 2, pp. 1775–1787, Feb. 2018.
- [26] W. Zhou, Y. Wang, R. E. Torres-Olguin, and Z. Chen, "Effect of reactive power characteristic of offshore wind power plant on low-frequency stability," *IEEE Trans. Energy Convers.*, vol. 35, no. 2, pp. 837–853, Jun. 2020.
- [27] A. Rygg, M. Molinas, C. Zhang, and X. Cai, "On the equivalence and impact on stability of impedance modeling of power electronic converters in different domains," *IEEE J. Emerg. Sel. Top. Power Electron.*, vol. 5, no. 4, pp. 1444–1454, Dec. 2017.