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Published in:
IEEE Journal of Emerging and Selected Topics in Power Electronics

DOI (link to publication from Publisher):
10.1109/JESTPE.2020.3025081

Publication date:
2022

Document Version
Accepted author manuscript, peer reviewed version

Link to publication from Aalborg University

Citation for published version (APA):

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Nonlinear Sub-synchronous Oscillation Damping Controller for Direct-drive Wind Farms with VSC-HVDC Systems

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Abstract—Due to the sub-synchronous interaction between the grid-side converter (GSC) of wind farms and the rectifier (REC) of voltage source converter-based high voltage direct current (VSC-HVDC) transmission system, sub-synchronous oscillations (SSOs) may occur in direct-drive wind farms with VSC-HVDC systems. Considering the nonlinearities and uncertainties of the system, a nonlinear SSO mitigation strategy is proposed in this paper based on the feedback linearization theory and sliding mode control (SMC). The feedback linearization theory is used to eliminate the nonlinearities, and the SMC is adopted to improve the robustness against uncertainties and disturbances. The proposed feedback linearization sliding mode controller (FLSMC) takes the advantages of feedback linearization control (FLC) and SMC. The FLC transforms the nonlinear forms of the GSC and REC into the linear forms through the coordinate transformation and feedback. Considering that the FLC is sensitive to parameter uncertainties and external disturbances, the SMC is combined with the FLC to improve the system robustness. An eigenvalue analysis and time-domain simulations are carried out, which demonstrates that the FLC outperforms over the traditional proportional-integral control for the SSO mitigation and decoupling. Meanwhile, the FLSMC shows better robustness against parameter uncertainties and external disturbances over the FLC and traditional damping control.

Index Terms—direct-drive wind farm, feedback linearization, sliding mode control (SMC), sub-synchronous oscillation (SSO), voltage source converter-based high voltage direct current (VSC-HVDC).

NOMENCLATURE

DFIG Doubly fed induction generator.
PMSG Permanent magnet synchronous generator.
WTG Wind turbine generator.
VSC-HVDC Voltage source converter-based high voltage direct current.
DDWFV Direct-drive wind farms with the VSC-HVDC.

This work was supported in part by the Natural Science Foundation of Hebei Province under Grant E2017502075, in part by the Fundamental Research Funds for the Central Universities of China under Grant 2018ZD001, and in part by the Joint Postgraduate Training Program of North China Electric Power University.

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SSO Sub-synchronous oscillation.
FACTS Flexible ac transmission systems.
REC Rectifier.
SSDC Sub-synchronous damping control.
PI Proportional-integral.
EFL Exact feedback linearization.
PFL Partial feedback linearization.
FLC Feedback linearization control.
PV Photovoltaic.
GSC Grid-side converter.
MSC Machine-side converter.
PLL Phase-locked loop.
FLSMC Feedback linearization sliding mode control.
SMC Sliding mode control.
$u_{dc}$ DC voltage of the GSC.
$C$ DC capacitor of the back-back converter.
$i_{dc1}$ DC-side input current of the wind farm.
$i_{dq}$ $d$- and $q$-axis output current of the GSC.
$u_{w1d}$, $u_{w1q}$ $d$- and $q$-axis output voltage of the GSC.
$L_g$ Filter inductance of the wind farm.
$u_{pg1}$, $u_{pgq}$ $d$- and $q$-axis primary-side voltage of the transformer $T_1$.
$\omega_g$ Angular frequency.
$R_c$, $L_c$ Equivalent resistance and inductance of the phase reactor.
$C_f$ Filter capacitor of the VSC-HVDC.
$i_{2d}, i_{2q}$ $d$- and $q$-axis input current of the VSC-HVDC.
$i_{sd}$, $i_{sq}$ $d$- and $q$-axis current flowing to the phase reactor.
$u_{cd}$, $u_{cq}$ $d$- and $q$-axis output voltage of the REC.
$C_{dc}$ DC capacitor of the VSC-HVDC.
$u_{dc1}$ DC voltage across $C_{dc}$.
$\omega_0$ Reference angular frequency.
$n_w$ Order of the GSC.
$u_{w1}$, $u_{w2}$ Control input variables of the GSC.
$\hat{h}_w(x)$ Control output variables of the GSC.
Order of the REC.

Control input variables of the REC.

Control output variables of the REC.

Total relative degree of the GSC and REC.

Total relative degree of the REC.

Transformed state variables of the GSC and REC.

Function of $x_w$, function of $x_v$.

External states to the order of the relative degrees from the GSC and REC.

Internal states associated to the order of $n_w - r_w$ and $n_v - r_v$.

State matrix, input matrix, and control input of the partially linearized GSC.

State matrix, input matrix, and control input of the partially linearized REC.

Pre-control variables of the GSC.

Pre-control variables of the GSC.

PI parameters of the DC voltage controller in the GSC.

PI parameters of the $q$-axis current controller in the GSC.

PI parameters of the $d$-axis voltage controller in the GSC.

PI parameters of the $q$-axis voltage controller in the REC.

$d$- and $q$-axis sliding mode surfaces of the GSC.

Parameters of constant rate reaching laws in the GSC.

$d$- and $q$-axis sliding mode surfaces of the REC.

Parameters of constant rate reaching laws in the REC.

Subscript indicates the reference of variables.

Uncertainties or disturbances in the GSC and REC.

FLC is used in the GSC and the PI control is adopted in the REC.

FLC is used in the REC and the PI control is adopted in the GSC.

Point of common coupling.

Output signal of SSDC.

Gain of the SSDC.

Phase-lead loop of the SSDC.

Phase-lag loop of the SSDC.

Number of phase-lead loops.

Number of phase-lag loops.

Switching loss of the REC.

Output current of the REC.

I. INTRODUCTION

By 2019, the global installed capacity of the wind power exceeded 651 GW [1]. For the wind energy integration, the DFIG with partial-scale converters and the PMSG with full-scale converters are two popular WTGs [2]. Compared with the DFIG, the PMSG is more efficient and maintenance-free due to the absence of slip rings, brushes, and gearboxes [3]. Meanwhile, with the increase of the capacity and connection distance of offshore wind farms, the VSC-HVDC transmission technology has become an economically feasible solution [4]. Thus, the DDWFV is a promising wind energy integration solution.

However, the stability of offshore wind farms with VSC-HVDC systems is a key issue due to no direct connection from the AC collection bus to a strong AC grid [5]. For instance, as reported in [6], an SSO frequency of about 21 Hz was observed in a wind farm with a VSC-HVDC system. It was found in [5]-[12] that the SSO in wind farms with VSC-HVDC systems was mainly related to the interaction between the controllers of wind farms and the REC controller of the VSC-HVDC. With the impedance analysis methods in [5]-[7], the SSO in the DDWFV was arguably originated by the interaction between the wind farm inverter controller and the VSC-HVDC REC controller. The controller parameters affect the SSO stability to a large extent. Meanwhile, the impedance analysis method was also used to analyze the stability of the DFIG-based wind farms with VSC-HVDC systems [8]. It has been found in [8] that the rotor-side converter controller of the DFIG and the REC controller of the VSC-HVDC play an important role in the SSO stability. With the eigenvalue analysis methods in [9]-[11], it has been revealed that the DDWFV experiences dynamic SSO instability due to the interactions between different controllers of the wind farm and the REC station. Moreover, in [12], an open-loop modal method was used, which has indicated that when the PMSGs participate in the open-loop modal couplings, strong sub-synchronous interactions between the converter control of the VSC-HVDC and the PMSGs cause the SSOs. The SSO can damage system equipment [13], [14], reduce the output power [15]-[18], and degrade the power quality [19], [20]. Therefore, it is of importance to develop an efficient SSO damping control strategy for the DDWFV.

At present, there are three types of methods to mitigate the SSO in wind farms: 1) using FACTS [21]-[26], 2) adding SSO damping to the converter controllers of wind farms [6], [27]-[32], and 3) optimizing the parameters of the present converter controllers [10], [33], [34]. However, using FACTS devices to mitigate the SSO in wind farms is not economically feasible. In addition, the SSDC is designed based on the approximately linearized model of a system. This implies that the controller lacks robustness and can achieve acceptable performance only within a predefined range of uncertainties. The disadvantage also exists in the methods by optimizing the controller parameters. Although this issue can be addressed by adaptively adjusting the parameters [35], [36], the dynamics are slow, and PI controller parameters exceeding a certain range weakens the wind farms’ faults ride-through capacity [27], [37]. Moreover, the major focus has been put on the SSO mitigation in wind farms connected to series-compensated system or a weak AC grid [38], whereas few studies are carried out to mitigate the SSO of wind farms with VSC-HVDC systems [6], [32], [34]. In [6], [32], the arm virtual resistance method, resonant voltage compensation method, and harmonic circulating current suppression were discussed to mitigate the SSO of the DDWFV. Meanwhile, optimizing the controller parameters of the DDWFV can improve the SSO [34]. As the SSO mitigation methods in [6], [32], [34] were designed based
on the approximately linearized model of the DDWFV, the inherent nonlinearities of the DDWFV cannot be considered, and then acceptable performance is only achieved within a predefined set of operations. To overcome this, a robust damping controller should be proposed to mitigate the SSO of the DDWFV over a wide range of operating conditions.

The main idea behind FLC is the reconfiguration of the nonlinear system using a transformation and feedback loop in order to obtain a linear relationship between the input and output of the system [39]. As the FLC can eliminate the nonlinearities of the DDWFV without additional devices, using the FLC to mitigate the SSO can fill in the above gaps. The FLC has been successfully used in other cases, e.g., in PV systems for decoupling the dynamical models of multiple PV units [40]. In PMSG wind power systems for low-voltage ride-through operation [41], in HVDC systems for decoupling power and circulating currents [42], and in DC microgrids for maintaining the desired voltage at the common dc bus [43]. However, the FLC was rarely used to suppress the SSO, and the application of the FLC in the DDWFV has not been explored. In [44], [45], the SSO in series-compensated DFIG-based wind farms was identified, and a nonlinear damping controller using the feedback linearization technique was designed to mitigate the SSO. Different from the case in [44], [45], the FLC is proposed in this paper to mitigate the SSO of the DDWFV. Although the FLC design process of the GSC is nearly the same as that in [44], [45], the PFL and internal dynamics of the REC are examined in this paper, which will be discussed in Section III. Additionally, considering that the FLC lacks intrinsic robustness against parametric uncertainties and external disturbances [46], a FLSMC scheme integrating FLC and SMC is proposed in this paper. The SMC stands out because of its inherent characteristics such as robustness, insensitivity to system variation, and simple implementation [47]. On one hand, the SMC relieves the dependence of the FLC on the accurate mathematical model. On the other hand, the FLC helps to establish a linear sliding mode function, and then the system dynamic performance is improved [48]. The main contributions of this paper are summarized as follows:

- The partial feedback linearization of the DDWFV is achieved, and the effectiveness and superiority of the FLC compared to the PI control in SSO mitigation are verified under various operating conditions.
- The FLSMC is designed to improve the robustness of the FLC, and the stability and robustness of the FLSMC are verified by theoretical analysis. Meanwhile, the superiority of the FLSMC compared to the PI, FLC, and SSDC is demonstrated.

The rest of this paper is organized as follows. In Section II, the model of the DDWFV is developed. In Section III, the FLC is designed through four steps. In Section IV, the SMC is combined with the FLC to improve the robustness of the DDWFV. The structures, SSO mitigation performances, and robustness of the PI, FLC, and FLSMC are discussed in Section V. The SSO mitigation performances, decoupling characteristics, and robustness of the proposed controller are then evaluated in Section VI through an eigenvalue analysis and time-domain simulations. Concluding remarks are given in Section VII.

II. MODELING OF THE DDWFV

The diagram of the DDWFV is divided into the direct-drive wind farm and the VSC-HVDC, as shown in Fig. 1. 40 wind turbines with the power rating of 5 MW for each are lumped into one unit of 200 MW capacity. Then, an equivalent model of a PMSG with the 200-MW installed capacity is equivalent to a direct-drive wind farm. The system parameters of the DDWFV are shown in Appendix A. It was indicated in [5] that the SSO of the DDWFV resulted from the interaction between the GSC and REC. Therefore, the control objectives of the proposed damping controller should be the GSC and REC. The dynamic mathematical models of the GSC and REC are derived in the following:

A. Dynamic Models of the GSC and REC

\[
\begin{align*}
\frac{d\text{un}_d}{dt} &= \frac{1}{C_1} i_{d_{\text{sl}}} - \frac{1}{C_1} i_d + \omega y u_{\text{dq}} \\
\frac{d\text{un}_g}{dt} &= \frac{1}{L_g} u_{\text{dq}} + R_i i_{d_{\text{sl}}} - \frac{1}{L_g} u_d + \omega y i_{\text{dq}} \\
\frac{d\text{is}_{\text{sl}}}{dt} &= \frac{1}{C_{\text{id}}} u_d - \frac{1}{C_{\text{id}}} i_{d_{\text{sl}}} + \omega y u_{\text{dq}} \\
\frac{d\text{is}_g}{dt} &= \frac{1}{L_g} u_{\text{dq}} - \frac{1}{L_g} i_{d_{\text{sl}}} - u_d - \omega y i_{\text{dq}} \\
\frac{d\text{is}_{\text{sl}}}{dt} &= \frac{3 \text{un}_d i_{d_{\text{sl}}} + 3 \text{un}_g i_{d_{\text{sl}}}}{2 C_{\text{id}} u_{\text{sl}}} + \frac{i_{d_{\text{sl}}}}{C_{\text{dc}}}
\end{align*}
\]
According to Fig. 1, the dynamics of the GSC are shown in (1), where the state variables are $\mathbf{u}_{d}$, $\mathbf{u}_{q}$, $i_{gd}$, and $i_{gq}$, and the control input variables are $\mathbf{u}_{w_d}$ and $\mathbf{u}_{w_q}$.

Furthermore, referring to Fig. 1, the dynamics of the REC are represented by (2), where the state variables are $\mathbf{u}_{d}$, $\mathbf{u}_{q}$, $\mathbf{u}_{w_d}$, and $\mathbf{u}_{w_q}$, and the control input variables are $\mathbf{u}_{w_d}$ and $\mathbf{u}_{w_q}$.

B. PI Control Structures of the DDWFV

In the wind farm, the MSC controls the $d$-axis current to be 0, which minimizes the loss of the generator. The DC voltage and reactive power are controlled by the GSC. The $d$-$q$ rotating coordinate system is set based on the node voltage $u_n$ and the $q$-axis component of $u_p$ is the input of a PLL. The control structures of the MSC, GSC, and PLL are shown in Figs. 2-4.

![Control structure of the machine-side converter with PI controllers.](image)

Fig. 2. Control structure of the machine-side converter with PI controllers, where $m_{w_d}$ and $m_{w_q}$ are the $d$- and $q$-axis modulation signals; $\omega_r$ is the electric speed of the PMSG; $\psi_r$ represents the magnetic flux.

![Control structure of the grid-side converter with PI controllers.](image)

Fig. 3. Control structure of the grid-side converter with PI controllers, where $m_{w_d}$ and $m_{w_q}$ are the $d$- and $q$-axis modulation signals.

![Control structure of the phase-locked loop in the wind farm.](image)

Fig. 4. Control structure of the phase-locked loop in the wind farm, where $k_{p,pi}$ and $k_{i,pi}$ are the proportional and integral coefficients.

![Control structure of the rectifier with PI controllers.](image)

Fig. 5. Control structure of the rectifier with PI controllers, where $m_{w_d}$ and $m_{w_q}$ are the $d$- and $q$-axis modulation signals.

In the VSC-HVDC, the REC controls the AC voltage amplitude and frequency, and the inverter controls the DC voltage. Supposing that the onshore AC grid is strong, the DC bus voltage can be kept constant by the inverter station. Then, a constant DC voltage source is equivalent to the inverter station, which is shown as $u_{dc}$ in Fig. 1. Notably, the PLL of the VSC-HVDC takes the reference angular frequency $\omega_0$ as an input signal, and $\omega_0 = 2\pi f_0$ ($f_0 = 50 \text{ Hz}$) [5]. The control structure of the REC is shown in Fig. 5.

III. FLC OF THE DDWFV

Traditional PI control strategies are designed based on the specific operating point of the DDWFV. The performance of the strategies is easily affected by the nonlinearities of the DDWFV. Therefore, the FLC is applied in the GSC and REC to cancel the nonlinearities. The design of the FLC mainly includes four steps: 1) scrutinizing EFL or PFL, 2) transforming the nonlinear system to a linear system by the coordinate transformation and feedback, 3) if the PFL is performed, the initial system should be divided into external dynamics and internal dynamics. The external dynamics need to be designed properly, and the stability of internal dynamics needs to be assured, 4) deriving the control law, and the pre-control variables after coordinate transformation are designed. Each step is discussed in detail as follows.

A. Scrutinizing Feedback Linearization

The first step of designing the FLC is to scrutinize the feedback linearization of the studied systems (1) and (2). Based on the control input $u$ and control output $y$, the dynamic models of (1) and (2) are represented in the general form of multiple-input and multiple-output (MIMO) systems as

$$\begin{align*}
\dot{x}_w &= f_w(x) + g_w(x)u_{w_1} + g_{w_2}(x)u_{w_2} \\
y_w &= h_w(x)
\end{align*}$$

in which (3) represents the affine nonlinear system of the GSC, $\mathbf{x}_w = [u_{dc}, i_{gd}, i_{gq}]^T$, $n_w = 3$, $u_{w_1} = u_{w_d}$ and $u_{w_2} = u_{w_q}$, $h_w(x) = [h_w(x), h_{w_2}(x)]^T = [u_{dc}, i_{gq}]^T$. (4) represents the affine nonlinear system of the REC, $\mathbf{x}_v = [u_{sd}, u_{sq}, i_{sd}, i_{sq}, u_{dc}]^T$, $n_v = 5$, $u_{v_1} = u_{vd}$ and $u_{v_2} = u_{vq}$, $h_v(x) = [h_v(x), h_{v_2}(x)]^T = [u_{sd}, u_{sq}]^T$. The expressions of $f_w(x), g_{u_1}(x), f_v(x)$, and $g_{u_2}(x)$ are shown in Appendix B.

The total relative degree determines the feedback linearizability of a nonlinear system. The system should be exactly linearized if the total relative degree equals to the order of the system, or the system can be partially linearized if the total relative degree is less than the order of the system. The total relative degree of the GSC is calculated as [42]

$$\begin{align*}
L_{x_1}^1 h_w(x) &= -3i_{gd}/2C_{\text{dc}} \\
L_{x_2}^1 h_w(x) &= -3i_{gq}/2C_{\text{dc}} \\
L_{x_1}^2 h_w(x) &= 0 \\
L_{x_2}^2 h_w(x) &= 1/\eta_g
\end{align*}$$

where $L$ defines the Lie derivative with respect to the corresponding subscripts. For example, $L^1_{x_1} h_w(x)$ represents the $(r_1 - 1)$th Lie derivative of $h_w(x)$ along $f(x)$, where $r_1$ is the relative
degree corresponding to the output function \( h(x) \). As \( B = [-3i_{g_L}/2C_{DC},-3i_{g_P}/2C_{DC}] \) is nonsingular, the total relative degree of the GSC is \( r_w = 1+1 = 2 \), which is less than the order of the GSC (\( n_w \)). Thus, the GSC is partially linearizable. In addition, the total relative degree (\( r_v \)) of the REC is calculated to be 4 in the same way. As \( r_v < n_v \), the REC is also partially linearizable.

### B. Nonlinear Coordinate Transformation and Feedback

Since the GSC and REC can be partial-feedback linearized, the transformed state variables of the GSC and REC, \( z_w \) and \( z_v \), are written as

\[ z_w = \Phi_w (x) = [z_{w1}, z_{w2}]^T \]

\[ z_v = \Phi_v (x) = [z_{v1}, z_{v2}, z_{v3}, z_{v4}]^T \]

Therefore, the partially linearized GSC and REC are expressed as

\[ \dot{z}_w = A_w (x)z_w + B_w (x)w \]

\[ \dot{z}_v = A_v (x)z_v + B_v (x)v \]

As the GSC controls the DC voltage and \( q \)-axis current, and REC controls the \( d \)- and \( q \)-axis AC voltage, we have

\[ z_{wo1} = [z_{w1}, z_{w2}]^T \]

\[ z_{vo1} = [z_{v1}, z_{v2}, z_{v3}, z_{v4}]^T \]

\( \Phi_w (x) = [h_{w1}, h_{w2}, h_{w3}, h_{w4}] = i_{qg} \)

\( \Phi_v (x) = [h_{v1}, h_{v2}, h_{v3}, h_{v4}] = u_{ad} \)

Referring to (1), (2), it gives that

\[ \dot{z}_{wo1} = \dot{h}_{w1} (x) = \frac{i_{qg}}{C} + \frac{3i_{g_L}}{2C_{DC}} - \frac{3i_{g_P}}{2C_{DC}} - u_{ad} \]

\[ \dot{z}_{wo2} = \dot{h}_{w2} (x) = -\frac{1}{L_y} u_{ad} - \frac{\omega_b i_{qg}}{L_y} + \frac{1}{L_y} u_{aq} \]

\[ \dot{z}_{vo1} = \dot{h}_{v1} (x) = \frac{i_{dL}}{C_{iL}} - \frac{i_{dL}}{C_{iL}} + \omega_b u_{aq} \]

\[ \dot{z}_{vo2} = \dot{h}_{v2} (x) = \frac{i_{dL}}{C_{iL}} - \frac{i_{dL}}{C_{iL}} - \omega_b u_{ad} \]

\[ \dot{z}_{vo3} = \dot{h}_{v3} (x) = \frac{\omega_b}{L_y} i_{qg} + \frac{R}{L_y} i_{qg} - \frac{2\omega_b}{L_y} i_{qg} \]

\[ \dot{z}_{vo4} = \dot{h}_{v4} (x) = \frac{\omega_b}{L_y} i_{qg} + \frac{R}{L_y} i_{qg} + \frac{2\omega_b}{L_y} i_{qg} \]

from which, it is shown that only the pre-control variables (\( v_{w1}, v_{w2}, v_{v1}, v_{v2} \)) are not determined. The pre-control variables are normally designed from the following linear control equations [41], [42]:

\[ v_{w1} = k_{w1} (u_{DCref} - u_{DC}) + k_{w1} \int (u_{DCref} - u_{DC}) \, dt \]

\[ v_{w2} = k_{w2} (i_{gref} - i_{g}) + k_{w2} \int (i_{gref} - i_{g}) \, dt \]

\[ v_{v1} = k_{v1} (u_{DCref} - u_{DC}) + k_{v1} \int (u_{DCref} - u_{DC}) \, dt \]

\[ v_{v2} = k_{v2} (i_{gref} - i_{g}) + k_{v2} \int (i_{gref} - i_{g}) \, dt \]

Accordingly, the control structures of the GSC and REC are obtained as...
under the FLC are shown in Figs. 6 and 7. As it can be seen from Figs. 3 and 6, Figs. 5 and 7, compared with the PI control, the FLC increases the algebraic computation, but certain \( d \)- and \( q \)-axis PI control loops are cancelled, thus reducing the difficulty of PI parameters tuning.

\[
(2i_d \mu_{DC} - 3i_g \mu_{gq})/3i_{gd}
\]

![Fig. 6. Control structure of the grid-side converter under the feedback linearization control.](image)

![Fig. 7. Control structure of the rectifier under the feedback linearization control.](image)

![Fig. 8. Control structure of the grid-side converter under the feedback linearization sliding mode control.](image)

![Fig. 9. Control structure of the rectifier under the feedback linearization sliding mode control.](image)

IV. FLSMC OF THE DDWFV

As the FLC is a model-based method, it is sensitive to parameter uncertainties and external disturbances [39]. In order to improve the robustness of the FLC, the SMC is used to design the pre-control variables \((v_{q1}, v_{q2}, v_{q1}, v_{q1})\) of (16), (17). The SMC offers good properties, such as fast dynamics and insensitivity to external disturbances. The design of SMC mainly includes two steps: 1) designing sliding mode surfaces and 2) deriving equivalent control laws. Then, the stability and robustness of the SMC should be examined.

A. Designing Sliding Mode Surfaces

Since the control objectives of the GSC are DC voltage and \( q \)-axis current, the sliding mode surface of the GSC is selected as

\[
S_{ad} = u_{DC} - u_{DCref}, S_{aq} = i_{qg} - i_{qgref}
\]

Notably, chattering is a major drawback of the SMC, which can be weakened by the rational design of SMC laws [47]. To reduce the system chattering, the SMC law of the GSC is designed based on constant rate reaching law as

\[
\begin{align*}
\dot{S}_{ad} &= -\varepsilon_{ad}\text{sgn}S_{ad} \quad \dot{S}_{aq} = -\varepsilon_{aq}\text{sgn}S_{aq} \\
S_{ad} &= S_{adref}, S_{aq} = S_{aqref}
\end{align*}
\]

Smaller \( \varepsilon_{ad} \) and \( \varepsilon_{aq} \) imply smaller chattering but longer settling time [49]. Similarly, the SMC law of the REC is designed as

\[
\begin{align*}
\dot{S}_{ad} &= -\varepsilon_{ad}\text{sgn}S_{ad} \quad \dot{S}_{aq} = -\varepsilon_{aq}\text{sgn}S_{aq} \\
S_{ad} &= S_{adref}, S_{aq} = S_{aqref}
\end{align*}
\]

B. Equivalent Control Laws

From (21), (22), the pre-control variables in (16) and (17) are designed based on the SMC as

\[
\begin{align*}
v_{q1} &= -\varepsilon_{ad}\text{sgn}S_{ad}, v_{q2} = -\varepsilon_{aq}\text{sgn}S_{aq} \\
v_{q1} &= -\varepsilon_{ad}\text{sgn}S_{ad}, v_{q2} = -\varepsilon_{aq}\text{sgn}S_{aq}
\end{align*}
\]

Combining FLC and SMC by substituting (23), (24) into (16), (17), we have the equivalent control laws of the FLSMC as

\[
\begin{align*}
\dot{m}_{ad1} &= \frac{2}{u_{DC}}(2i_d \mu_{DC} - 3i_g \mu_{gq})/3i_{gd} \\
\dot{m}_{aq1} &= \frac{2}{u_{DC}}(2i_d \mu_{DC} + L_i e_{ad} - L_i e_{aq})/3i_{gd}, \\
\dot{m}_{ad2} &= \frac{2}{u_{DC}}(2i_d \mu_{DC} + L_i e_{ad} - L_i e_{aq})/3i_{gd}, \\
\dot{m}_{aq2} &= \frac{2}{u_{DC}}(2i_d \mu_{DC} + L_i e_{ad} - L_i e_{aq})/3i_{gd},
\end{align*}
\]

From (25), (26), the control structures of the GSC and REC under the FLSMC are shown in Figs. 8 and 9.

C. Proof of the Stability and Robustness

\[
\begin{align*}
V_{w}(S) &= S_{w}^{T}S_{w} = \frac{1}{2}S_{ad}^{2} + \frac{1}{2}S_{aq}^{2} \\
V_{w}(S) &= S_{w}^{T}S_{w} = \frac{1}{2}S_{ad}^{2} + \frac{1}{2}S_{aq}^{2}
\end{align*}
\]

1) Proof of the Stability: The first objective of the SMC is to
ensure the convergence of the operating points. To examine the stability, a Lyapunov function is introduced in (27), where \( S_w = [S_{wd} \ S_{wq}]^T \), \( S_v = [S_{vd} \ S_{vq}]^T \). The SMC is considered asymptotically stable if \( dV_w/dt < 0 \) and \( dV_v/dt < 0 \). The derivative of \( V_v \) is calculated as

\[
\dot{V}_v(S) = \frac{\partial V_v(S)}{\partial S_{wd}} \frac{dS_{wd}}{dt} + \frac{\partial V_v(S)}{\partial S_{wq}} \frac{dS_{wq}}{dt} = S_w \dot{S}_{wd} + S_q \dot{S}_{wq} \tag{28}
\]

\[
\dot{V}_v(S) = -\epsilon_{wd} S_{wd} sgn S_{wd} - \epsilon_{wq} S_{wq} sgn S_{wq} = -\epsilon_{wd} |S_{wd}| - \epsilon_{wq} |S_{wq}|
\]

As \( \epsilon_{wd}, \epsilon_{wq} > 0 \), \( dV_v/dt \) is a negative-definite function. Similarly, \( dV_w/dt \) is also a negative-definite function. Therefore, \( S_{wd}, S_{wq}, S_{vd}, \) and \( S_{vq} \) approach zero asymptotically, and the proposed FLSMC is asymptotically stable.

2) Proof of the Robustness: Under practical application conditions, parameter uncertainties and external disturbances may appear in the control system. In such conditions, \( (21) \) and \( (22) \) are rewritten as

\[
\dot{S}_w = F_w + H_w \tag{29}
\]

\[
\dot{S}_v = F_v + H_v \tag{30}
\]

where \( F_w = [-\epsilon_{wd} sgn S_{wd} \ -\epsilon_{wq} sgn S_{wq}]^T \), \( H_w \) represents the uncertainties or disturbances in the GSC, and \( H_v = [H_{wd} H_{wq}]^T \); \( F_v = [-\epsilon_{vd} sgn S_{vd} \ -\epsilon_{vq} sgn S_{vq}]^T \), \( H_v \) represents the uncertainties or disturbances in the REC, and \( H_v = [H_{vd} H_{vq}]^T \). Substituting \( (29) \) into \( (28) \) leads to

\[
\dot{V}_v(S) = \epsilon_{wd} S_{wd} + S_q (\epsilon_{wq} sgn S_{wq} + H_{wq})
\]

\[
= -\epsilon_{wd} |S_{wd}| + H_{wd} S_{wd} + (-\epsilon_{wq} |S_{wq}| + H_{wq} S_{wq}) \tag{31}
\]

According to the Lyapunov’s stability theorem, the GSC under the FLSMC features strong robustness if \( (31) \) is less than 0. Thus, the coefficients of the SMC should be designed properly as

\[
\epsilon_{wd} > H_{wd} \quad \text{and} \quad \epsilon_{wq} > H_{wq} \tag{32}
\]

Similarly, to assure the robustness of the REC under the FLSMC, \( \epsilon_{vd} \) and \( \epsilon_{vq} \) need to be designed larger than \( H_{vd} \) and \( H_{vq} \), respectively. Considering that the coefficients of the FLSMC affect the chattering, settling time, and robustness, they should be designed to make a trade-off among these performances.

V. COMPARISON AMONG PI, FLC, AND FLSMC

In this section, the structures, SSO mitigation performances, and robustness of the PI, FLC, and FLSMC are compared, as shown in Table I. From the perspective of control structures under different control strategies (see Figs. 3, 5, 6-9), the PI control strategy has the most PI loops (3 in the GSC and 4 in the REC), but the algebraic computation is light. In contrast, the number of PI loops in both the GSC and REC under the FLC are two, whereas the algebraic computation is moderate. It is worth pointing out that the FLSMC has no PI loops, and its algebraic computation is almost at the same level as the FLC. From the perspective of SSO mitigation performances, as the PI control strategy is designed based on the approximately linearized model of the DDWFV, it can only achieve desired performance within certain operation conditions. Thus, compared with the FLC and FLSMC, the PI control strategy performs poorly in SSO mitigation under various operating conditions. From the perspective of robustness under different control strategies, as the SMC improves the robustness of the DDWFV, the FLSMC shows stronger robustness than the PI control and FLC. In brief, the FLSMC provides an acceptable trade-off among complexity, SSO damping performances, and robustness.

<table>
<thead>
<tr>
<th>TABLE I STRUCTURES, SSO MITIGATION PERFORMANCES AND ROBUSTNESS OF DIFFERENT CONTROL STRATEGIES.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Control strategies</td>
</tr>
<tr>
<td>PI</td>
</tr>
<tr>
<td>FLC</td>
</tr>
<tr>
<td>FLSMC</td>
</tr>
</tbody>
</table>

It is worth mentioning that the heuristic algorithms (e.g., genetic algorithm, simulated annealing algorithm, and particle swarm algorithm) can also be used to mitigate the SSO of series-compensated DFIG-based wind farms by optimizing controller parameters [25], [50]-[52]. In the optimization with heuristic algorithms, the objective function was set based on the small-signal model of a system, and the robustness was improved based on the transfer function from disturbances to control output [52]. Compared with the FLC and FLSMC, the optimization method does not need to examine the feedback linearization and internal dynamics of a system. However, as the objective functions in [25], [50]-[52] are the linearized state-space model, the method cannot achieve a satisfactory performance over a wide range of operating conditions. This implies that the heuristic algorithms may face the risk of local optima [51]. Although this issue can be solved by performing the optimization every time when the operating point changes, the process is time-consuming. In [51], [52], representative operating conditions were included in the optimization process, and then controller parameters were optimized to make compromises among different conditions. However, it is difficult to cover the full range of operating conditions in the optimization process, and a high amount of calculation is required.

VI. CONTROLLER PERFORMANCE EVALUATION

In this section, the SSO mitigation performances and step response characteristics of the PI and FLC are first compared. Then, the robustness of the FLC and FLSMC is benchmarked. Finally, the performance of FLSMC is compared with a SSDC to verify its superiority. The well-tuned control parameters of the PI, FLC, FLSMC, and SSDC are shown in Appendix D.

A. Evaluation of SSO Mitigation Performance

The SSO mitigation performances of the PI and FLC are compared through eigenvalue analysis and PSCAD/EMTDC simulations under different wind speeds, DC voltages and sizes of the wind farm.

1) Different Wind Speeds: Define FLC-GSC as the scenario that the FLC is used in the GSC and the PI control is used in the REC; Define FLC-REC as the scenario that the FLC is used in the REC and the PI control is used in the GSC. Based on the small-signal model of Fig. 1 [9], the root locus of the SSO modes under different wind speeds is shown in Fig. 10 (every two adjacent points have a wind speed difference of 0.5 m/s).
It can be seen from Fig. 10 that the FLC-GSC significantly improves the SSO damping under different wind speeds. However, compared with the PI control, the SSO damping under the FLC-REC does not increase obviously. The main reason is that the GSC participates more in the SSO mode compared with the REC. The normalized participation factors of the SSO mode under the PI control are shown in Fig. 11, where $x_4 = \int (u_{DC} - u_{DCref})dr$, $x_5 = \int (i_{gref} - i_{g})dr$, and $x_7 = \int (i_{dref} - i_{d})dr$. It can be seen from Fig. 11 that the state variables of the GSC participate more in the SSO mode than those of the REC. Thus, it is better to use the FLC in the GSC.

To verify the theoretical analysis in Fig. 10, simulations are performed when a three-phase short-circuit ground fault happens in the common bus (point e in Fig. 1) at 2 s and is cleared after 50 ms. The responses of the wind farm DC voltage and PCC voltage are shown in Fig. 12. It can be seen from Fig. 12(a) that the DC voltage exhibits decayed SSO under the PI, FLC-GSC, and FLC-REC. The SSO frequency under the PI control at 8 m/s wind speed is 1/0.189 (5.291) Hz. As the state variables of the REC participate in the SSO with less participation factors under the PI control (see Fig. 11), replacing PI control with FLC in the REC almost has no effect on the SSO mitigation. Thus, the DC voltage responses under the FLC-REC and PI control are nearly unchanged, as shown in Fig. 12(a). Compared with the FLC-REC, as the state variables of the GSC participate in the SSO with more participation factors under the PI control (see Fig. 11), replacing PI control with FLC in the GSC will affect the SSO mitigation to a large extent. Thus, the DC voltage under the FLC-GSC decays faster and the fluctuation is smaller, as shown in Fig. 12(a).

A large DC capacitor can reduce the DC voltage fluctuations under unbalanced active power [53], as shown in Fig. 13. Therefore, compared with the PI control, a smaller DC
capacitor can be used in the FLC-GSC to maintain the DC voltage at the same level, which is cost-effective. Meanwhile, it can be seen from Fig. 12(b) that the transient responses of the PCC voltage under the FLC-GSC are faster, but there are large fluctuations in the transient process. This is mainly because the FLC is a model-based control strategy and is sensitive to external disturbances. According to Figs. 10 and 12(a), the quantitative comparisons of the SSO mitigation performances under different wind speeds are shown in Table II, where the eigenvalues, overshoots and settling time of wind farm DC voltage are presented.

Fig. 13. DC voltage transient responses for three-phase short-circuit ground fault at 2 s with different DC capacitors under the PI control.

2) Different DC Voltages: Under the condition of 8 m/s wind speed, the root locus of the SSO mode under different wind farm DC voltages is shown in Fig. 14 (every two adjacent points have a voltage difference of 0.5 kV).

Fig. 14 shows that compared with the SSO modes under the PI control, the SSO modes under the FLC-GSC are farther from the imaginary axis on the left side of the complex plane, and thus the SSO damping increases under the FLC-GSC. However, there is not much difference between the SSO modes under the FLC-REC and those under the PI control. To verify the theoretical analysis in Fig. 14, PSCAD/EMTDC simulations are performed under different wind farm DC voltages. The disturbance that occurred in the simulations was the same as that in Fig. 12. The responses of the wind farm DC voltage and PCC voltage are shown in Fig. 15.
It can be seen from Fig. 15 that compared with the PI control and FLC-REC, the SSO damping of the wind farm DC voltage increases significantly under the FLC-GSC, but the fluctuations of the PCC voltage are large when the wind farm DC voltage is 5.5 kV. Meanwhile, the responses of the wind farm DC voltage and PCC voltage under the FLC-REC and PI control are almost identical. This implies that the FLC-REC cannot achieve obvious SSO damping performances. As it can be known from Figs. 14 and 15(a), the quantitative comparisons of the SSO mitigation performances under different wind farm DC voltages are shown in Table III.

3) Different Sizes: To evaluate the SSO mitigation characteristics under different capacity of the wind farm, a wind park including one cluster, two clusters and three clusters is respectively used for the SSO analysis, as shown in Fig. 16. In each cluster, 40 PMSGs with the power rating of 5 MW for each are lumped into one unit of 200 MW capacity. The clusters are linked to the PCC via collector cables, where \( R_g \), \( L_g \) and \( C_g \) are collector cable parameters with the subscripts “1”, “2”, and “3”, representing the corresponding cluster. The simulation parameters of each cluster are shown in Appendix A, and the wind speed is set to 7 m/s.

![Wind farm diagram](image)

Fig. 16. Wind farms of different sizes: (a) one cluster, (b) two clusters and (c) three clusters.

As the FLC-REC cannot achieve obvious SSO damping, its characteristics are not evaluated. The FLC and PI control are respectively applied in the GSC of each cluster. The disturbance that occurred in the simulation was the same as that in Fig. 12. The responses of the wind farm DC voltage under the PI control and FLC-GSC are shown in Fig. 17.

![Wind farm DC voltage dynamics](image)

Fig. 17. Wind farm DC voltage dynamics for a three-phase short-circuit ground fault under different sizes of the wind farm: (a) the DC voltage with the PI control and (b) the DC voltage with the FLC-GSC.

It can be seen from Fig. 17(a) that with the increase of the clusters (the capacity) under the PI control, the SSO becomes more serious. This conclusion was also verified in [6], [7], [11] by the eigenvalue and impedance analysis methods. When there are three clusters in the wind farm with the PI control, the DDWFV cannot recover to the stable state within 2 s. In contrast, Fig. 17(b) shows that the FLC-GSC stabilizes the oscillation within 0.76 s under different sizes of the wind farm.

With the increase of the clusters (the capacity), the number of the GSCs increases, and then, the nonlinear parts of the GSCs increase, which affects the performance of the DDWFV with the PI control. However, as the FLC-GSC linearizes the GSCs, its performance is less affected by the nonlinear parts. Thus, it can be seen from Fig. 17(b) that the FLC-GSC is less affected by the size of the wind farm. The quantitative comparisons of the overshoots and settling time under different sizes of the wind farm are listed in Table IV, where one, two, and three denote one cluster, two clusters, and three clusters, respectively. It is observed in Table IV that the overshoots and settling time under the FLC-GSC are smaller than those under the PI control. With the increase of the clusters, the settling time of the system with the PI control increases, whereas the settling time is almost unchanged with the FLC-GSC.

### Table III

<table>
<thead>
<tr>
<th>Controllers</th>
<th>Eigenvvalues</th>
<th>Overshoots</th>
<th>Settling time (s)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>5 kV</td>
<td>5.5 kV</td>
<td>6 kV</td>
</tr>
<tr>
<td>PI</td>
<td>-0.3887±26.5017</td>
<td>-0.4194±28.1157</td>
<td>-0.4522±30.0688</td>
</tr>
<tr>
<td>FLC-REC</td>
<td>-0.4478±25.8347</td>
<td>-0.4859±27.4088</td>
<td>-0.5301±32.3107</td>
</tr>
<tr>
<td>FLC-GSC</td>
<td>-5.1770±45.0260</td>
<td>-5.2211±45.0148</td>
<td>-5.2250±44.9020</td>
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</tbody>
</table>

### Table IV

<table>
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<tr>
<th>Controllers</th>
<th>Overshoots</th>
<th>Settling time (s)</th>
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</thead>
<tbody>
<tr>
<td></td>
<td>One</td>
<td>Two</td>
</tr>
<tr>
<td>PI</td>
<td>5.14%</td>
<td>5.34%</td>
</tr>
<tr>
<td>FLC-GSC</td>
<td>3.38%</td>
<td>1.86%</td>
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</tbody>
</table>

Authorized licensed use limited to: Aalborg Universitetsbibliotek. Downloaded on October 01, 2020 at 13:00:50 UTC from IEEE Xplore. Restrictions apply.
B. Evaluation of Step Response Characteristics

The system structure in this case is shown in Fig. 1, and the parameters are shown in Appendix A (the wind speed is set to 8 m/s). The stable operating point of the GSC is operated as $u_{DC} = 5 \text{kV}$ and $i_{dq} = 0 \text{kA}$. At $t = 2 \text{s}$, $u_{DC}$ is changed from 5 kV to 6 kV while $i_{dq}$ keeps constant. Fig. 18 illustrates the responses of the PI control and FLC-GSC to a step change in the DC voltage of the wind farm.

![DC voltage comparison](image1)

![q-axis current comparison](image2)

Fig. 18. Performance comparison of the controllers for a step change in the wind farm DC voltage at 2 s.

It can be seen from Fig. 18 that for the PI control, the settling time of $u_{DC}$ is 1.021 s and the overshoot is $(6.352 - 6)/6 = 5.87\%$. For the FLC-GSC, the settling time of $u_{DC}$ is 0.682 s, and the overshoot is $(6.201 - 6)/6 = 3.35\%$. Therefore, both the settling time and overshoot under the FLC-GSC are less than those under the PI control. In addition, an obvious fluctuation appears in $i_{dq}$ under the PI control, whereas the fluctuation under the FLC-GSC is nearly suppressed to zero. This implies that the FLC-GSC achieves a nearly complete decoupling control between each control objective. Similar conclusions can be drawn when $i_{dq}$ is changed from 0 kA to 1 kA at 2 s, as shown in Fig. 19. The better decoupling of the wind farm DC voltage and q-axis current is due to that the FLC realizes the linearization of the control input and output variables of the nonlinear system, thereby realizing the decoupling control between the control input and output variables [42].

C. Evaluation of Robustness

In this section, the robustness of the FLC and FLSMC is evaluated through PSCAD/EMTDC simulations under the conditions of parameter uncertainties and external disturbances. The FLC and FLSMC are applied in the GSC, respectively. The wind speed is set to 7 m/s.

1) Parameter Uncertainty: It can be known from (16) and (25) that the values of the DC capacitor ($C$) and filter inductance ($L_q$) are included in the control laws of the FLC-GSC and FLSMC. Due to measurement errors, the values of $C$ and $L_q$ may deviate from the actual values. This implies that the measured values of $C$ and $L_q$ in the controllers are uncertain. To evaluate the robust performance of the FLSMC subject to parametric uncertainties, the values of $C$ and $L_q$ in the controllers are set to 50% and 100% of the actual values, respectively. The disturbance that occurred in the simulation was the same as that in Fig. 12. The system transient responses under the FLC-GSC and FLSMC are shown in Figs. 20 and 21.

![DC voltage response](image3)

![PCC voltage response](image4)

Fig. 20. System transient responses of the FLC-GSC when the measured $C$ and $L_q$ in the controllers are set to 50% and 100% of the actual values: (a) DC voltage and (b) PCC voltage.

![DC voltage response](image5)

![PCC voltage response](image6)

Fig. 21. System transient responses of the FLSMC when the measured $C$ and $L_q$ in the controllers are set to 50% and 100% of the actual values: (a) DC voltage and (b) PCC voltage.
As illustrated in Figs. 20 and 21, the wind farm DC voltage and PCC voltage under the FLC-GSC become unstable due to the parameter uncertainties in $C$ and $L_g$. However, the variables under the FLSMC still remain stable when the measured values in the controllers are set to 50% of the actual values, though with slower response speed and higher overshoots compared with the normal operating condition.

2) Short Circuit Fault: The disturbance that occurred in the simulation was the same as that in Fig. 12. Under 7 m/s wind speed, the responses of the wind farm DC voltage and PCC voltage under the FLC-GSC and FLSMC are shown in Fig. 22.

**Fig. 22.** System transient responses of FLC-GSC and FLSMC for three-phase short-circuit ground fault at 2 s: (a) DC voltage and (b) PCC voltage.

It can be seen from Fig. 22 that compared with the FLC-GSC, the transient fluctuation under the FLSMC is smaller, and then the impact of transient fluctuations on the system is slight. Thus, the FLSMC presents stronger robustness against external disturbances than the FLC-GSC. According to the wind farm DC voltages of Figs. 20-22, the quantitative comparisons of the robustness under the conditions of parameter uncertainties and external disturbances are summarized in Table V.

### D. Comparison with SSDC Method

To show that the FLSMC is superior to traditional SSDC methods, the performances of the FLSMC are compared with the SSDC in [16]. The SSDC is composed of a bandpass filter, a compensator, and a limiter, which is shown in Fig. 23. It can be seen from Fig. 23 that locally available variable $u_{DC}$ is used as input signal since it has a great influence on the SSO mode (see Fig. 11). The output signal of SSDC ($i_{SSDC}$) is added to the inner loop of DC voltage control. The bandpass filter is used to pick out the concerned sub-synchronous signal and avoid interference with the normal control function of GSC. The compensator flexibly adjusts the magnitude and phase of the input signal to achieve better control performance, and it is composed of phase shifters and a gain, as shown in (33).

**Fig. 23.** Structure of the SSDC.

$$T(s) = G(s) = \frac{sT_1 + 1}{sT_2 + 1}$$

(33)

Based on the parameters in Appendix D, the FLSMC and SSDC are applied in the GSC, respectively. The disturbance that occurred in the simulation was the same as that in Fig. 12. Under the condition of 7 m/s wind speed, the responses of the wind farm DC voltage and PCC voltage under the PI, SSDC, and FLSMC are shown in Fig. 24.

**Fig. 24.** System transient responses of PI, FLSMC, and SSDC for three-phase short-circuit ground fault at 2 s: (a) DC voltage and (b) PCC voltage.

It can be seen from Fig. 24 that the FLSMC and SSDC both improve the SSO compared with the PI, but the FLSMC shows better SSO mitigation performance and robustness over the SSDC. The main reason is that the FLSMC eliminates the nonlinearities of the GSC and REC, thus presenting better large-signal stability. Meanwhile, the SMC improves the robustness of the DDWFV.
VII. CONCLUSION

In this paper, the feedback linearization theory is applied to mitigate the SSO of the DDWFV. The GSC of the wind farm and REC of the VSC-HVDC are partially linearized. Additionally, the SMC is combined with the feedback linearization theory to improve the robustness, and the stability and robustness of the FLSMC are theoretically verified. Finally, the effectiveness and superiority of the FLSMC are verified by an eigenvalue analysis and time-domain simulations. The main conclusions drawn from this paper are summarized as:

1) Compared with the PI control, the FLC eliminates the non-linearities of the GSC and REC, thus presenting better SSO mitigation performances under various operating conditions. Although the FLC increases the algebraic computation, certain d- and q-axis PI control loops are cancelled, thus reducing the difficulty of PI parameters tuning.

2) When comparing with the PI control, the FLC exhibits better decoupling control characteristics due to that the FLC can realize the linearization of the control input and output variables of the nonlinear system, thereby realizing the decoupling control between the control input and output variables.

3) In contrast to applying the FLC in the REC, using the FLC in the GSC shows better SSO mitigation characteristics under various operating conditions.

4) Compared with the FLC and SSDC, the FLSMC shows stronger robustness against parameter uncertainties and external disturbances, which reduces the settling time and overshoots of wind farm DC voltage and PCC voltage. Then, a smaller and cheaper DC capacitor can be used in the FLSMC to maintain the DC voltage level.

APPENDIX A

<table>
<thead>
<tr>
<th>TABLE A1</th>
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<td>PARAMETERS OF EQUIVALENT DIRECT-DRIVE WIND FARM</td>
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<tr>
<td>Rated voltage (kV)</td>
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<tr>
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<tr>
<td>Turn ratio k₂:1 (kV/kV)</td>
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</table>

APPENDIX B

\[
\begin{align*}
\mathbf{f}_x(x) &= \begin{bmatrix}
\frac{i_{dc1}}{C} - \frac{1}{L_c} u_{dc1} + \frac{1}{L_c} u_{dc2} \\
\frac{1}{L_c} u_{dc2} - \frac{1}{L_c} u_{dc1}
\end{bmatrix}, \quad \mathbf{g}_x(x) = \begin{bmatrix}
\frac{3i_{dc1}}{2C_{dc}} - \frac{3i_{dc2}}{2C_{dc}} \\
0 - \frac{1}{L_c}
\end{bmatrix}
\end{align*}
\]  

\[
\begin{align*}
\mathbf{f}_i(x) &= \begin{bmatrix}
\frac{1}{C_i} u_{ci} - \frac{1}{C_i} u_{ci} + \frac{1}{C_i} u_{ci} \\
\frac{1}{C_i} u_{ci} - \frac{1}{C_i} u_{ci}
\end{bmatrix}, \quad \mathbf{g}_i(x) = \begin{bmatrix}
0 - \frac{1}{L_c} \\
\frac{3i_{dc1}}{2C_{dcu_{dc1}}} - \frac{3i_{dc2}}{2C_{dcu_{dc2}}}
\end{bmatrix}
\end{align*}
\]  

APPENDIX C

To examine the internal dynamics stability of the GSC and REC, the coordinate transformation needs to be constructed for \( z_{wi} \) and \( z_{c} \) to satisfy 1) the Jacobian matrix is nonsingular, and 2) the following assumption holds:

\[
\lim_{t \to \infty} h_i(x) \to 0 \tag{C1}
\]

(C1) indicates that the system dynamics approach zero when time approaches infinity. To satisfy the above conditions, we select \( z_{wi} \) as

\[
z_{wi} = \left[ \frac{-1}{2} L_{w} i_{wi} - \frac{1}{2} L_{w} i_{wi} - \frac{1}{2} C_{w} u_{dc} \right] \tag{C2}
\]

Under the condition of (C1), for the GSC of wind farm, \( h_{wi}(x) = z_{wi} = u_{dc0} = 0 \) and \( h_{wi}(x) = z_{wi} = u_{eq} = 0 \). Therefore, (C2) is simplified as

\[
z_{wi} = \left[ \frac{1}{2} L_{w} i_{wi} \right] \tag{C3}
\]

The dynamic equation of \( z_{wi} \) is obtained from (1) as

\[
z_{wi} = \left[ \frac{1}{2} (u_{wi} - u_{wi}) \right] \tag{C4}
\]

Since \( u_{wi} < u_{wi} \), \( dz_{wi}/dt < 0 \). Thus, (C4) is the internal dynamic equation of the GSC representing a stable system. Similarly, for the REC, \( z_{c} \) is selected as

\[
z_{c} = \left[ -\frac{1}{2} C_{c} u_{c} - \frac{1}{2} C_{c} u_{c} - \frac{1}{2} C_{c} u_{c} \right] \tag{C5}
\]

Under the condition of (C1), \( h_{c}(x) = z_{c} = u_{dc0} = 0 \) and \( h_{c}(x) = z_{c} = u_{eq} = 0 \). Therefore, (C5) is simplified as

\[
z_{c} = \left[ \frac{1}{2} C_{c} u_{c} \right] \tag{C6}
\]

The dynamic equation of \( z_{c} \) is obtained from (2) as

\[
z_{c} = \left[ u_{c} i_{c} - \frac{3}{2} \left( u_{dc} i_{c} + u_{dc} i_{c} \right) \right] \tag{C7}
\]

According to Fig. 1, the following relationship holds when the system is in steady-state operation condition:
It can be known form (C8) that

$$u_{a/d} < \frac{3}{2} (u_{d/d} + u_{a/d})$$  \hspace{1cm} (C9)$$

Thus, it can be known from (C7) that $\frac{dz_{d/d}}{dt} < 0$, which implies that the internal dynamic of the REC is stable. As the internal dynamics of the GSC and REC are both stable, the PFL method is implementable to the GSC and REC of the DDWFV.

### APPENDIX D

#### TABLE D1

**REFERENCE VALUES OF CONTROL SYSTEMS**

<table>
<thead>
<tr>
<th>Modules</th>
<th>Parameters</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>MSC</td>
<td>$d$-axis current reference $i_{\text{ref},d}$ (kA)</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>Speed reference $\omega_{\text{ref}}$ (rad/s)</td>
<td>37.699</td>
</tr>
<tr>
<td>GSC</td>
<td>DC voltage reference $u_{\text{ref},d}$ (kV)</td>
<td>5</td>
</tr>
<tr>
<td></td>
<td>$q$-axis current reference $i_{\text{ref},q}$ (kA)</td>
<td>0</td>
</tr>
<tr>
<td>REC</td>
<td>$d$-axis voltage reference $u_{\text{ref},d}$ (kV)</td>
<td>89.815</td>
</tr>
<tr>
<td></td>
<td>$q$-axis voltage reference $u_{\text{ref},q}$ (kV)</td>
<td>0</td>
</tr>
</tbody>
</table>

#### TABLE D2

**PARAMETERS OF PI CONTROLLERS**

<table>
<thead>
<tr>
<th>Modules</th>
<th>Parameters</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>PLL</td>
<td>Proportional $k_{p,\text{pl}}$, integral $k_{i,\text{pl}}$</td>
<td>5.9</td>
</tr>
<tr>
<td>MSC</td>
<td>$d$-axis voltage control outer loop coefficient (proportional $k_{p,d}$, integral $k_{i,d}$)</td>
<td>0.0029, 100</td>
</tr>
<tr>
<td></td>
<td>$q$-axis voltage control outer loop coefficient (proportional $k_{p,q}$, integral $k_{i,q}$)</td>
<td>0.0029, 100</td>
</tr>
<tr>
<td>GSC</td>
<td>$d$-axis voltage control inner loop coefficient (proportional $k_{p,d}$, integral $k_{i,d}$)</td>
<td>2.5, 10000</td>
</tr>
<tr>
<td></td>
<td>$q$-axis voltage control inner loop coefficient (proportional $k_{p,q}$, integral $k_{i,q}$)</td>
<td>2.5, 10000</td>
</tr>
</tbody>
</table>

#### TABLE D3

**PARAMETERS OF FLC**

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>$k_{q1}$, $k_{q2}$</td>
<td>350, 2000</td>
</tr>
<tr>
<td>$k_{p1}$, $k_{p2}$</td>
<td>350, 2000</td>
</tr>
<tr>
<td>$k_{q1}$, $k_{q2}$</td>
<td>75, 111</td>
</tr>
<tr>
<td>$k_{p1}$, $k_{p2}$</td>
<td>60, 1111</td>
</tr>
</tbody>
</table>

#### TABLE D4

**PARAMETERS OF FLSMC**

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>$C_{\text{dc}}$, $C_{\text{pq}}$</td>
<td>100, 100</td>
</tr>
<tr>
<td>$C_{\text{d}}$, $C_{\text{dq}}$</td>
<td>100, 100</td>
</tr>
</tbody>
</table>

#### TABLE D5

**PARAMETERS OF SSDC**

<table>
<thead>
<tr>
<th>Modules</th>
<th>Parameters</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>4-order Butterworth bandpass filter</td>
<td>Center frequency (Hz)</td>
<td>5.3</td>
</tr>
<tr>
<td></td>
<td>Bandwidth (Hz)</td>
<td>2</td>
</tr>
</tbody>
</table>

### REFERENCES


