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Comparative Study of Symmetrical Controlled Grid-Connected Inverters

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***Abstract*—The asymmetrical dynamics of the conventional synchronous reference frame phase-locked loop (PLL) enhance the complexity of the system and endanger the stability of the grid-connected voltage source converters. In this paper, the admittance modeling of three different symmetrical control strategies, i.e., voltage oriented control with symmetrical PLL, voltage modulated direct power control, and proportional resonant control, are proposed and uniformed based on the same power tracking property. The voltage feedforward and decoupling of the controllers are both considered. Moreover, due to the symmetrical structure of the three control strategies, their admittance models can directly be modeled under $\alpha\beta$ reference frame as single-input single-output systems. In addition, to reveal the similarity between different control methods and emphasize their differences, the admittance models are constructed by using the same framework. The admittance modeling is verified using frequency-scan, and the control performance is tested using MATLAB/Simulink Simscape Power System and experimental prototype. Finally, the stability criterion based on proposed admittance modeling is verified by simulation results carried out in real-time simulation platform RTLAB from Opal-RT.**

***Index Terms*—Admittance-based analysis, proportional resonant control, voltage oriented control, voltage souce converter (VSC), voltage modulated control**

I. Introduction

POWER electronic devices such as voltage source converters (VSCs) have been nowadays widely applied in the modern power system [1]. However, the wideband interactions between the VSC and grid impedance bring potential instability problem to the system and have been brought to the forefront of the academics [2]–[9]. In order to address the challenge, multiple analysis methods have been proposed. Among these methods, the admittance analysis successfully depicts the frequency response of the grid-connected VSC system [10]. Moreover, by equivalenting the system with a Thevenin/Norton equivalent circuit, the admittance based analysis can easily identify the system stability through the Nyquist stability criterion [4], [6]–[8].

It has been proved that the control strategy of the VSC plays a decisive role in the harmonic interaction between the VSC and power grid [6]. The most typical control strategy of VSC is called voltage oriented control (VOC), which requires a phase-locked loop (PLL) for the grid synchronization [8]. In conventional synchronous reference frame PLL (SRF-PLL), the phase signal of the grid voltage is extracted only by its q-axis component. Therefore, it is inherently asymmetric, which brings a frequency-coupling phenomenon and greatly enhances the complexity of the admittance model [7], [8]. Usually, due to the inherent asymmetrical characteristic, the admittance of the inverter using VOC has to be modeled in the form of a matrix as a multi-input multi-output (MIMO) system [11]. The impedance matrix can be established in the dq reference frame for simplicity. The stability analysis based on dq frame model requires using generalized Nyquist stability criterion [6]. Moreover, the analysis based on dq frame model is not easy to interpret the mechanism of frequency-coupling [12]. The $\alpha\beta$ admittance modeling method based on harmonic linearization is proposed [12], [13]. The $\alpha\beta$ frame admittance modeling approach can readily describe the system in the same coordinate system, thus has a more intuitive physical meaning [14], [15]. The neglecting of cross-coupled components in the $\alpha\beta$ modeling leads to inaccuracy of the stability analysis [7]. To describe the generation of coupled components, a complex equivalence method is proposed in [7], which also unifies the dq frame and $\alpha\beta$ frame admittance modeling methods. The coupling is regarded as a conjugate of the corresponding vector under the dq reference frame.

A symmetrical PLL (S-PLL) is proposed in [8]. Compared to SRF-PLL, the S-PLL also tracks the voltage magnitude using a defined imaginary part of the voltage phase signal, thereby it exhibits symmetrical characteristics and eliminates the generation of frequency-coupling components. The S-PLL based VOC (S-VOC) can also be applied into the doubly-fed induction generator for impedance characteristics reshaping and improvement of grid-integration stability [16].

The proportional-resonant (PR) control is also a typical control strategy designed for grid-connected inverters [17]–[21]. Using PR control can reduce the system complexity since it avoids PLL for grid synchronization [21]. It is constructed directly under the $\alpha\beta$ reference frame with a symmetrical structure and can realize zero ac tracking error. However, the control performance cannot be guaranteed once the grid voltage frequency is deviated [18]. A stability assessment of

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the grid-connected VSC using PR is proposed in [22]. The paper focuses on the direct-voltage control, and the power control is not included.

Direct power control (DPC) is also a widely-studied VSC control strategy, which successfully avoids the use of PLL [23]–[27]. Conventional look-up-table (LUT) based DPC has an nonfixed switching frequency, which deteriorates its steady-state control performance. In recent decades, improved DPC methods such as voltage modulated (VM)-DPC and sliding-mode control (SMC) [24] are proposed and widely applied [25]. They obtain constant switching frequency by using space vector modulation (SVM) techniques and thus have better steady-state performance compared to the conventional DPC methods. The VM-DPC is recently proved to be effective and widely applicable [26], [28], [29]. It improves the performance of VSC compared with SMC and LUT-DPC. In addition, the VM-DPC maintains a satisfactory steady-state performance similar to that of conventional VOC [25]. The VM-DPC is a symmetrical control method, and its admittance model can be linearized and built directly under $\alpha\beta$ frame [30]. Note the VM-DPC still brings frequency coupling phenomenon due to the inherent power integration. However, the coupling can be restrained by using a band-pass filter.

Note that it is normally difficult to compare the admittance characteristics of different control methods fairly. Partly because the control parameters are always selected based on a specific control structure to meet the best performance, i.e., the best input reference tracking property. Therefore, one of the purposes of this paper is to unify the power tracking properties of three different symmetrical controllers, i.e., S-VOC, PR control, and VM-DPC. Among three control methods, S-VOC and PR are both typical linear control methods, and the VM-DPC is a DPC method using SVM. Then, based on the same power tracking property, the admittance characteristics of the three control strategies are presented and compared in detail. The innovations can be summarized as follows,

1) The power tracking properties of three different symmetrical control methods are described by using a unified second-order transfer function. The unified control performance of three controllers is verified using simulation carried out in MATLAB/Simulink Simscape Electrical and experimental prototype.
2) The admittance models of three symmetrical controllers are summarized. To further reveal interconnection and the difference between the three control methods, the proposed admittances are presented as a unified basic admittance plus additional virtual admittance determined by different controllers. The admittances are modeled directly under the $\alpha\beta$ reference frame with a clear physical meaning.
3) The effects of the three symmetrical control structures on the harmonic response and stability of grid-connected inverters are compared. The accuracy of the Nyquist stability analysis based on the proposed admittance modelling method is verified using real-time simulation platform RTLAB from Opal-RT.

The rest of the paper is organized as follows: an overview of the admittance analysis of VSC using conventional VOC and frequency-coupling phenomenon caused by SRF-PLL are discussed in Section II. The admittance modeling of three different symmetrical control strategies is presented in Section III. A unified performance analysis is presented in Section IV. In Section V, the admittance modeling methods of three symmetrical controllers are presented. Section VI presents the simulation results and finally, the conclusions are given in Section VI.

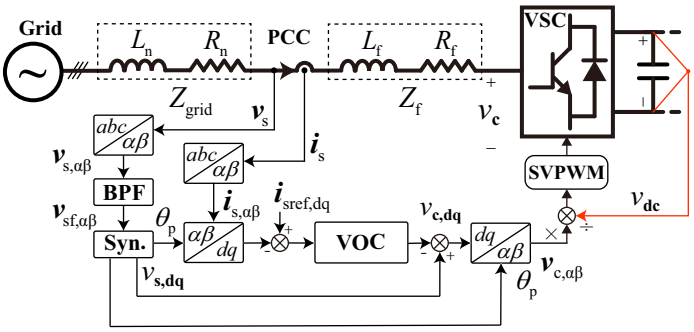

Fig. 1. Diagram of grid-connected voltage source converter (VSC) using voltage oriented control (VOC).

II. Overview of Admittance Analysis of VOC Based Grid-connected Inverters

To better introduce the concept of symmetrical control, an overview of the admittance analysis of the conventional VOC is introduced. Moreover, the frequency-coupling phenomenon caused by SRF-PLL is explained using the $\alpha\beta$ frame admittance model. This study focuses on the inner-loop dynamics, and the dc voltage is considered to be constant. Moreover, the dynamics of PWM is neglected in the admittance modeling. The modulation is considered to be capable of generating output as an ideal voltage waveform.

A. Voltage Oriented Control of VSC

A typical grid-connected VSC is presented in Fig.1, where the complex space vectors of the voltage and current can be denoted by the sum of their fundamental components and harmonic components using boldface letters, e.g., $\boldsymbol{v}_s = \boldsymbol{v}_{s1} + \tilde{\boldsymbol{v}}_{sh}$, $\boldsymbol{i}_s = \boldsymbol{i}_{s1} + \tilde{\boldsymbol{i}}_{sh}$. Take the point of common coupling (PCC) voltage $\boldsymbol{v}_s$ as an example, the space vector can either be presented under $\alpha\beta$ or under dq frame as,

$$\begin{aligned} \boldsymbol{v}_s &= \underbrace{v_{s1,\alpha} + jv_{s1,\beta}}_{\boldsymbol{v}_{s1,\alpha\beta}} + \underbrace{\tilde{v}_{sh,\alpha} + j\tilde{v}_{sh,\beta}}_{\tilde{\boldsymbol{v}}_{sh,\alpha\beta}} \\ \boldsymbol{v}_s &= \underbrace{v_{s1,d} + jv_{s1,q}}_{\boldsymbol{v}_{s1,dq}} + \underbrace{\tilde{v}_{sh,d} + j\tilde{v}_{sh,q}}_{\tilde{\boldsymbol{v}}_{sh,dq}} \end{aligned} \tag{1}$$

The basic equation of the grid-connected VSC is,

$$\boldsymbol{v}_s - \boldsymbol{v}_c = R_f \boldsymbol{i}_s + L_f \frac{d\boldsymbol{i}_s}{dt} \tag{2}$$

where $\boldsymbol{v}_c$ and $\boldsymbol{i}_s$ are the VSC voltage and output current, R_f and L_f are the resistance and inductance of VSC filter. Using

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(2), the fundamental components of the grid-connected VSC can be also written in s' domain under dq frame as,

$$\boldsymbol{v}_{\mathrm{s1,dq}} - \boldsymbol{v}_{\mathrm{c1,dq}} = [R_{\mathrm{f}} + (s' + j\omega_{\mathrm{s}})L_{\mathrm{f}}]\boldsymbol{i}_{\mathrm{s1,dq}} \tag{3}$$

where $\boldsymbol{v}_{\mathrm{c1,dq}}$, $\boldsymbol{i}_{\mathrm{s1,dq}}$ are the fundamental space vectors of the VSC voltage and the current under dq frame, respectively. ω_{s} denotes the fundamental frequency of the grid voltage. The relation can be translated into the stationary reference frame using $s = s' + j\omega_{\mathrm{s}}$, and the equation of the harmonic components can be expressed in $\alpha\beta$ frame as,

$$\tilde{\boldsymbol{v}}_{\mathrm{sh},\alpha\beta} - \tilde{\boldsymbol{v}}_{\mathrm{ch},\alpha\beta} = (R_{\mathrm{f}} + sL_{\mathrm{f}})\tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta}, \tag{4}$$

where $\tilde{\boldsymbol{v}}_{\mathrm{ch},\alpha\beta}$, $\tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta}$ denote the harmonic components of $\boldsymbol{v}_{\mathrm{c},\alpha\beta}$, and $\boldsymbol{i}_{\mathrm{s},\alpha\beta}$, respectively. The filter admittance is denoted by $Y_{\mathrm{f}} = 1/Z_{\mathrm{f}}(s) = 1/(R_{\mathrm{f}} + sL_{\mathrm{f}})$.

The voltage harmonics $\boldsymbol{v}_{\mathrm{sh}}$ at the PCC may deteriorate the performance of the PLL and VSC controller. Therefore, a band-pass filter (BPF) is utilized in this paper to block the harmonics and dc components in the measured PCC voltage. The transfer function of the BPF can be expressed in the $\alpha\beta$ frame as follows:

$$G_{\mathrm{fil}}(s) = \frac{2\omega_{\mathrm{c}}s}{s^2 + 2\omega_{\mathrm{c}}s + \omega_{\mathrm{s}}^2}, \tag{5}$$

where $\omega_{\mathrm{c}} = \zeta_{\mathrm{f}}\omega_{\mathrm{s}}$ is the resonance bandwidth of the BPF, and ζ_{f} is the damping ratio of the BPF. Note that $G_{\mathrm{fil}}(j\omega_{\mathrm{s}}) = 1$. Therefore, the voltage input of the controller can be expressed under $\alpha\beta$ and dq reference frames, respectively, as follows,

$$\begin{aligned} \boldsymbol{v}_{\mathrm{sf},\alpha\beta} =& \boldsymbol{v}_{\mathrm{s1},\alpha\beta} + \underbrace{G_{\mathrm{fil}}(s)\tilde{\boldsymbol{v}}_{\mathrm{sh},\alpha\beta}}_{\tilde{\boldsymbol{v}}_{\mathrm{shf},\alpha\beta}} \\ \boldsymbol{v}_{\mathrm{sf,dq}} =& \boldsymbol{v}_{\mathrm{s1,dq}} + \underbrace{G_{\mathrm{fil}}(s' + j\omega_{\mathrm{s}})\tilde{\boldsymbol{v}}_{\mathrm{sh,dq}}}_{\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}}, \end{aligned} \tag{6}$$

where $\tilde{\boldsymbol{v}}_{\mathrm{shf},\alpha\beta}$ and $\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}$ denote the filtered voltage input under $\alpha\beta$ and dq reference frames, respectively. Note that the most typical control used in practical VSC is the symmetrically decoupled PI control consisting of current feedforward decoupling and voltage feedforward linearization components. Therefore, the fundamental component of the VSC voltage $\boldsymbol{v}_{\mathrm{c1,dq}}$ can be expressed in terms of the fundamental components in the dq reference frame as,

$$\begin{aligned} \boldsymbol{v}_{\mathrm{c1,dq}} = &- \underbrace{L_{\mathrm{f}}\,(K_{\mathrm{p}} + \frac{K_{\mathrm{i}}}{s'})}_{G_{\mathrm{PI,dq}}(s')}(\boldsymbol{i}_{\mathrm{sref,dq}} - \boldsymbol{i}_{\mathrm{s1,dq}}) \\ & \underbrace{-j\omega_{\mathrm{s}}L_{\mathrm{f}}i_{\mathrm{s1,dq}} + \boldsymbol{v}_{\mathrm{s1,dq}}}_{\text{current/voltage feedforward}} \end{aligned}, \tag{7}$$

where $G_{\mathrm{PI,dq}}(s')$ denotes the basic PI control unit, K_{p} and K_{i} are the PI control parameters. The current references $\boldsymbol{i}_{\mathrm{sref,dq}}$ and the current fundamental component $i_{\mathrm{s1,dq}}$ can be calculated as follows,

$$\boldsymbol{i}_{\mathrm{sref,dq}} = -\frac{2}{3}\frac{\boldsymbol{S}_{\mathrm{sref}}}{|v_{\mathrm{s1}}|}\ \boldsymbol{i}_{\mathrm{s1,dq}} = -\frac{2}{3}\frac{\boldsymbol{S}_{\mathrm{s1}}}{|v_{\mathrm{s1}}|}, \tag{8}$$

where $S_{\mathrm{sref}} = P_{\mathrm{sref}} + jQ_{\mathrm{sref}}$ denotes the reference of output complex power. $S_{\mathrm{s1}} = P_{\mathrm{s1}} + jQ_{\mathrm{s1}} = -\frac{3}{2}[\mathrm{Re}(\boldsymbol{v}_{\mathrm{s1}}\boldsymbol{i}_{\mathrm{s1}}^*) + j\mathrm{Im}(\boldsymbol{v}_{\mathrm{s1}}\boldsymbol{i}_{\mathrm{s1}}^*)]$ represents the fundamental components of the instantaneous complex power. By neglecting the harmonic components and the dynamics of time delay, the power tracking dynamics can be described as a second-order transfer function by substituting (6), (7) into (3),

$$T_{\mathrm{VSC}}^{\mathrm{PI}} = \frac{\boldsymbol{i}_{\mathrm{s1,dq}}}{\boldsymbol{i}_{\mathrm{sref,dq}}} = \frac{\boldsymbol{S}_{\mathrm{s1,dq}}}{\boldsymbol{S}_{\mathrm{sref,dq}}} = \frac{K_{\mathrm{p}}s' + K_{\mathrm{i}}}{s'^2 + (K_{\mathrm{p}} + \frac{R_{\mathrm{f}}}{L_{\mathrm{f}}})s' + K_{\mathrm{i}}}. \tag{9}$$

To ensure the performance of fundamental dc regulation, normally, the control parameters can be chosen based on the natural frequency ω_{n} and damping factor ζ derived from the characteristic polynomial of (9) [31],

$$\begin{cases} K_{\mathrm{i}} = \omega_{\mathrm{n}}^2 \\ K_{\mathrm{p}} = 2\zeta_{\mathrm{n}}\omega_{\mathrm{n}} - \dfrac{R_{\mathrm{f}}}{L_{\mathrm{f}}} \end{cases}. \tag{10}$$

Except for the fundamental components, the voltage output is also affected by the current and voltage harmonics as follows,

$$\tilde{\boldsymbol{v}}_{\mathrm{ch,dq}} = G_{\mathrm{del}}(s)[\underbrace{(L_{\mathrm{f}}G_{\mathrm{PI,dq}}(s') - j\omega_{\mathrm{s}}L_{\mathrm{f}})}_{\mathrm{G_{c,dq}(s')}}\tilde{\boldsymbol{i}}_{\mathrm{sh,dq}} + \tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}], \tag{11}$$

where $G_{\mathrm{c,dq}}(s')$ denotes the influence of PI control with feedforward on VSC voltage harmonics. Note that the time delay dynamics represented by $G_{\mathrm{del}}(s)$ is [7], [22],

$$G_{\mathrm{del}}(s) = e^{-1.5T_{\mathrm{del}}s} \approx \frac{1 - 1.5T_{\mathrm{del}}s}{1 + 1.5T_{\mathrm{del}}s} \tag{12}$$

where T_{del} is the sampling period. Using the frequency translation $s' = s - j\omega_{\mathrm{s}}$, $\tilde{\boldsymbol{v}}_{\mathrm{ch,dq}}$ can also be written under the $\alpha\beta$ frame as,

$$\begin{aligned} \tilde{\boldsymbol{v}}_{\mathrm{ch},\alpha\beta} =& G_{\mathrm{del}}(s)[\underbrace{(L_{\mathrm{f}}G_{\mathrm{PI},\alpha\beta}(s - j\omega_{\mathrm{s}}) - j\omega_{\mathrm{s}}L_{\mathrm{f}})}_{\mathrm{G_{c,\alpha\beta}(s-j\omega_s)}}\tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta} \\ & + \tilde{\boldsymbol{v}}_{\mathrm{shf},\alpha\beta}], \end{aligned} \tag{13}$$

The admittance of VSC with the PI control, denoting its harmonic-domain response, can be derived in the $\alpha\beta$ reference frame by substituting (6) and (13) into (4) as follows,

$$Y_{\mathrm{VSC}}^{\mathrm{PI}} = \frac{\tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta}}{\tilde{\boldsymbol{v}}_{\mathrm{sh},\alpha\beta}} = \frac{Y_{\mathrm{f}}(s)[1 - G_{\mathrm{del}}(s)G_{\mathrm{fil}}(s)]}{1 + G_{\mathrm{del}}(s)Y_{\mathrm{f}}(s)G_{\mathrm{c},\alpha\beta}(s - j\omega_{\mathrm{s}})}. \tag{14}$$

Note that $T_{\mathrm{VSC}}^{\mathrm{PI}}$ and $Y_{\mathrm{VSC}}^{\mathrm{PI}}$ represents the power tracking property and the response to the voltage harmonics, respectively. $Y_{\mathrm{VSC}}^{\mathrm{PI}}$ mainly affects the small-signal stability of the grid-connected VSC under weak-grid conditions. Moreover, it can be found that $Y_{\mathrm{VSC}}^{\mathrm{PI}}$ is only related to the control parameters and has no relation with the system operation point.

B. SRF-PLL and Frequency Coupling Phenomenon

Recent studies revealed that the SRF-PLL affects the harmonic response of the system and introduced frequency coupling phenomenon due to its asymmetrical structure. For the modeling of SRF-PLL, an $\alpha\beta$ frame admittance modeling presented in [7] is used in this section. The input of the PLL is a filtered PCC voltage $\boldsymbol{v}_{\mathrm{sf},\alpha\beta}^{\mathrm{s}}$ under the $\alpha\beta$ reference frame, the dq-transformation using SRF-PLL can be decomposed as follows,

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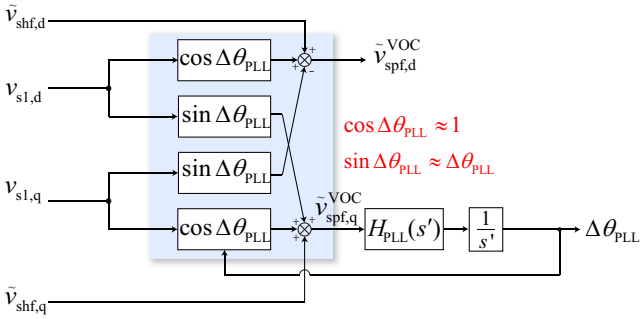

Fig. 2. Small-signal block diagram of the conventional single-reference frame phase-locked-loop (SRF-PLL).

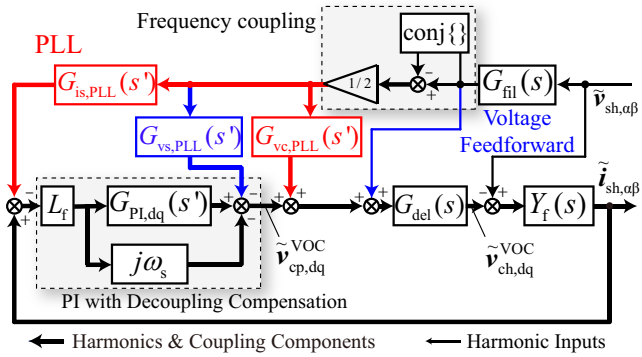

Fig. 3. Small-signal block diagram of grid-connected voltage source converter using SRF-PLL based VOC.

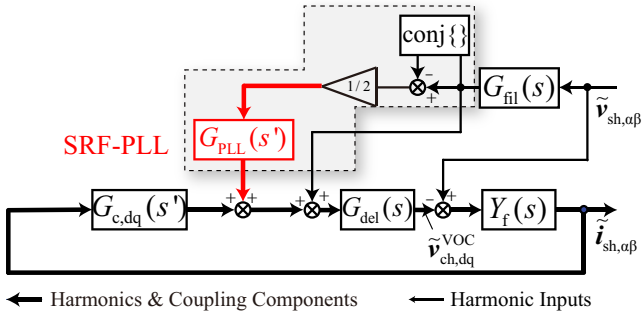

Fig. 4. Simplified small-signal block diagram of grid-connected voltage source converter using SRF-PLL based VOC.

$$\begin{aligned}\boldsymbol{v}^{\mathrm{c}}_{\mathrm{sf,dq}} &= \boldsymbol{v}^{\mathrm{s}}_{\mathrm{sf},\alpha\beta}e^{-j\theta_{\mathrm{p}}} \\ &= \boldsymbol{v}^{\mathrm{s}}_{\mathrm{sf,dq}}e^{-j\Delta\theta_{\mathrm{PLL}}} = (\boldsymbol{v}^{\mathrm{s}}_{\mathrm{s1,dq}} + \tilde{\boldsymbol{v}}^{\mathrm{s}}_{\mathrm{shf,dq}})e^{-j\Delta\theta_{\mathrm{PLL}}} \\ &\approx (\boldsymbol{v}^{\mathrm{s}}_{\mathrm{s1,dq}} + \tilde{\boldsymbol{v}}^{\mathrm{s}}_{\mathrm{shf,dq}})(1 - j\Delta\theta_{\mathrm{PLL}}) \\ &= \boldsymbol{v}^{\mathrm{s}}_{\mathrm{s1,dq}} + \underbrace{\tilde{\boldsymbol{v}}^{\mathrm{s}}_{\mathrm{shf,dq}} - j\boldsymbol{v}^{\mathrm{s}}_{\mathrm{s1,dq}}\Delta\theta_{\mathrm{PLL}}}_{\tilde{\boldsymbol{v}}^{\mathrm{VOC}}_{\mathrm{spf,dq}}} - \underbrace{j\tilde{\boldsymbol{v}}^{\mathrm{s}}_{\mathrm{shf,dq}}\Delta\theta_{\mathrm{PLL}}}_{\text{Negligible}}\end{aligned}, \tag{15}$$

where the superscripts $'s'$ and $'c'$ denote the grid dq reference frame and PLL dq reference frame, respectively. $\theta_{\mathrm{p}} = \omega_{\mathrm{s}}t + \Delta\theta_{\mathrm{PLL}}$ is the detected voltage phase angle. Note that $\boldsymbol{v}^{\mathrm{s}}_{\mathrm{s1,dq}}$ represents the fundamental components of the voltage, under dq reference frame, $\boldsymbol{v}^{\mathrm{s}}_{\mathrm{s1,dq}} = |\boldsymbol{v}_{\mathrm{s1}}|$. The effect of the PLL tracking error to the harmonic components is very small thus it can be neglected, i.e., $\tilde{\boldsymbol{v}}^{\mathrm{s}}_{\mathrm{shf,dq}} \approx \tilde{\boldsymbol{v}}^{\mathrm{c}}_{\mathrm{shf,dq}}$. Therefore, the superscripts $'c'$ and $'s'$ are ignored in the following. $\Delta\theta_{\mathrm{PLL}}$ is the error between the detected voltage phase and actual grid voltage phase value. $\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}\Delta\theta_{\mathrm{PLL}}$ denotes the coupling between voltage harmonics and the error of phase measurement, which is small enough to be neglected. The total variation of the measured voltage can be defined as $\tilde{\boldsymbol{v}}^{\mathrm{VOC}}_{\mathrm{spf,dq}}$. Note that $\tilde{\boldsymbol{v}}^{\mathrm{VOC}}_{\mathrm{spf,dq}}$ can be separately presented by its d and q axis components as follows,

$$\begin{cases}\tilde{v}^{\mathrm{VOC}}_{\mathrm{spf,d}} = \tilde{v}_{\mathrm{shf,d}} \\ \tilde{v}^{\mathrm{VOC}}_{\mathrm{spf,q}} = \tilde{v}_{\mathrm{shf,q}} - |\boldsymbol{v}_{\mathrm{s1}}|\Delta\theta_{\mathrm{PLL}}\end{cases}. \tag{16}$$

The SRF-PLL is designed based on the variation of the q-axis voltage component $\tilde{v}^{\mathrm{VOC}}_{\mathrm{spf,q}}$,

$$\Delta\theta_{\mathrm{PLL}} = \frac{1}{s'}\underbrace{(K_{\mathrm{PLL,p}} + \frac{K_{\mathrm{PLL,i}}}{s'})}_{H_{\mathrm{PLL}}(s')}\tilde{v}^{\mathrm{VOC}}_{\mathrm{spf,q}}. \tag{17}$$

where $K_{\mathrm{PLL,p}}$ and $K_{\mathrm{PLL,i}}$ denote the parameters of SRF-PLL. By substituting (17) into (16), the following is obtained,

$$\Delta\theta_{\mathrm{PLL}} = \underbrace{\frac{H_{\mathrm{PLL}}(s')}{s' + |\boldsymbol{v}_{\mathrm{s1}}|H_{\mathrm{PLL}}(s')}}_{T_{\mathrm{PLL}}(s')}\tilde{v}_{\mathrm{shf,q}}. \tag{18}$$

The small-signal block diagram of the conventional SRF-PLL is shown in Fig. 2. Note that the main reason of the frequency-coupling caused by SRF-PLL is its inherent unsymmetrical structure. The one-dimensional voltage input under q axis $v_{\mathrm{shf,q}}$ can be seen as the sum of two conjugated vectors, i.e., $\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}$, and $\tilde{\boldsymbol{v}}^{*}_{\mathrm{shf,dq}}$ [7], [8]. Therefore, the phase variation $\Delta\theta_{\mathrm{PLL}}$ can be expressed as,

$$\Delta\theta_{\mathrm{PLL}} = T_{\mathrm{PLL}}(s')\frac{\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}} - \tilde{\boldsymbol{v}}^{*}_{\mathrm{shf,dq}}}{2j}. \tag{19}$$

Based on the derivation similar to (15), the current harmonic components of the control signal is depicted as follows,

$$\tilde{\boldsymbol{i}}^{\mathrm{VOC}}_{\mathrm{sp,dq}} = \tilde{\boldsymbol{i}}_{\mathrm{sh,dq}} - j\boldsymbol{i}_{\mathrm{s1,dq}}\Delta\theta_{\mathrm{PLL}}. \tag{20}$$

The controlled VSC voltage is also affected by the PLL during the inversion dq transform, which can be derived as

$$\tilde{\boldsymbol{v}}^{\mathrm{VOC}}_{\mathrm{ch,dq}} = \tilde{\boldsymbol{v}}_{\mathrm{cp,dq}} + j\boldsymbol{v}_{\mathrm{c1,dq}}\Delta\theta_{\mathrm{PLL}}. \tag{21}$$

Therefore, the harmonics of the components can be derived by substituting (19) into (15), (20) and (21) given as,

$$\begin{aligned}\tilde{\boldsymbol{v}}^{\mathrm{VOC}}_{\mathrm{spf,dq}} &= \tilde{\boldsymbol{v}}_{\mathrm{shf,dq}} - \underbrace{T_{\mathrm{PLL}}(s')\boldsymbol{v}_{\mathrm{s1,dq}}}_{G_{\mathrm{vs,pll}}(s')}\frac{\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}} - \tilde{\boldsymbol{v}}^{*}_{\mathrm{shf,dq}}}{2} \\ \tilde{\boldsymbol{i}}^{\mathrm{VOC}}_{\mathrm{sp,dq}} &= \tilde{\boldsymbol{i}}_{\mathrm{sh,dq}} - \underbrace{T_{\mathrm{PLL}}(s')\boldsymbol{i}_{\mathrm{s1,dq}}}_{G_{\mathrm{is,pll}}(s')}\frac{\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}} - \tilde{\boldsymbol{v}}^{*}_{\mathrm{shf,dq}}}{2} \\ \tilde{\boldsymbol{v}}^{\mathrm{VOC}}_{\mathrm{ch,dq}} &= G_{\mathrm{del}}(s)[\tilde{\boldsymbol{v}}^{\mathrm{VOC}}_{\mathrm{cp,dq}} + \underbrace{T_{\mathrm{PLL}}(s')\tilde{\boldsymbol{v}}_{\mathrm{c1,dq}}}_{G_{\mathrm{vc,pll}}(s')}\frac{\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}} - \tilde{\boldsymbol{v}}^{*}_{\mathrm{shf,dq}}}{2}]\end{aligned}, \tag{22}$$

where $G_{\mathrm{is,pll}}(s')$ denotes the effect of asymmetrical SRF-PLL on the current inputs. $G_{\mathrm{vc,pll}}(s')$ represents the influence of

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the SRF-PLL on the dq inverse transformation of the VSC output voltage and $G_{\mathrm{vs,pll}}(s')$ express the effect of the SRF-PLL on voltage feedforward components. For simplicity, the harmonics of the VSC voltage is considered to be equal to the output signal $\boldsymbol{v}_{\mathrm{ch,dq}}^{\mathrm{PLL}}$. Therefore, the effect of VOC and PLL on the control can be expressed as,

$$\begin{aligned}\tilde{\boldsymbol{v}}_{\mathrm{ch,dq}}^{\mathrm{VOC}} =& G_{\mathrm{del}}(s)[G_{\mathrm{c,dq}}(s')\tilde{i}_{\mathrm{sp,dq}}^{\mathrm{VOC}} + \tilde{\boldsymbol{v}}_{\mathrm{spf,dq}}^{\mathrm{VOC}} \\ &+ G_{\mathrm{vc,pll}}(s')\frac{\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}} - \tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}^{*}}{2}].\end{aligned} \tag{23}$$

The small-signal block diagram of the conventional VOC is shown in Fig. 3. Furthermore, (23) can be simplified as follows,

$$\tilde{\boldsymbol{v}}_{\mathrm{ch,dq}}^{\mathrm{VOC}} = \tilde{\boldsymbol{v}}_{\mathrm{ch1,dq}}^{\mathrm{VOC}} + \tilde{\boldsymbol{v}}_{\mathrm{ch2,dq}}^{\mathrm{VOC}} \tag{24}$$

where $\tilde{\boldsymbol{v}}_{\mathrm{ch1,dq}}^{\mathrm{VOC}}$ and $\tilde{\boldsymbol{v}}_{\mathrm{ch2,dq}}^{\mathrm{VOC}}$ denote the positive-sequence components and coupling components of the controlled VSC voltage, separately,

$$\begin{cases}\tilde{\boldsymbol{v}}_{\mathrm{ch1,dq}}^{\mathrm{VOC}} =& G_{\mathrm{del}}(s)(G_{\mathrm{c,dq}}(s')\tilde{\boldsymbol{i}}_{\mathrm{sh,dq}} \\ & + \tilde{\boldsymbol{v}}_{\mathrm{shf,dq}} + \frac{1}{2}G_{\mathrm{PLL}}(s')\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}) \\ \tilde{\boldsymbol{v}}_{\mathrm{ch2,dq}}^{\mathrm{VOC}} =& \frac{1}{2}G_{\mathrm{del}}(s)G_{\mathrm{PLL}}(s')\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}^{*}\end{cases} \tag{25}$$

where $G_{\mathrm{PLL}}(s')$ denotes the effect of PLL on the system harmonic response,

$$G_{\mathrm{PLL}}(s') = -G_{\mathrm{c,dq}}(s')G_{\mathrm{is,pll}}(s') - G_{\mathrm{vs,pll}}(s') + G_{\mathrm{vc,pll}}(s'). \tag{26}$$

Using (24), the small-signal block diagram of VOC can be simplified as shown in Fig. 4. Due to the frequency-coupling effect caused by the SRF-PLL, the harmonics disturbance v_{sh} at PCC generates two components with different frequencies, i.e., $\tilde{\boldsymbol{v}}_{\mathrm{ch1,dq}}^{\mathrm{VOC}}$ at $\omega_{\mathrm{s}} - \omega_{\mathrm{h}}$ and $\tilde{\boldsymbol{v}}_{\mathrm{ch2,dq}}^{\mathrm{VOC}}$ at $\omega_{\mathrm{h}} - \omega_{\mathrm{s}}$, in the VSC output voltage. The generation process of the current coupling-component $\tilde{\boldsymbol{i}}_{\mathrm{sh2,dq}}$ under dq reference frame, i.e., the coupling components under $\alpha\beta$ reference frame $\tilde{\boldsymbol{i}}_{\mathrm{sh2},\alpha\beta}$ with frequency $2\omega_{\mathrm{s}} - \omega_{\mathrm{h}}$ can be summarized as follows,

$$\tilde{\boldsymbol{v}}_{\mathrm{sh},\alpha\beta} \rightarrow \tilde{\boldsymbol{v}}_{\mathrm{sh,dq}}\begin{cases}\tilde{\boldsymbol{v}}_{\mathrm{ch1,dq}}^{\mathrm{VOC}}|_{\omega_h-\omega_s} \rightarrow \tilde{\boldsymbol{i}}_{\mathrm{sh1},\alpha\beta}|_{\omega_h} \\ \tilde{\boldsymbol{v}}_{\mathrm{ch2,dq}}^{\mathrm{VOC}}|_{\omega_s-\omega_h} \rightarrow \tilde{\boldsymbol{i}}_{\mathrm{sh2},\alpha\beta}|_{2\omega_s-\omega_h}.\end{cases} \tag{27}$$

III. Symmetrical Control

In this section, the three different symmetrical control methods, i.e., S-VOC, VM-DPC, and PR, are introduced and compared.

A. VOC Using Symmetrical PLL (S-VOC)

The unsymmetrical PLL can be transformed into a symmetrical one by replacing the original phase angle with a complex one $\boldsymbol{\theta} = \theta_{\mathrm{d}} + j\theta_{\mathrm{q}}$ [8]. Using the complex phase angle and the approximation similar to (15), the dq-transformation carried out by the symmetrical PLL can be expressed as,

$$\begin{aligned}\boldsymbol{v}_{\mathrm{s,dq}} =& (|\boldsymbol{v}_{\mathrm{s1}}| + \tilde{\boldsymbol{v}}_{\mathrm{shf,dq}})e^{-j\Delta\boldsymbol{\theta}_{\mathrm{PLL}}} \\ \approx& (|\boldsymbol{v}_{\mathrm{s1}}| + \underbrace{\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}} - |\boldsymbol{v}_{\mathrm{s1}}|\Delta\boldsymbol{\theta}_{\mathrm{PLL}}}_{\tilde{\boldsymbol{v}}_{\mathrm{spf,dq}}^{\mathrm{SVOC}}}),\end{aligned} \tag{28}$$

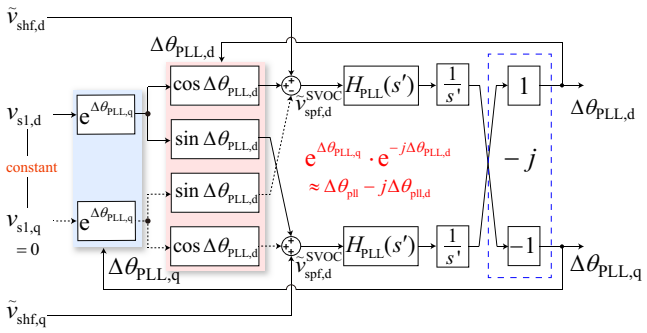

Fig. 5. Small-signal block diagram of the symmetrical phase-locked loop (PLL).

where the phase and magnitude oscillation caused by the voltage harmonics can be approximated as $e^{-j\Delta\boldsymbol{\theta}_{\mathrm{PLL}}} \approx \Delta\theta_{\mathrm{PLL,q}} - j\Delta\theta_{\mathrm{PLL,d}}$. $\Delta\theta_{\mathrm{PLL,d}}$ represents the phase tracking error. The additional $\Delta\theta_{\mathrm{PLL,q}}$ denotes the voltage magnitude oscillation caused by the harmonics. Therefore, the disturbance of the measured voltage signal $\tilde{\boldsymbol{v}}_{\mathrm{spf,dq}}^{\mathrm{SVOC}}$ in the symmetrical PLL can be expressed as,

$$\begin{cases}\tilde{v}_{\mathrm{spf,d}}^{\mathrm{SVOC}} =& \tilde{v}_{\mathrm{shf,d}} - |\boldsymbol{v}_{\mathrm{s1}}|\Delta\theta_{\mathrm{PLL,q}} \\ \tilde{v}_{\mathrm{spf,q}}^{\mathrm{SVOC}} =& \tilde{v}_{\mathrm{shf,q}} - |\boldsymbol{v}_{\mathrm{s1}}|\Delta\theta_{\mathrm{PLL,d}}\end{cases}. \tag{29}$$

Compared to (16), it shows that error of the voltage magnitude $\tilde{v}_{\mathrm{spf,d}}^{\mathrm{SVOC}}$ is also tracked by $\Delta\theta_{\mathrm{PLL,q}}$. The structure is overall a symmetrical one. Therefore, using the definition of $\boldsymbol{\Delta}\theta_{\mathrm{PLL}}$, the symmetrical PLL is designed as,

$$\boldsymbol{\Delta}\theta_{\mathrm{PLL}} = -j\frac{1}{s'}H_{\mathrm{PLL}}(s')\tilde{\boldsymbol{v}}_{\mathrm{spf,dq}}^{\mathrm{SVOC}} \tag{30}$$

The equivalent small-signal model of the symmetrical PLL is shown in Fig. 5. Substituting (30) into (29) yields,

$$\boldsymbol{\Delta}\theta_{\mathrm{PLL}} = -jT_{\mathrm{PLL}}(s')\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}. \tag{31}$$

Compared to (19), the conjugate part $\tilde{\boldsymbol{v}}_{\mathrm{shf,dq}}^{*}$ is eliminated. Therefore, the output VSC voltage signal can be simplified as,

$$\begin{aligned}\tilde{\boldsymbol{v}}_{\mathrm{ch,dq}}^{\mathrm{SVOC}} =& G_{\mathrm{del}}(s)G_{\mathrm{c,dq}}(s')\tilde{\boldsymbol{i}}_{\mathrm{sh,dq}} \\ &+ G_{\mathrm{del}}(s)G_{\mathrm{fil}}(s)[1 + G_{\mathrm{PLL}}(s')]\tilde{\boldsymbol{v}}_{\mathrm{sh,dq}}.\end{aligned} \tag{32}$$

Similar to (13), the expression of (32) in the $\alpha\beta$ frame can be found by replacing s' with $s - j\omega_{\mathrm{s}}$ given as,

$$\begin{aligned}\tilde{\boldsymbol{v}}_{\mathrm{ch},\alpha\beta}^{\mathrm{SVOC}} =& G_{\mathrm{del}}(s)G_{\mathrm{c},\alpha\beta}(s - j\omega_{\mathrm{s}})\tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta} \\ &+ G_{\mathrm{del}}(s)G_{\mathrm{fil}}(s)[1 + G_{\mathrm{PLL}}(s - j\omega_{\mathrm{s}})]\tilde{\boldsymbol{v}}_{\mathrm{sh},\alpha\beta}.\end{aligned} \tag{33}$$

Since the harmonic admittance of VSC using symmetrical PLL can be expressed as a SISO system, based on the basic admittance $Y_{\mathrm{VSC}}^{\mathrm{PI}}$, the equivalent admittance of the grid-connected VSC using symmetrical PLL based VOC can be deduced by substituting (32) into (3) as follows,

$$\boldsymbol{Y}_{\mathrm{VSC}}^{\mathrm{SVOC}} = \frac{\tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta}}{\tilde{\boldsymbol{v}}_{\mathrm{sh},\alpha\beta}} = Y_{\mathrm{VSC}}^{\mathrm{PI}} + Y_{\mathrm{n}}^{\mathrm{SVOC}}, \tag{34}$$

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where $Y_{\mathrm{n}}^{\mathrm{SVOC}}$ denotes the additional virtual admittance of the symmetrical PLL on the original $Y_{\mathrm{VSC}}^{\mathrm{PI}}$. The expression of $Y_{\mathrm{n}}^{\mathrm{SVOC}}$ is,

$$\begin{aligned} Y_{\mathrm{n}}^{\mathrm{SVOC}} =&[1+Y_{\mathrm{f}}(s)G_{\mathrm{c},\alpha\beta}(s-j\omega_{\mathrm{s}})]^{-1}Y_{\mathrm{f}}(s) \\ &\cdot G_{\mathrm{fil}}(s)G_{\mathrm{PLL}}(s-j\omega_{\mathrm{s}}) \end{aligned}. \tag{35}$$

Note that the value of the voltage magnitude $|v_{\mathrm{s1}}|$ in symmetrical PLL should be updated in real-time according to the system operation point.

B. Proportional Resonant Control

The basic control structure of a PR control is shown in Fig. 6. The outer loop can be written in terms of complex vectors as,

$$\boldsymbol{i}_{\mathrm{sref},\alpha\beta} = -\frac{2}{3}\frac{1}{|\boldsymbol{v}_{\mathrm{sf}}^2|}\boldsymbol{v}_{\mathrm{sf},\alpha\beta}(P_{\mathrm{sref}} - jQ_{\mathrm{sref}}). \tag{36}$$

The PR current controller using ideal decoupled proportional and reduced-order generalized integrator (ROGI) has following transfer function ,

$$\begin{aligned} \boldsymbol{v}_{\mathrm{c},\alpha\beta}^{\mathrm{PR}} =&\boldsymbol{v}_{\mathrm{c1},\alpha\beta}^{\mathrm{PR}} + \tilde{\boldsymbol{v}}_{\mathrm{ch},\alpha\beta}^{\mathrm{PR}} \\ =&-L_{\mathrm{f}}(K_{\mathrm{p}} + \frac{K_{\mathrm{i}}}{s-j\omega_{\mathrm{s}}})(\boldsymbol{i}_{\mathrm{sref},\alpha\beta} - \boldsymbol{i}_{\mathrm{s},\alpha\beta}). \\ &-j\omega_{\mathrm{s}}L_{\mathrm{f}}i_{\mathrm{s},\alpha\beta} + \boldsymbol{v}_{\mathrm{sf},\alpha\beta} \end{aligned} \tag{37}$$

For the sake of reducing the computation burden, the ROGI is often merged into a second-order generalized integrator (SOGI) as $K_{\mathrm{p}} + K_{\mathrm{i}}/(s-j\omega_{\mathrm{s}}) \rightarrow K_{\mathrm{p}} + 2K_{\mathrm{i}}s/(s^2+\omega_s^2)$ [22]. Since the main control of PR using SOGI is different from $G_{\mathrm{c},\alpha\beta}(s)$. Therefore, to maintain unity, only PR control with ROGI is considered in this paper. The control block diagram of the PR control is presented in Fig. 6. The fundamental components in (37) can be re-configured and expressed in dq reference frame as follows,

$$\begin{aligned} \boldsymbol{v}_{\mathrm{c1,dq}} =&-L_{\mathrm{f}}(K_{\mathrm{p}} + \frac{K_{\mathrm{i}}}{s'})(\boldsymbol{i}_{\mathrm{sref,dq}} - \boldsymbol{i}_{\mathrm{s1,dq}}) \\ &-j\omega_{\mathrm{s}}L_{\mathrm{f}}i_{\mathrm{s1,dq}} + \boldsymbol{v}_{\mathrm{s1,dq}} \end{aligned}. \tag{38}$$

By substituting (38) into (3), the transient dynamics of the fundamental current component tracking can be derived as the same as (9). Therefore, it can be concluded that the S-VOC and PR share the same power tracking property. By dividing the current input into $\boldsymbol{i}_{\mathrm{s},\alpha\beta} = \boldsymbol{i}_{\mathrm{s1},\alpha\beta} + \tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta}$ the harmonic components of the VSC output voltage can be deduced by substituting (36) into (37) as,

$$\begin{aligned} \tilde{\boldsymbol{v}}_{\mathrm{ch},\alpha\beta} =&G_{\mathrm{del}}(s)G_{\mathrm{c},\alpha\beta}(s)\tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta} + \\ &G_{\mathrm{del}}(s)G_{\mathrm{fil}}(s)[1+G_{\mathrm{PR}}(s)]\tilde{\boldsymbol{v}}_{\mathrm{sh},\alpha\beta}, \end{aligned} \tag{39}$$

where $G_{\mathrm{PR}}(s)$ can be expressed as,

$$G_{\mathrm{PR}}(s) = \frac{2L_{\mathrm{f}}}{3|\boldsymbol{v}_{\mathrm{s1}}^2|}(K_{\mathrm{p}} + \frac{2K_{\mathrm{i}}s}{s^2+\omega_s^2})(P_{\mathrm{sref}} - jQ_{\mathrm{sref}}). \tag{40}$$

The coupling in $|\boldsymbol{v}_{\mathrm{sf}}^2|$ is not considered in the admittance model, i.e., $|\boldsymbol{v}_{\mathrm{sf}}^2|$ is simplified as a known constant $|\boldsymbol{v}_{\mathrm{s1}}^2|$ in (40). Note that a constant value $|\boldsymbol{v}_{\mathrm{s1}}^2|$ can be used to replace the real-time calculation of $|\boldsymbol{v}_{\mathrm{sf}}^2|$ in (36) for the sake of eliminating the coupling terms. However, it sacrifices the accuracy of power tracking once the system operation point changes.

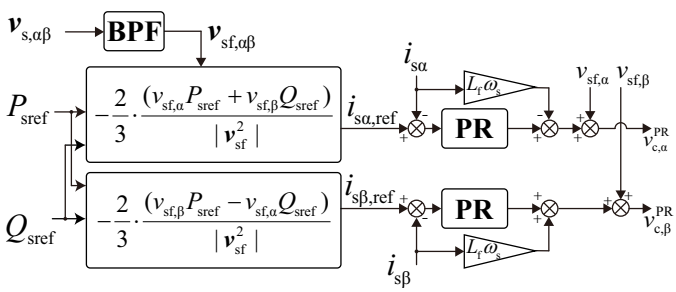

Fig. 6. Block diagram of proportional resonant (PR) control.

The harmonic admittance of the VSC using PR control can be derived by substituting (39) into (4) as follows,

$$\boldsymbol{Y}_{\mathrm{VSC}}^{\mathrm{PR}} = \frac{\tilde{\boldsymbol{i}}_{\mathrm{sh},\alpha\beta}}{\tilde{\boldsymbol{v}}_{\mathrm{sh},\alpha\beta}} = Y_{\mathrm{VSC}}^{\mathrm{PI}} + Y_{\mathrm{n}}^{\mathrm{PR}}, \tag{41}$$

where $Y_{\mathrm{n}}^{\mathrm{PR}}$ denotes the influence of PR control on VSC system admittance,

$$\begin{aligned} \boldsymbol{Y}_{\mathrm{n}}^{\mathrm{PR}} =&-[1+G_{\mathrm{del}}(s)Y_{\mathrm{f}}(s)G_{\mathrm{c},\alpha\beta}(s-j\omega_{\mathrm{s}})]^{-1} \\ &\cdot Y_{\mathrm{f}}(s)G_{\mathrm{del}}(s)G_{\mathrm{fil}}(s)G_{\mathrm{PR}}(s) \end{aligned}. \tag{42}$$

C. VM-DPC Control

VM-DPC is a symmetrical control strategy designed directly in $\alpha\beta$ reference frame. The method guarantees power tracking without using PLL with zero steady-state error. A brief introduction of the control scheme is presented firstly in this subsection. The instantaneous powers of the VSC can be expressed as,

$$\begin{cases} P_{\mathrm{sf}} =P_{\mathrm{s1}} + P_{\mathrm{shf}} = -\dfrac{3}{2}\mathrm{Re}(\boldsymbol{v}_{\mathrm{sf}}\boldsymbol{i}_{\mathrm{s}}^*) \\ Q_{\mathrm{sf}} =Q_{\mathrm{s1}} + Q_{\mathrm{shf}} = -\dfrac{3}{2}\mathrm{Im}(\boldsymbol{v}_{\mathrm{sf}}\boldsymbol{i}_{\mathrm{s}}^*) \end{cases}, \tag{43}$$

where P_{shf} and Q_{shf} denote the instantaneous powers generated by the harmonics of PCC voltage and output current. $\boldsymbol{i}_{\mathrm{s}}^*$ is the conjugate of $\boldsymbol{i}_{\mathrm{s}}$. The differential of powers are presented as,

$$\begin{cases} \dfrac{dP_{\mathrm{sf}}}{dt} =-\dfrac{3}{2}\mathrm{Re}(\dfrac{d\boldsymbol{v}_{\mathrm{sf},\alpha\beta}}{dt}\boldsymbol{i}_{\mathrm{s},\alpha\beta}^*) - \dfrac{3}{2}\mathrm{Re}(\boldsymbol{v}_{\mathrm{sf},\alpha\beta}\dfrac{d\boldsymbol{i}_{\mathrm{s},\alpha\beta}^*}{dt}) \\ \dfrac{dQ_{\mathrm{sf}}}{dt} =-\dfrac{3}{2}\mathrm{Im}(\dfrac{d\boldsymbol{v}_{\mathrm{sf},\alpha\beta}}{dt}\boldsymbol{i}_{\mathrm{s},\alpha\beta}^*) - \dfrac{3}{2}\mathrm{Im}(\boldsymbol{v}_{\mathrm{sf},\alpha\beta}\dfrac{d\boldsymbol{i}_{\mathrm{s},\alpha\beta}^*}{dt}) \end{cases}. \tag{44}$$

Note that the VM-DPC is designed based on an ideal grid voltage condition. Therefore, the differential of filtered grid voltage can be approximated as,

$$\frac{d}{dt}\boldsymbol{v}_{\mathrm{sf},\alpha\beta} \approx j\omega_{\mathrm{s}}\boldsymbol{v}_{\mathrm{sf},\alpha\beta}. \tag{45}$$

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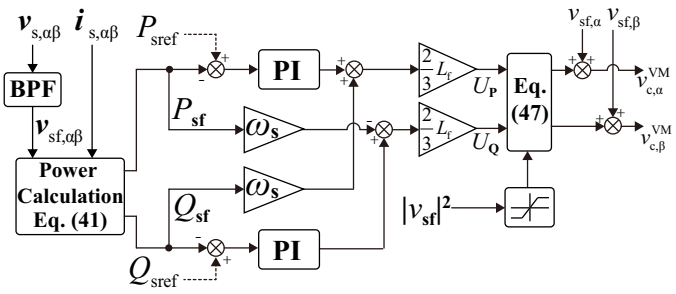

Fig. 7. Control scheme of voltage modulated direct power control (VM-DPC).

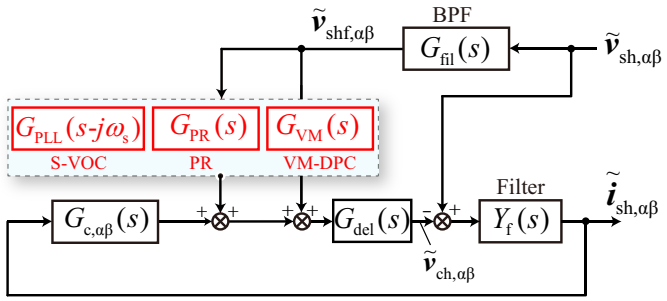

Fig. 8. Small-signal block diagram of grid-connected VSC using three symmetrical control strategies with BPF and voltage feedforward.

By substituting (2) and (45) into (44), the instantaneous variation of the active and reactive powers of VSC can be calculated as,

$$\begin{cases} \dfrac{dP_{\rm sf}}{dt} = -\omega_{\rm s}Q_{\rm sf} - \dfrac{R_{\rm f}}{L_{\rm f}}P_{\rm sf} + \dfrac{3}{2L_{\rm f}}U_{\rm P} \\ \dfrac{dQ_{\rm sf}}{dt} = \omega_{\rm s}P_{\rm sf} - \dfrac{R_{\rm f}}{L_{\rm f}}Q_{\rm sf} + \dfrac{3}{2L_{\rm f}}U_{\rm Q}, \end{cases} \tag{46}$$

where $U_{\rm P}$ and $U_{\rm Q}$ are the power regulation inputs, which are,

$$\begin{aligned} U_{\rm P} &= {\rm Re}(\boldsymbol{v}_{{\rm sf},\alpha\beta}\boldsymbol{v}^*_{{\rm c},\alpha\beta}) - |\boldsymbol{v}^2_{\rm sf}| \\ U_{\rm Q} &= {\rm Im}(\boldsymbol{v}_{{\rm sf},\alpha\beta}\boldsymbol{v}^*_{{\rm c},\alpha\beta}) \end{aligned}. \tag{47}$$

Based on (46) and (47), the power tracking can be designed in closed-loop using PI and power feedforward, which can be expressed as follows,

$$\begin{cases} U_{\rm P} = \dfrac{2L_{\rm f}}{3}G_{\rm PI,dq}(s')(P_{\rm sref} - P_{\rm sf}) + \omega_{\rm s}Q_{\rm sf} \\ U_{\rm Q} = \dfrac{2L_{\rm f}}{3}G_{\rm PI,dq}(s')(Q_{\rm sref} - Q_{\rm sf}) - \omega_{\rm s}P_{\rm sf} \end{cases}. \tag{48}$$

Finally, the output VSC voltage can be decomposed into its fundamental and harmonic components as $\boldsymbol{v}^{\rm VM}_{{\rm c},\alpha\beta} = \boldsymbol{v}^{\rm VM}_{{\rm c1},\alpha\beta} + \boldsymbol{v}^{\rm VM}_{{\rm ch},\alpha\beta}$, which can be deduced from (47) given as,

$$\begin{cases} v^{\rm VM}_{{\rm c},\alpha} = (v_{{\rm sf},\alpha}U_{\rm P} + v_{{\rm sf},\beta}U_{\rm Q})/|\boldsymbol{v}^2_{\rm sf}| + v_{{\rm sf},\alpha} \\ v^{\rm VM}_{{\rm c},\beta} = (v_{{\rm sf},\beta}U_{\rm P} - v_{{\rm sf},\alpha}U_{\rm Q})/|\boldsymbol{v}^2_{\rm sf}| + v_{{\rm sf},\beta} \end{cases}. \tag{49}$$

Note that by substituting (43) and (48) into (46), the dynamics of the fundamental power tracking can be extracted and expressed in terms of the same equation given as (9),

$$\frac{\boldsymbol{S}_{\rm s1}}{\boldsymbol{S}_{\rm sref}} = \frac{K_{\rm p}s' + K_{\rm i}}{s'^2 + (K_{\rm p} + \frac{R_{\rm f}}{L_{\rm f}})s' + K_{\rm i}}. \tag{50}$$

Therefore, it is found that VM-DPC and VOC share the same power tracking property. Moreover, VM-DPC has better tracking performance since it eliminates PLL [26]. The control scheme of VM-DPC is presented in Fig. 7.

It can be found from (49) that integration control of the powers inevitably result in a cross coupling between harmonic components and fundamental components. However, if the magnitude $\tilde{\boldsymbol{v}}_{\rm shf}$ can be reduced by using the BPF, then, $\tilde{\boldsymbol{v}}^{\rm VM}_{{\rm ch},\alpha\beta}$ can be approximately linearized [30]. The details of the linearization of $\tilde{\boldsymbol{v}}^{\rm VM}_{{\rm ch},\alpha\beta}$ is presented as,

$$\begin{aligned} \tilde{\boldsymbol{v}}^{\rm VM}_{{\rm ch},\alpha\beta} =& G_{\rm del}(s)G_{{\rm c},\alpha\beta}(s)\tilde{\boldsymbol{i}}_{{\rm sh},\alpha\beta} \\ &+ G_{\rm del}(s)G_{\rm fil}(s)[1 + G_{\rm VM}(s)]\tilde{\boldsymbol{v}}_{{\rm sh},\alpha\beta}, \end{aligned} \tag{51}$$

where $G_{{\rm c},\alpha\beta}(s)$ can be found equal to $G_{{\rm c,dq}}(s')$ as follows,

$$G_{{\rm c},\alpha\beta}(s) = L_{\rm f}(K_{\rm p} + \frac{K_{\rm i}}{s - j\omega_{\rm s}} - j\omega_{\rm s}) = G_{{\rm c,dq}}(s'), \tag{52}$$

and $G_{\rm VM}(s)$ can be written as,

$$G_{\rm VM}(s) = \frac{2L_{\rm f}K_{\rm p}}{3|\boldsymbol{v}^2_{\rm s1}|}(P_{\rm sref} - jQ_{\rm sref}). \tag{53}$$

The detailed admittance model of VSC using VM-DPC can be derived by substituting (51) into (4) . The admittance model can be derived as,

$$\boldsymbol{Y}^{\rm VM}_{\rm VSC} = \frac{\tilde{\boldsymbol{i}}_{{\rm sh},\alpha\beta}}{\tilde{\boldsymbol{v}}_{{\rm sh},\alpha\beta}} = Y^{\rm PI}_{\rm VSC} + Y^{\rm VM}_{\rm n}. \tag{54}$$

where $Y^{\rm VM}_{\rm n}$ denotes the virtual admittance of VM-DPC, which represents the adverse effect of the VM-DPC on the basic admittance $Y^{\rm PI}_{\rm VSC}$. The expression of $Y^{\rm VM}_{\rm n}$ is,

$$\begin{aligned} \boldsymbol{Y}^{\rm VM}_{\rm n} =& -[1 + G_{\rm del}(s)Y_{\rm f}(s)G_{{\rm c},\alpha\beta}(s - j\omega_{\rm s})]^{-1} \\ &\cdot Y_{\rm f}(s)G_{\rm del}(s)G_{\rm fil}(s)G_{\rm VM}(s) \end{aligned}. \tag{55}$$

Finally, the small-signal block diagram of three symmetrical control strategies can be unified using the same frame as shown in Fig. 8. The differences of the block diagram of three methods are highlighted in red color. By comparing (53) with (40), it can be found the difference between $G_{\rm PR}(s)$ and $G_{\rm VM}(s)$ can be deduced as,

$$G_{\rm PR}(s) - G_{\rm VM}(s) = \frac{2L_{\rm f}}{3|\boldsymbol{v}^2_{\rm s1}|}\frac{2K_{\rm i}}{s^2 + \omega_s^2}(P_{\rm sref} - jQ_{\rm sref}). \tag{56}$$

It shows that the difference between $Y^{\rm PR}_{\rm VSC}$ and $Y^{\rm VM}_{\rm VSC}$ gets Larger when the frequency of the harmonics is close to the fundamental frequency, i.e., $s = j\omega_{\rm h} \approx j\omega_{\rm s}$. On the other side, when $\omega_{\rm h}$ is away from $\omega_{\rm s}$, it can be assumed that $Y^{\rm PR}_{\rm VSC} \approx Y^{\rm VM}_{\rm VSC}$ in the high- and low- frequency range.

IV. Unified Performance Analysis

The unified dynamic response of three control strategies is discussed in this section. The power tracking dynamics of three

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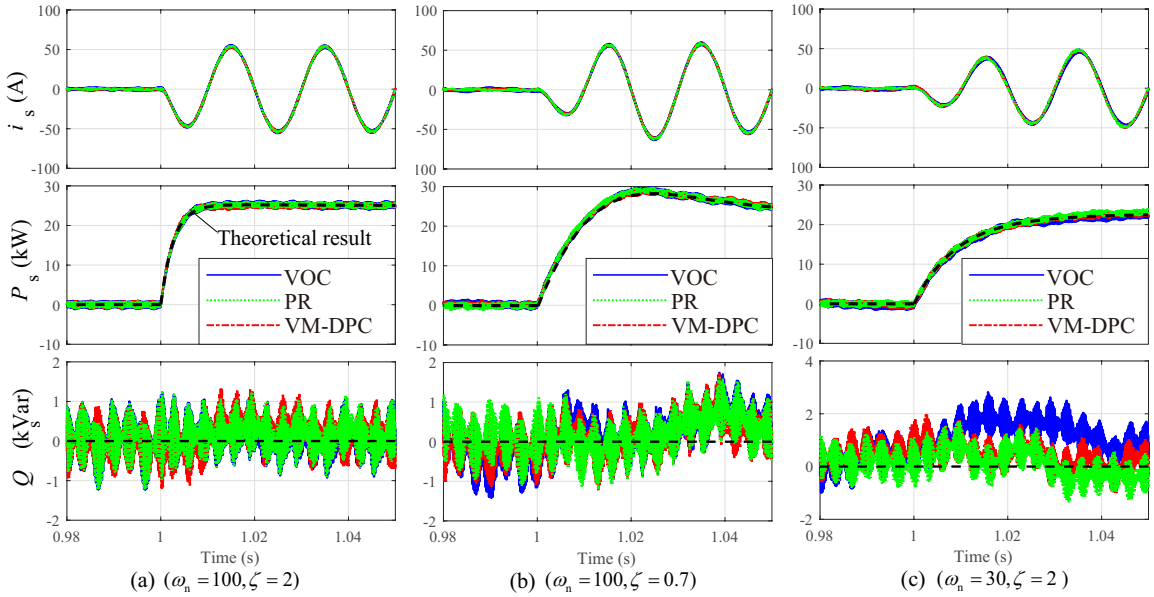

Fig. 9. Transient dynamics comparison of three different control methods under different control parameters. (a) $\omega_{\rm n} = 100, \zeta = 2$. (b) $\omega_{\rm n} = 100, \zeta = 0.7$. (c) $\omega_{\rm n} = 30, \zeta = 2$

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TABLE I
PARAMETERS OF SIMULATED GRID-CONNECTED VSC SYSTEM AND VM-DPC CONTROLLER

Parameter	Symbol	Value	Unit
Rated power	$P_{\rm sref}$	25	kW
Switching frequency	$f_{\rm w}$	4	kHz
Sampling frequency	$f_{\rm sa}$	4	kHz
Time delay	$T_{\rm del}$	0.0001	s
Fundamental frequency	f	50	Hz
Ground-to-line Voltage	$v_{\rm s,rms}$	220	V
Natural frequency of BPF	$\omega_{\rm fn}$	314	rad/s
Damping ratio of BPF	$\zeta_{\rm f}$	0.1	
Dc voltage	$v_{\rm dc}$	730	V
Filter resistance	$R_{\rm f}$	0.12	Ω
Filter inductor	$L_{\rm f}$	6	mH
Control Parameters			
Parameter	Value	Parameter	Value
$K_{\rm p}$	121.4	$K_{\rm i}$	10000
$\omega_{\rm n}$	100	ζ	0.7
$K_{\rm PLL,p}$	1.5	$K_{\rm PLL,i}$	130
$f_{\rm bw\_PLL}$	100 Hz		

controllers can be deduced by transforming (9), (50) into the time domain as follows,

$$\begin{cases} \boldsymbol{S}_{\rm s} = \boldsymbol{S}_{\rm sref}\{1 + e^{-\zeta\omega_n t}[(\dfrac{K_{\rm p}}{\omega_d} - \dfrac{\zeta}{\sqrt{1-\zeta^2}})\sin(\omega_d t) \\ \qquad - \cos(\omega_d t)]\},\ \zeta < 1 \\ \boldsymbol{S}_{\rm s} = \boldsymbol{S}_{\rm sref}\{1 + e^{-\zeta\omega_n t}[(\dfrac{K_{\rm p}}{\omega_d} - \dfrac{\zeta}{\sqrt{\zeta^2-1}})\sinh(\omega_d t) \\ \qquad - \cosh(\omega_d t)]\},\ \zeta > 1 \end{cases} \tag{57}$$

where $\omega_{\rm d} = \omega_{\rm n}\sqrt{1-\zeta^2}$ denotes the natural damping frequency. To compare the transient response characteristics of different control strategies, a simulation is carried out in MATLAB/Simulink Simscape Power System. The IGBT switching signals are generated by using SVPWM with a switching frequency of 4 kHz. The dc-link voltage is set to 730 V. The VSC is connected to an ideal power grid. The circuit and control parameters are presented in Table I. Fig. 9 shows the simulation result of system step response with different control parameters. $\Delta P_{\rm sref} = 25$ kW is set at 1s. Three different control settings are chosen, i.e., ($\omega_{\rm n} = 100, \zeta = 2$), ($\omega_{\rm n} = 100, \zeta = 0.7$), and ($\omega_{\rm n} = 30, \zeta = 2$). The black dotted line shows the dynamics response calculated based on (57). They are in agreement with the simulation results. It can be recognized that all three VSCs with different control methods track their references very well. Moreover, they also exhibit a similar transient response. The couplings between active and reactive power regulation of three methods are all small. It can be concluded that three different control methods have the same power tracking property. It can also be found that the system transient performance is improved either by increasing $\omega_{\rm n}$ from 30 to 100 or increasing ζ from 0.7 to 2.

Note that the transient response in Fig. 9 are obtained under ideal grid conditions. The steady-state performance of VSC using different controllers is also tested under non-ideal grid conditions as presented in Fig. 10. The system performance under frequency variation conditions is shown in Fig. 10 (a). it can be observed that the PR control strategy leads to a steady-state error in the reactive power output. The system performance under three-phase voltage drop (0.2 p.u) and phase-A voltage drop (0.2 p.u) are tested, respectively as presented in Fig. 10 (b) and (c). It can be observed that three control strategies exhibit similar steady-state performance under the voltage drop conditions. In addition, note that the unbalanced voltage drop will cause power fluctuation and harmonic components. The reactive power fluctuation of VSC using the S-VOC is relatively large among the three control strategies, which is caused by the error of phase-locking under unbalanced grid conditions.

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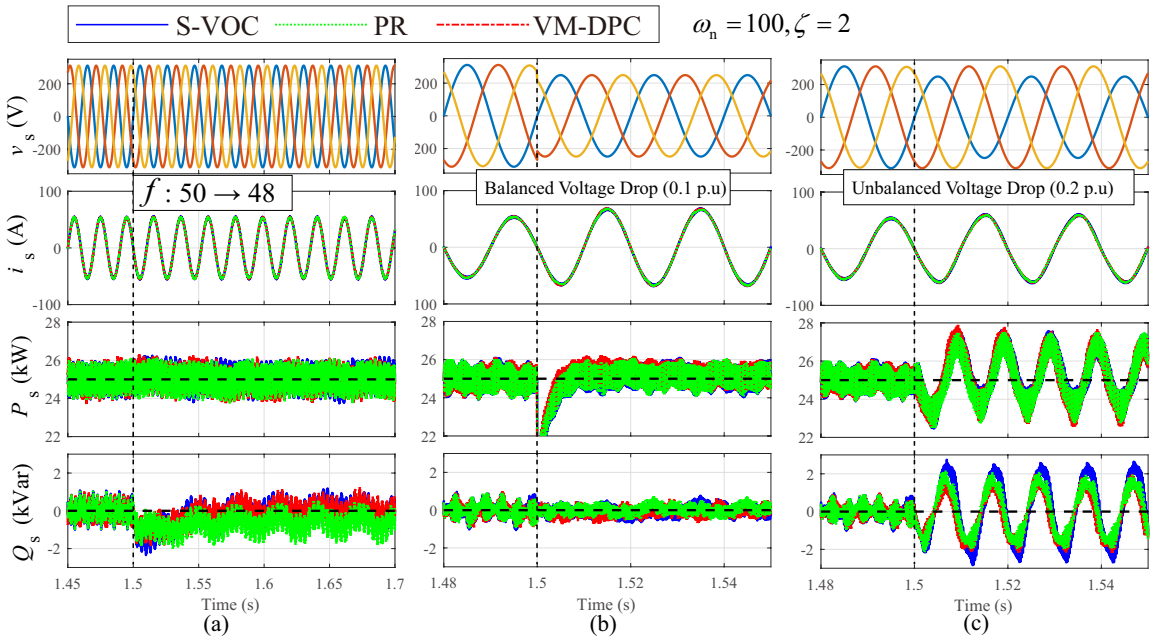

Fig. 10. Transient dynamics comparison of three different control methods under non-ideal grid conditions. (a) Frequency variation. (b) Balanced voltage drop condition. (c) Unbalanced voltage drop condition.

In this section, the unified transient performance of three control methods is analysed. Note that once the control parameters are determined, the basic admittance $Y_{\mathrm{VSC}}^{\mathrm{PI}}$ is also determined. However, since each strategy has different additional admittance, their influence will affect the admittance characteristics and the grid-connection stability. In the next section, the admittance of three control methods is analysed, the accuracy of the admittance modeling is verified using the frequency-scan method. Moreover, the stability of the three control methods is compared based on the unified power tracking dynamics. In order to better explain the work of this paper, a diagram that summarizes the structure is presented in Fig. 11.

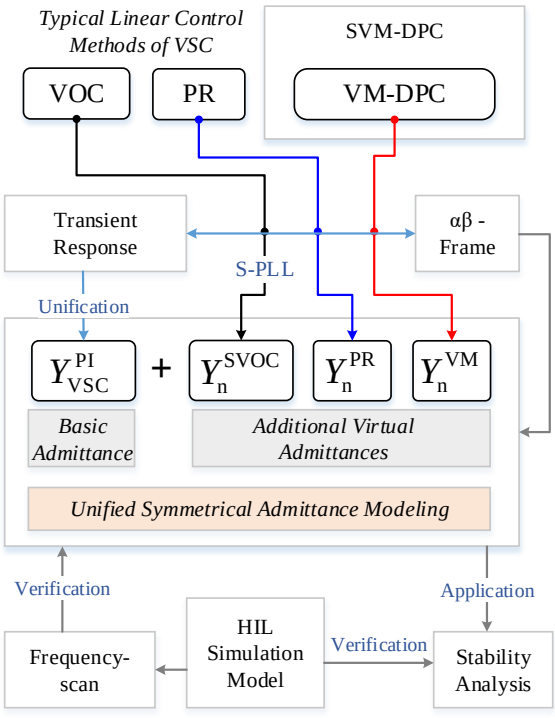

Fig. 11. Structure of the paper.

V. Admittance Specification and Stability Analysis

This section determines the admittance specifications of the three control strategies. The circuit and control parameters are the same as presented in Table I.

A. Admittance Model Verification

The accuracy of the proposed admittance modeling can be verified through the frequency-scan method. The bode plots of the proposed admittance characteristic calculated based on (34), (54), and (41), are compared with the corresponding frequency-scan results as presented in Fig. 12. The frequency-scan results are carried out using detailed models which consider both the time delay and PWM. The control parameters of the comparison study is chosen as ($K_{\mathrm{p}} = 121, K_{\mathrm{i}} = 10000 \rightarrow \omega_{\mathrm{n}} = 100, \zeta = 0.7$). The bandwidth of the symmetrical PLL is set to $f_{\mathrm{bw\_PLL}} = 100$ Hz. It can be observed that the harmonic response of grid-connected VSC using S-VOC and PR are very close to each other. The difference of admittance characteristics is mainly reflected near the fundamental frequency (50 Hz), that the VSC using VM-DPC exhibits a comparatively low admittance magnitude, followed by the PR control and the S-VOC. Among the three control strategies, the S-VOC appears most sensitive to voltage harmonics, while

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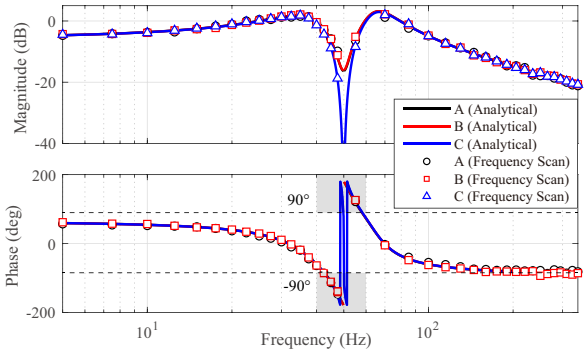

Fig. 12. Verification of the proposed admittance modelling with different control strategies ($\omega_n = 100$, $\zeta = 0.7$). (A) S-VOC, (B) PR control, (C) VM-DPC.

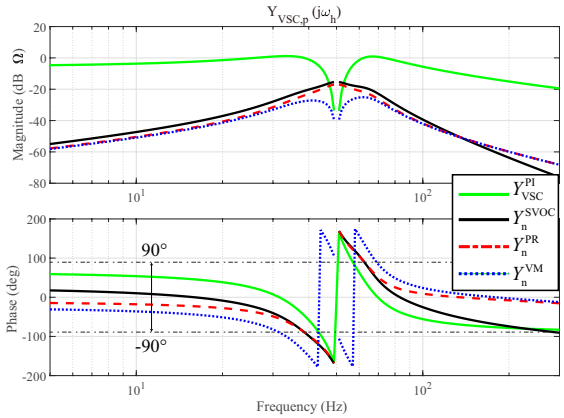

Fig. 13. Comparison between the basic admittance Y_{VSC}^{PI} and the virtual admittances of different symmetrical control methods.

VM-DPC seems to be the most robust one.

The adverse effect of the control structures on the equivalent admittance characteristics of grid-connected VSC is studied by comparing Y_{VSC}^{PI} to the virtual admittances Y_n^{SVOC}, Y_n^{VM}, and Y_n^{PR}, as presented in Fig. 13. It can be observed that in the high- and low- frequency range, the admittance characteristic of grid-connected VSC is mostly determined by the basic admittance Y_{VSC}^{PI} since $Y_{VSC}^{PI} \gg (Y_n^{SVOC}, Y_n^{VM}, Y_n^{PR})$. The virtual admittances Y_n^{VM}, Y_n^{SVOC}, and Y_n^{PR} successively show the negative damping characteristics, i.e., their phases are not within $-90° \sim 90°$, when the frequency moves close to 50 Hz. Since Y_n^{VM} exhibits the lowest magnitude, the negative damping of VM-DPC is the smallest among three strategies, which shows consistency with Fig. 12.

Fig. 14 shows a comparison of the analytical and frequency-scan results of the VSC equivalent admittances using different control strategies. The three different controllers are compared in separated figures. In each figure, the lines and dots with the same color denote the controllers that have the same control parameters. In other words, they share the same basic admittance Y_{VSC}^{PI}. Three groups of control parameters are selected, group (A) denotes ($K_p = 380, K_i = 10000 \rightarrow \omega_n = 100, \zeta = 2$), group (B) denotes ($K_p = 121, K_i = 10000 \rightarrow \omega_n = 100, \zeta = 0.7$), and group (C) denotes ($K_p = 100, K_i = 900 \rightarrow \omega_n = 30, \zeta = 2$), respectively. Moreover, when the parameters of the main controller change, the Bode plots of Y_{VSC}^{SVOC}, Y_{VSC}^{VM}, and Y_{VSC}^{PR} show similar variation rules. The results indicate that the decrease of damping ratio ζ and natural frequency ω_n will increase the magnitude of the system admittances, i.e., the system tends to be more sensitive to the voltage harmonics and becomes less stable under weak-grid connected conditions.

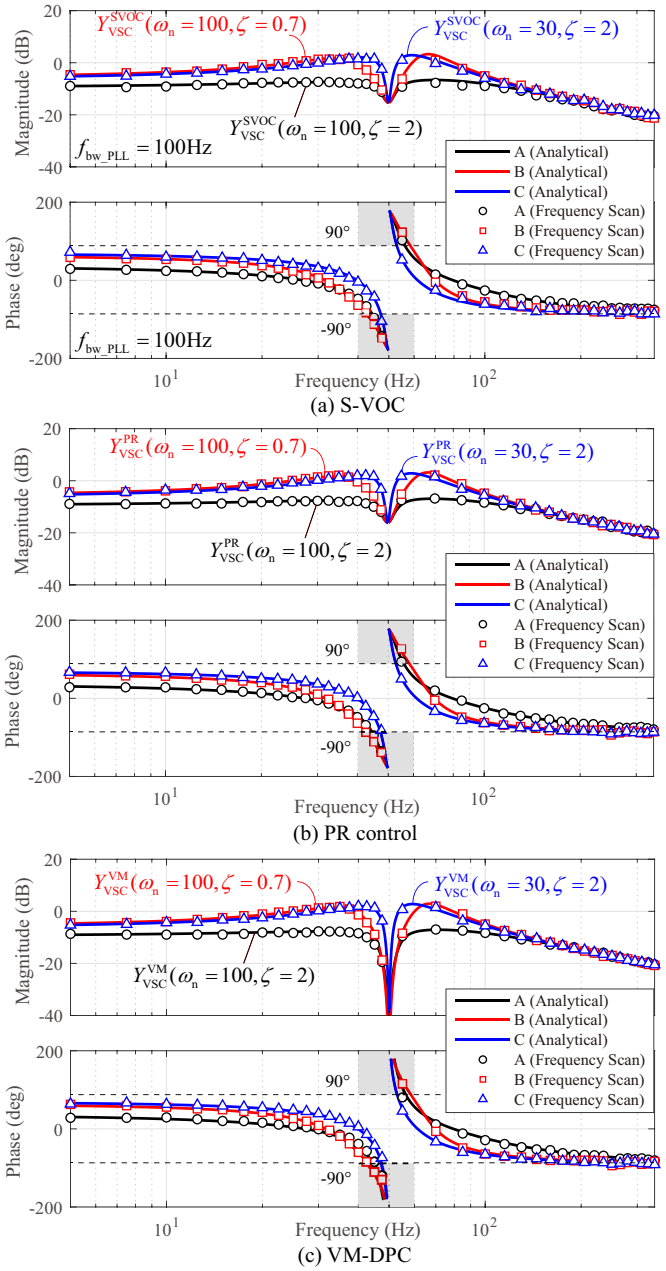

Fig. 14. Verification of the proposed admittance modelling under different control settings by using frequency-scan method. (a) Y_{VSC}^{SVOC}, (b)Y_{VSC}^{PR} , (c) Y_{VSC}^{VM}. (A) $\omega_n = 100$ rad/s, $\zeta = 0.7$, (B) $\omega_n = 100$ rad/s, $\zeta = 2$, (C) $\omega_n = 20$ rad/s, $\zeta = 0.7$.

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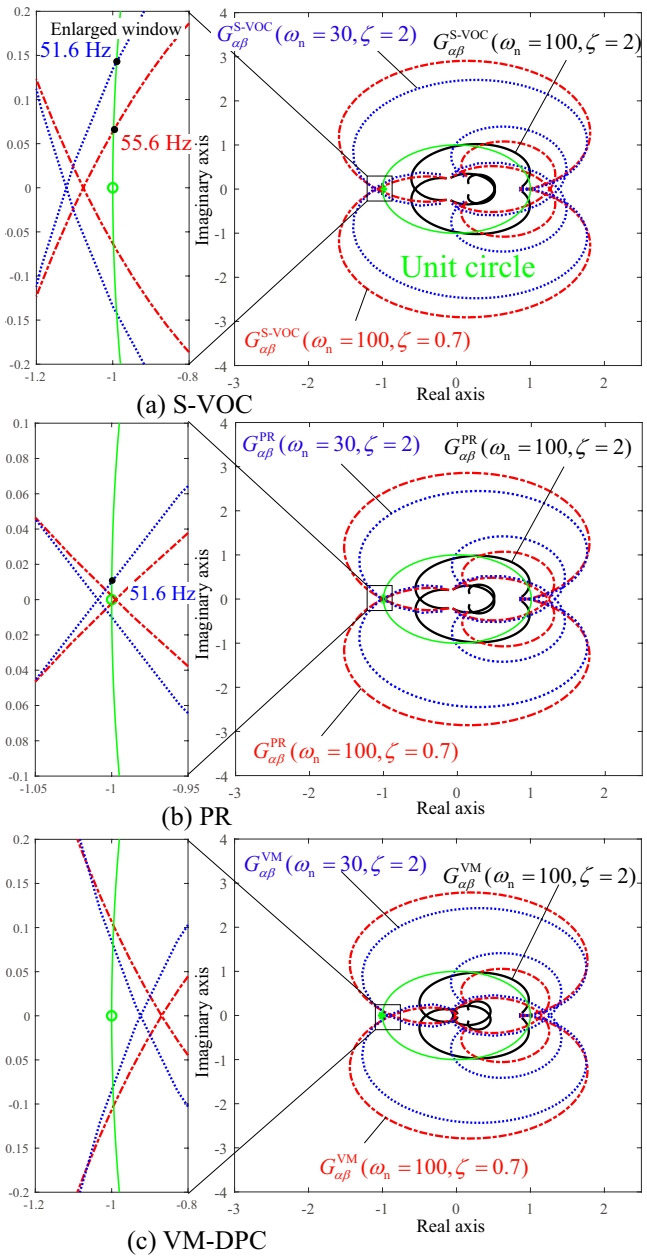

Fig. 15. Comparison of the Nyquist plots of three control methods with different control parameters.

B. Stability Analysis

In this subsection, the availability of the proposed method is verified by using the Nyquist stability criterion and simulation results. The grid admittance can be assumed as a simple, balanced R-L circuit, which can be expressed under the $\alpha\beta$ reference frame by using Laplace-transform as follows,

$$Y_{\text{grid}}(s) = \frac{1}{L_{\text{n}}s + R_{\text{n}}}, \tag{58}$$

where R_{n} is the grid resistance, L_{n} is the grid series inductance. The stability of the grid-connected VSC can be identified by using simple SISO Nyquist stability criterion [4]. The system can be simplified as Thevenin equivalent circuit consisting of the equivalent VSC admittance represented by

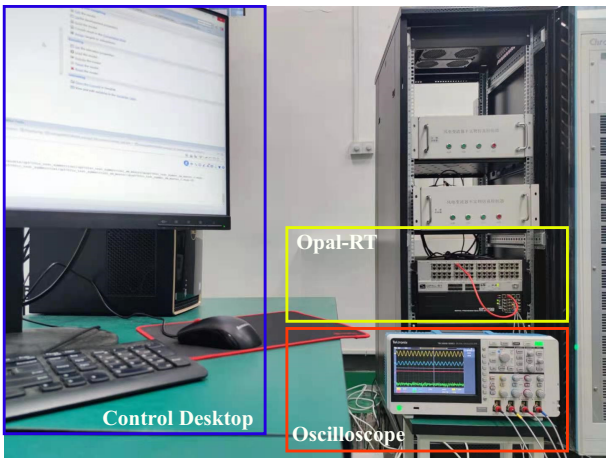

Fig. 16. Real-time simulation platform RTLAB from OPAL-RT for testing the three control methods.

Y_{VSC}, which are derived in (34), (54), and (41), respectively, and the grid admittance Y_{grid}. The stability of the system can be identified by the eigenvalues of the transfer function given as,

$$H_{\text{s}} = \frac{1}{1 + G_{\alpha\beta}(s)}, \tag{59}$$

where $G_{\alpha\beta}(s) = Y_{\text{VSC}}(s)/Y_{\text{grid}}(s)$ is the feedback characteristic equation of the system. Based on the linear-control theory that the closed-loop transfer function H_{s} guarantees stable operation only if $G_{\alpha\beta}(s)$ satisfies the Nyquist stability criterion, i.e., the encirclement of $(-1, j0)$ by the admittance ratio $G_{\alpha\beta}(s)$ is zero.

Fig. 15 shows a comparison of the Nyquist plot of $G_{\alpha\beta}(s)$ using different control methods. The VSCs are considered to be connected with grid with equivalent impedance ($L_{\text{n}} = 4.5$ mH, $R_{\text{n}} = 0.6\ \Omega$). Three different control parameter settings are chosen as the same as in Fig. 14. The bandwidth of the symmetrical PLL is set to 100 Hz. Fig. 15 (a) shows the Nyquist diagram of $G_{\alpha\beta}(s)$ describing a grid-connected VSC using S-VOC. It can be seen that the encirclement around $(-1, 0)$ of the Nyquist curve $Y_{\text{VSC}}^{\text{SVOC}}(s)/Y_{\text{grid}}(s)$ is zero when $\omega_{\text{n}} = 100, \zeta = 2$. However, the system becomes unstable when ζ changes from 2 to 0.7 and ω_{n} keeps 100 since the Nyquist curve encircles $(-1 + j0)$. It can be also found that the Nyquist curve intersects unit circle at 55.6 Hz, which also indicates the frequency of the oscillation component. A similar trend can be found when ω_{n} changes from 100 to 30 while $\zeta = 0.7$, the Nyquist curve encircles $(-1 + j0)$ and intersects unit circle at 51.9 Hz. From the enlarged window of Fig. 15 (b), it can be found that the point of intersection indicating $\omega_{\text{n}} = 100, \zeta = 0.7$, and $\omega_{\text{n}} = 30, \zeta = 2$, are very close to the critical point. Therefore, the system will be unstable. $G_{\alpha\beta}^{\text{PR}}$ with $\omega_{\text{n}} = 30$ and $\zeta = 2$ intersects the unit circle at 51.9 Hz. From Fig. 15 (c), it can be found all three curves avoid the critical point. Therefore, it can be concluded that the grid-connected VSC using the VM-DPC can maintain stable with all three control parameter settings.

The effectiveness of the Nyquist stability criterion based on proposed admittance modeling method is verified using

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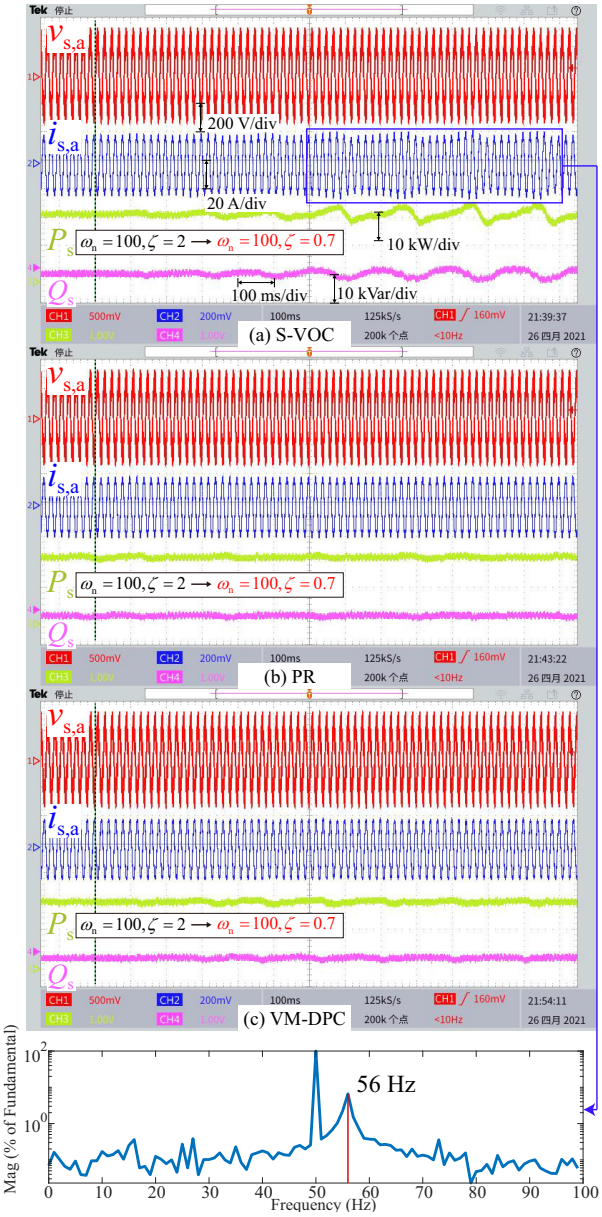

Fig. 17. Comparison of the grid interconnection test of the systems using different control methods when ($\omega_n = 100$, $\zeta = 0.7$). (a) VOC with symmetrical PLL ($f_{bw\_PLL} = 100$). (b) PR. (c) VM-DPC.

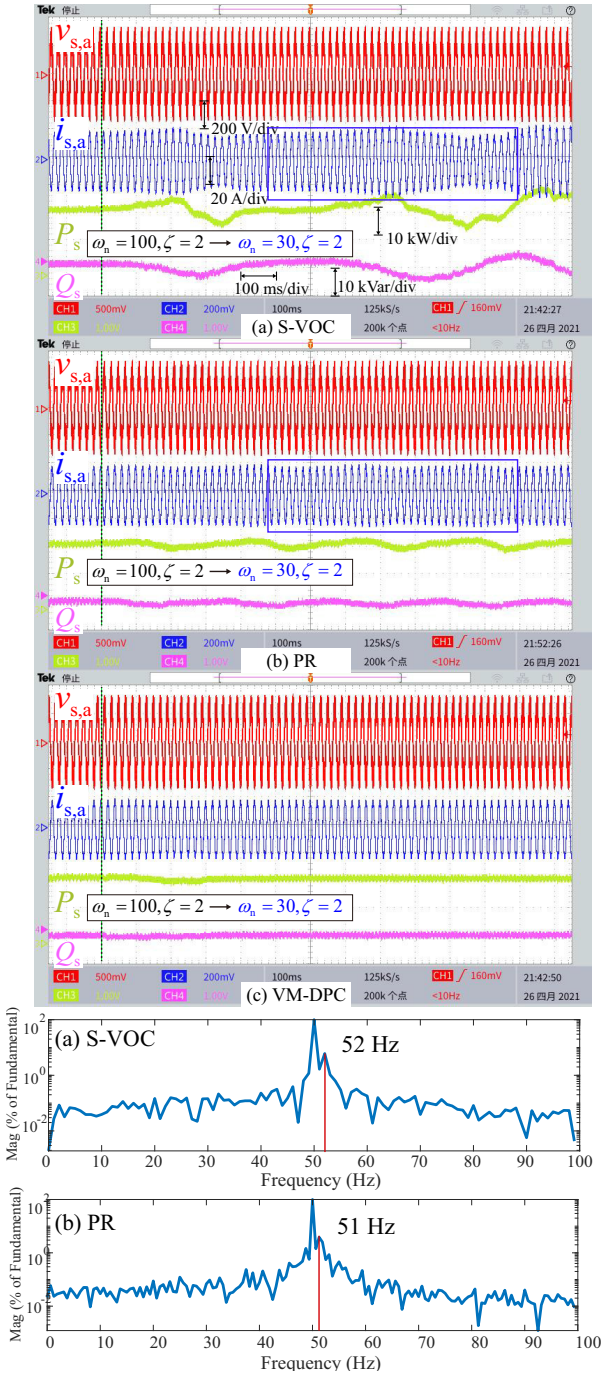

Fig. 18. Comparison of the grid interconnection test of the systems using different control methods under ($\omega_n = 30$, $\zeta = 2$). (a) VOC with symmetrical PLL ($f_{bw\_PLL} = 100$). (b) PR. (c) VM-DPC.

detailed simulation models implemented in real-time simulation platform RTLAB from Opal-RT as shown in Fig. 16. The circuit and the control parameters of the test are set as presented in Table I. Both time delay and PWM are considered in the simulation model. The VSCs are initially connected to a grid with equivalent impedance set as an inductor $L_n = 4.5$ mH in series with a resistance $R_n = 0.6\ \Omega$. In Fig. 17, the control parameters are set to $(\omega_n = 100, \zeta = 2)$ at the beginning of the simulation. Then, the control parameters of three systems change to $(\omega_n = 100, \zeta = 0.7)$. The results indicate that the grid-connected VSC using S-VOC becomes unstable. The frequency of the main oscillation component is 56 Hz, which is consistent with the Nyquist stability analysis. On the other side, the VSC using the VM-DPC and PR still maintain stability after the change. Fig. 18 demonstrates the cases of changing the control parameters from $(\omega_n = 100, \zeta = 2)$ to $(\omega_n = 30, \zeta = 2)$. It can be observed that VSCs using S-VOC and PR become out of control, and the powers start to oscillate after the parameters changing. The frequencies of the main oscillation components of the S-VOC and PR control are about 56 Hz and 51 Hz, which are consistent with the analytical results as presented in Fig. 15. The VSC using the VM-DPC control remains stable. Therefore, the accuracy of the admittance model and correctness of the Nyquist stability criterion are confirmed.

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TABLE II
PROS AND CONS OF THE THREE CONTROL STRATEGIES

Control	Advantages	Disadvantages
VOC	· Robust to parameter/ frequency variations · Measurement of grid frequency	· Use of coordinate transformation (dq frame) · Complex control structure
(PLL)		· Frequency-coupling
(S-PLL)	· No frequency coupling	· Use of voltage magnitude
PR	· Simple structure	· Affected by frequency variation
VM-DPC	· Robust to parameter/ frequency variations · Strong grid-connection stability · No coordinate transformation · Simple structure	· PI control brings frequency-coupling

Finally, the pros and cons of VOC, PR and VM-DPC are summarized as presented in Table. II. The results prove that the VM-DPC exhibits satisfactory control performance at the same level as VOC and PR and shows the best grid-connection stability among the three control methods.

C. Experimental Verification

The transient response characteristics of the three control strategies at the same current loop bandwidth are also tested by using an experimental prototype as presented in Fig. 19. A three-phase 7.5 kW Danfoss converter with L-filter is connected to the grid. A grid simulator is used to generate 220 V rms voltage. A constant dc voltage is supplied using a dc power supply. The PCC line-to-line voltages and the currents are measured by using a DS2004 A/D board. The three different control strategies are implemented by using the dSPACE DS1007 system. The parameters of the system and controllers are chosen to be the same as presented in Table. I. The inner-loop control parameters are set to ($\omega_n = 408$, $\zeta = 2.47$). Fig. 20 shows the transient response of the three different methods when the reference of active power steps from 0 to 3 kW. Notice that three controllers have the same power convergence time of about 1.5 ms. The results confirm previous analysis that three different control methods have the same transient dynamic characteristics.

VI. CONCLUSION

In this paper, a uniformed comparative admittance analysis of grid-connected inverters using three symmetrical control strategies, i.e., S-VOC, PR control, and VM-DPC, is presented. The control methods are uniformed by the same power tracking dynamic expressed by a second-order transfer function. The admittance models of the three control methods considering voltage filtering and feedforward linearization are derived based on an unified framework, and the adverse effect of the different symmetrical controllers on the system harmonic response is compared by using virtual admittance. The simulation and experimental results show the transient response of three methods are the same. The experiment The admittance analysis presents the difference in the admittance characteristics mainly reflect near the fundamental frequency. The grid-connected VSC using VM-DPC seems to be the least sensitive toward voltage harmonics. Moreover, it also exhibits the highest stability among the three symmetrical controllers. The bode plots of the equivalent admittances of the VSC using S-VOC and PR are very close to each other. The Nyquist stability criterion based on the proposed admittance modeling is verified by using the RTLAB simulation platform from Opal-RT.

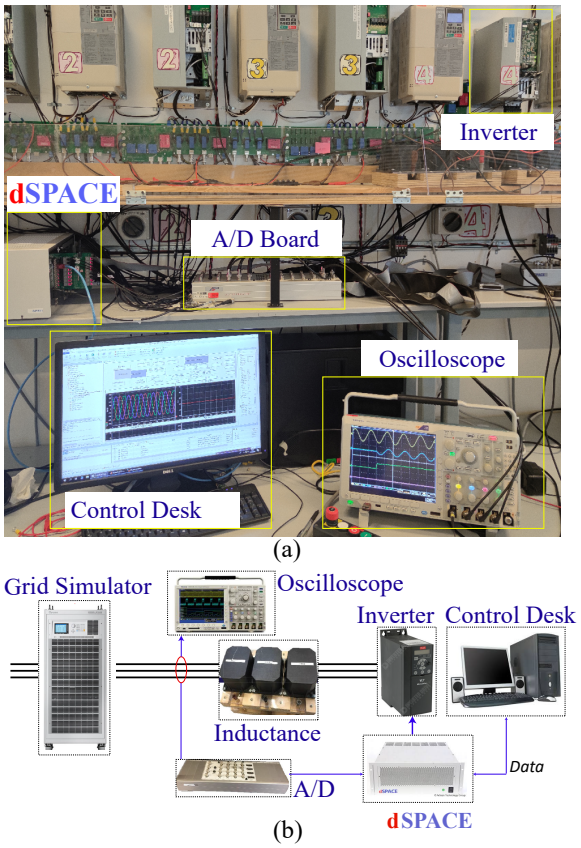

Fig. 19. Experimental setup at Aalborg University.

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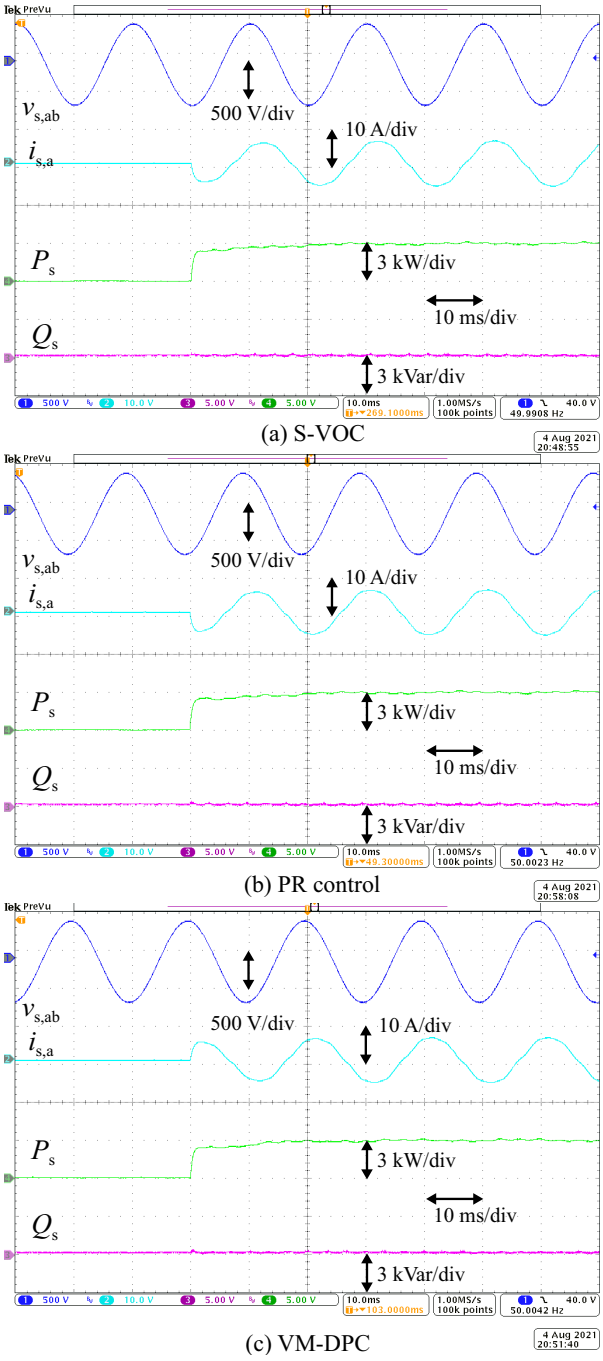

Fig. 20. Transient dynamics comparison of three different control methods. (a) S-VOC. (b) PR. (c) VM-DPC.

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