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A Decomposed Two-Port Network Impedance Modeling Method of Type-3 Wind Generation System with Grid-Forming Control

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Abstract—The doubly-fed induction generator (DFIG) has been widely used in the field of wind generation. However, due to the couplings on both dc-side and ac-side, the full-order small-signal impedance model of the DFIG system is hard to build. To solve this problem, this paper proposes a decomposed two-port network impedance modeling method. With this method, the total admittance of the DFIG system is decomposed into four components, and each of them can be calculated independently. Then, a full-order small-signal admittance model can be obtained accordingly. The proposed modeling method has three main advantages: 1) high accuracy; 2) low complexity of the modeling process; 3) the impact of the dc-link coupling can be analyzed intuitively. Finally, this paper provides the ac terminal admittance calculation and measurement results of the DFIG system with grid-forming control for cross-validation.

Keywords—double-fed induction generator, impedance model, dc-link coupling, two-port network, grid-forming control

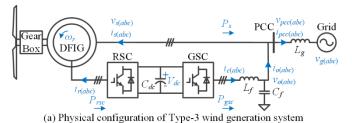
I. INTRODUCTION

As the penetration of wind energy increases, conventional wind generators with grid-following (GFL) control may not fully meet grid code requirements to keep voltage and frequency stability [1]. Hence, grid-forming (GFM) control technology is deemed to be a promising solution for future low-inertia power systems, because it can inherit the voltage source characteristics of synchronous generators (SGs) [2]-[4]. To ensure wind generators can operate stably in power systems, small-signal modeling and stability analysis are key tools to design a stable and robust wind generator system [5].

Currently, Type-3 and Type-4 wind generators are two mainstream types of wind generators [6]. Compared with Type-4 wind generators, Type-3 wind generators have the advantage of lower cost, so they have been widely used in on-shore wind power plants [7]-[10]. For the Type-3 wind generator, the

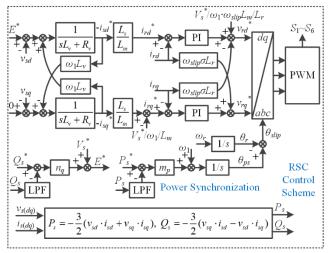
doubly-fed induction generator (DFIG) and the back-to-back converters are connected on both rotor-side and stator-side, so the DFIG stator currents and the grid-side converter (GSC) output currents are influenced by each other. This physical coupling effect makes it difficult to derive the full-order impedance model of the Type-3 wind generators. A conventional modeling method is assuming a constant dc-link voltage by ignoring the dc-link coupling, so that the rotor-side converter (RSC) and the GSC can be modeled separately [11]-[13]. However, the obtained model is not a full-order model, and it lacks accuracy in the low-frequency region. Moreover, to build a full-order impedance model, some improved modeling methods have been proposed [14], [15]. In [14], a full-order impedance model is derived by combining two three-port modules of the GSC and RSC. In [15], a full-order sequence impedance modeling method based on voltage perturbations and current responses is introduced. However, these modeling methods are still too complicated to show physical insights, and impact of the dc-link coupling has not been revealed clearly.

Different from existing methods, this paper proposes a novel decomposed two-port network impedance modeling method for the Type-3 wind generator. With this method, the physically coupled DFIG system can be decomposed into four subsystems and it can be modeled by a 2×2 matrix. Thus, the admittance models of the RSC and the GSC can be represented by the diagonal elements of the matrix, while the dc-link coupling between the RSC and the GSC is able to be reflected by the non-diagonal elements. Each of the four elements in the 2×2 matrix can be calculated independently. Then, a full-order admittance model can be obtained by adding the four elements. The proposed modeling method can be used for Type-3 wind generators with either GFL or GFM control. In this paper, Type-3 wind generators with GFM control are chosen as an example for analysis to show the effectiveness of the proposed method.



 $V_{dc}^{*2} \xrightarrow{PI} i_{cd} \xrightarrow{V_{cd}} V_{cd} \xrightarrow{V_{cd}} dq$ $V_{dc}^{*2} \xrightarrow{V_{dc}^{*2}} V_{cd} \xrightarrow{V_{cd}^{*}} V_{cd} \xrightarrow{V_{cd}^{*}} dq$ $V_{dc}^{*2} \xrightarrow{V_{cd}^{*2}} V_{cd} \xrightarrow{V_{cd}^{*2}} V_{cd} \xrightarrow{V_{cd}^{*2}} V_{cd} \xrightarrow{V_{cd}^{*2}} Qq$ $V_{dc}^{*2} \xrightarrow{V_{cd}^{*2}} V_{cd} \xrightarrow{V_{cd}^{*2}} Qq$ $V_{dc}^{*2} \xrightarrow{V_{cd}^{*2}} V_{cd} \xrightarrow{V_{cd}^{*2}} Qq$ $V_{dc}^{*2} \xrightarrow{V_{cd}^{*2}} V_{cd} \xrightarrow{V_{cd}^{*2}} Qq$ $V_{cd}^{*2} \xrightarrow{V_{cd}^$

(b) PLL-based dc voltage control scheme on the GSC



(c) Virtual-admittance-based GFM control scheme on the RSC

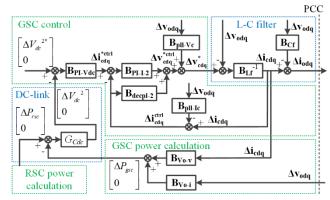
Fig. 1. Typical GFM control scheme for Type-3 wind generation systems.

The rest of this paper is organized as follows. Section II introduces a typical Type-3 wind generation system with grid-forming control, which is selected in this paper for study. Section III introduces a new two-port network-based decomposed impedance modeling method. Then, the calculated results and frequency scanning results of the terminal admittance by simulation are presented in Section IV. Finally, this paper is concluded in Section V.

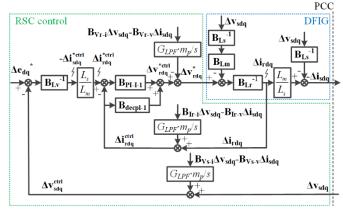
II. STUDY SYSTEM

A. Configurations of Typical Grid-Forming Control Scheme for Type-3 Wind Generation System

The physical configuration of Type-3 wind generation system is shown in Fig. 1(a), which includes a DFIG, an RSC, and a GSC. The reference directions of all variables are defined in Fig. 1(a). Besides, the detailed control schemes of the GSC and RSC are presented in Fig. 1(b) and (c), respectively.



(a) Small-signal d-q impedance model of GSC



(b) Small-signal d-q impedance model of RSC+DFIG

Fig. 2. Small-signal impedance model of GSC and RSC+DFIG.

Fig. 1(b) shows a typical PLL-based dc voltage control scheme on the GSC [16], which includes an inner current control loop, a dc voltage control loop, and a PLL.

Moreover, Fig. 1(c) shows a typical virtual-admittancebased GFM control scheme on the RSC [5], where an inner current control loop, an outer voltage control loop, and a power synchronization control loop are included.

As shown in Fig. 1, the Type-3 wind generator can be divided into two subsystems (i.e., the RSC-side subsystem and GSC-side subsystem). The small-signal impedance models of two subsystems in the frequency domain are modeled in Fig. 2, where the symbol **B** represents a 2×2 matrix, and the symbol *G* represents a transfer function (See the Appendix). Since the dclink voltage is controlled by the GSC, the model of the dc-link is included in the GSC-side subsystem, as shown in Fig. 2(a). Besides, considering the DFIG is controlled by the RSC, the model of the DFIG is included in the RSC-side subsystem, as shown in Fig. 2(b). Then, modeling methods of the whole system will be introduced in the following section.

III. PROPOSED DECOMPOSED TWO-PORT NETWORK IMPEDANCE MODELING METHOD

Since the GSC and the RSC are coupled on both dc-side and ac-side in the DFIG system, it is hard to derive its full-order small-signal impedance model. In this section, port network analysis will be used to solve this problem.

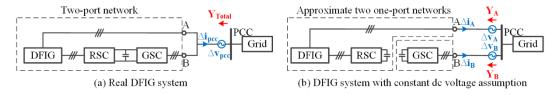


Fig. 3. Conventional approximate impedance modeling method for the DFIG system $(Y_{Total} \approx Y_A + Y_B)$.

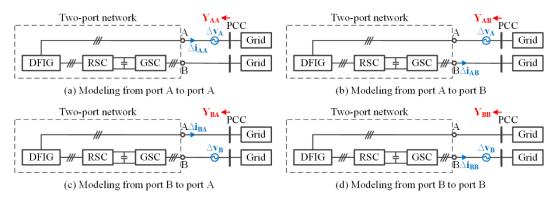


Fig. 4. Proposed two-port-network-based full-order impedance modeling method $(Y_{Total} = Y_{AA} + Y_{AB} + Y_{BA} + Y_{BB})$.

A. Conventional Impedance Modeling Method of DFIG System by Ignoring DC-link Voltage Dynamics

As presented in Fig. 3(a), the real DFIG system can be represented by a two-port network. When injecting a small-signal voltage perturbation $\Delta \mathbf{v}_{pcc}$ at the point of common coupling (PCC), there is a small-signal current response $\Delta \mathbf{i}_{pcc}$ at the PCC. Thus, the total ac terminal admittance \mathbf{Y}_{Total} of the DFIG system can be obtained according to $\Delta \mathbf{v}_{pcc}$ and $\Delta \mathbf{i}_{pcc}$. However, due to the coupling between port A and port B, it is hard to derive the analytical expression of \mathbf{Y}_{Total} . To overcome this difficulty, a conventional impedance modeling method is shown in Fig. 3(b), where the dc-link voltage is assumed to be constant [11]-[13]. Thus, the original two-port network can be separated into two one-port networks, and each one-port network can be modeled easily. However, the obtained model is not a full-order model, and part of the accuracy is lost by using this approximate method.

B. Proposed Decomposed Impedance Modeling Method

Different from existing modeling methods, a decomposed two-port network impedance modeling method for the DFIG system is proposed in this paper, as shown in Fig. 4, where the ac-side of the DFIG system is decoupled. Port A and port B are connected to two identical grids, respectively. Thus, the four admittance components YAA, YAB, YBA, and YBB of the two-port network can be derived separately. The relationship between the total ac terminal admittance YTotal and the four admittance components is given by (1). The detailed expressions of YAA, YAB, YBA, and YBB will be introduced as follows.

$$\begin{split} \mathbf{Y}_{Total} &= \frac{-\Delta \mathbf{i}_{pcc}}{\Delta \mathbf{v}_{pcc}} = \frac{-(\Delta \mathbf{i}_{AA} + \Delta \mathbf{i}_{AB} + \Delta \mathbf{i}_{BA} + \Delta \mathbf{i}_{BB})}{\Delta \mathbf{v}_{pcc}} \\ &= \mathbf{Y}_{AA} + \mathbf{Y}_{AB} + \mathbf{Y}_{BA} + \mathbf{Y}_{BB} \end{split} \tag{1}$$

As shown in Fig. 4(a), when small-signal voltage perturbation Δv_A is injected into the two-port network system from port A, there is a current response Δi_{AA} at port A. Thus, the admittance Y_{AA} can be derived as:

$$\mathbf{Y}_{\mathbf{A}\mathbf{A}}^{\mathbf{d}\mathbf{q}}(s) = \frac{-\Delta \mathbf{i}_{\mathbf{A}\mathbf{A}}^{\mathbf{d}\mathbf{q}}(s)}{\Delta \mathbf{v}_{\mathbf{A}}^{\mathbf{d}\mathbf{q}}(s)} = \frac{\Delta \mathbf{i}_{\mathbf{s}\mathbf{d}\mathbf{q}}(s)}{\Delta \mathbf{v}_{\mathbf{s}\mathbf{d}\mathbf{q}}(s)}$$
(2)

where Δi_{sdq} and Δv_{sdq} are the stator current and stator voltage.

According to Fig. 2(b), the admittance Y_{AA} in (2) can be deduced as:

$$\mathbf{Y}_{AA}^{dq}(s) = \{ \frac{L_{s}}{L_{m}} (\mathbf{B}_{Lr} + \mathbf{B}_{PI-I-1} - \mathbf{B}_{decpl-1}) + G_{LPF} \cdot \frac{m_{p}}{s}$$

$$\cdot [\mathbf{B}_{Vr-v} + (\mathbf{B}_{PI-I-1} - \mathbf{B}_{decpl-1}) \cdot \mathbf{B}_{Ir-v} + \frac{L_{s}}{L_{m}} \cdot \mathbf{B}_{PI-I-1} \cdot \mathbf{B}_{Lr}^{-1}$$

$$\cdot [\mathbf{B}_{Vs-v}] \}^{-1} \cdot \{ \mathbf{B}_{Lm} \mathbf{B}_{Ls}^{-1} + \frac{L_{s}}{L_{m}} \cdot \mathbf{B}_{PI-I-1} \cdot \mathbf{B}_{Lv}^{-1} + \frac{L_{s}}{L_{m}} (\mathbf{B}_{Lr}$$

$$+ \mathbf{B}_{PI-I-1} - \mathbf{B}_{decpl-1}) \cdot \mathbf{B}_{Ls}^{-1} + G_{LPF} \cdot \frac{m_{p}}{s} \cdot [\mathbf{B}_{Vr-i} + (\mathbf{B}_{PI-I-1} - \mathbf{B}_{decpl-1}) \cdot \mathbf{B}_{Ir-i} + \frac{L_{s}}{L_{m}} \cdot \mathbf{B}_{PI-I-1} \cdot \mathbf{B}_{Lv}^{-1} \cdot \mathbf{B}_{Vs-i}] \}$$

where the detailed expressions of the matrixes are listed in (A1) - (A12) in the Appendix.

Moreover, as shown in Fig. 4(b), when small-signal voltage perturbation Δv_A is injected into the two-port network system from port A, there is a current response Δi_{AB} at port B due to the dc-link coupling. Hence, the admittance Y_{AB} can be expressed as (4).

$$\mathbf{Y}_{AB}^{dq}(s) = \frac{-\Delta \mathbf{i}_{AB}^{dq}(s)}{\Delta \mathbf{v}_{A}^{dq}(s)} = \frac{-\Delta \mathbf{i}_{odq}(s)}{\Delta \mathbf{v}_{sdq}(s)}$$
(4)

where Δi_{odq} is the output current of the GSC and Δv_{sdq} is the stator voltage of the DFIG.

In Fig. 2(a), assuming that $\Delta \mathbf{v}_{odq}$ is equal to zero, the transfer function from ΔP_{rsc} to $\Delta \mathbf{i}_{odq}$ can be deduced as:

$$\Delta \mathbf{i}_{odq}(s) = -(\mathbf{B}_{Lf} + \mathbf{B}_{PI-I-2} - \mathbf{B}_{PI-I-2} \mathbf{B}_{PI-Vde} G_{Cdc} \mathbf{B}_{Vo-V}$$

$$-\mathbf{B}_{decpl-2})^{-1} \cdot (\mathbf{B}_{PI-I-2} \mathbf{B}_{PI-Vde} G_{Cdc}) \cdot \begin{bmatrix} \Delta P_{rsc} \\ 0 \end{bmatrix}$$
(5)

Since the output power of the RSC is basically the same as the output power of the DFIG on the rotor side, the small-signal expression of the power ΔP_{rsc} is given by (6).

$$\begin{bmatrix} \Delta P_{rsc} \\ 0 \end{bmatrix} = -\frac{3}{2} \cdot \left\{ \begin{bmatrix} i_{rd0} & i_{rq0} \\ 0 & 0 \end{bmatrix} \cdot \begin{bmatrix} \Delta v_{rd} \\ \Delta v_{rq} \end{bmatrix} + \begin{bmatrix} v_{rd0} & v_{rq0} \\ 0 & 0 \end{bmatrix} \cdot \begin{bmatrix} \Delta i_{rd} \\ \Delta i_{rq} \end{bmatrix} \right\} (6)$$

where subscript '0' represents steady-state operating points.

Then, according to the stator/rotor voltage and flux expressions of the DFIG system listed in (7), the power ΔP_{rsc} in (6) can be derived as (8).

$$\begin{cases} v_{rd} = R_r i_{rd} + s \psi_{rd} - \omega_{slip} \psi_{rq} \\ v_{rq} = R_r i_{rq} + s \psi_{rq} + \omega_{slip} \psi_{rd} \\ \psi_{sd} = L_s i_{sd} + L_m i_{rd} \\ \psi_{sq} = L_s i_{sq} + L_m i_{rq} \\ \psi_{rd} = L_r i_{rd} + L_m i_{sd} \\ \psi_{rq} = L_r i_{rq} + L_m i_{sq} \end{cases}$$

$$(7)$$

$$\begin{bmatrix} \Delta P_{rsc} \\ 0 \end{bmatrix} = -\frac{3}{2} \cdot \left\{ \begin{bmatrix} (L_m^2 - L_s L_r) s - 2R_r L_s \\ L_m \end{bmatrix} \cdot \begin{bmatrix} i_{rd0} & i_{rq0} \\ 0 & 0 \end{bmatrix} - \frac{\omega_{slip}}{\omega_l} \cdot \begin{bmatrix} v_{sd0} & v_{sq0} \\ 0 & 0 \end{bmatrix} \right\} \cdot \begin{bmatrix} \Delta i_{sd} \\ \Delta i_{sq} \end{bmatrix} + \begin{bmatrix} L_r s + 2R_r \\ L_m \end{bmatrix} \cdot \begin{bmatrix} i_{rd0} & i_{rq0} \\ 0 & 0 \end{bmatrix} \quad (8)$$

$$+ \omega_{slip} \cdot \begin{bmatrix} -i_{sq0} & i_{sd0} \\ 0 & 0 \end{bmatrix} \cdot \begin{bmatrix} s & -\omega_l \\ \omega_l & s \end{bmatrix}^{-1} \cdot \begin{bmatrix} \Delta v_{sd} \\ \Delta v_{sq} \end{bmatrix} \right\}$$

It can be seen from (2) that Δi_{sdq} can be expressed by " $Y_{AA} \cdot \Delta v_{sdq}$ ". So, (8) can be derived as (9).

$$\begin{bmatrix} \Delta P_{rsc} \\ 0 \end{bmatrix} = -\frac{3}{2} \cdot \left\{ \begin{bmatrix} (L_m^2 - L_s L_r) s - 2R_r L_s \\ L_m \end{bmatrix} \cdot \begin{bmatrix} i_{rd0} & i_{rq0} \\ 0 & 0 \end{bmatrix} \right\}$$

$$-\frac{\omega_{slip}}{\omega_l} \cdot \begin{bmatrix} v_{sd0} & v_{sq0} \\ 0 & 0 \end{bmatrix} \cdot \mathbf{Y}_{AA}^{\mathbf{dq}}(s) + \begin{bmatrix} L_r s + 2R_r \\ L_m \end{bmatrix} \cdot \begin{bmatrix} i_{rd0} & i_{rq0} \\ 0 & 0 \end{bmatrix}$$

$$+\omega_{slip} \cdot \begin{bmatrix} -i_{sq0} & i_{sd0} \\ 0 & 0 \end{bmatrix} \cdot \begin{bmatrix} s & -\omega_l \\ \omega_l & s \end{bmatrix}^{-1} \right\} \cdot \Delta \mathbf{v}_{\mathbf{sdq}}(s)$$

$$(9)$$

Substituting (5) and (9) into (4), the admittance Y_{AB} can be deduced as:

$$\mathbf{Y}_{AB}^{dq}(s) = \frac{-3}{2} \cdot (\mathbf{B}_{Lf} + \mathbf{B}_{PI-I-2} - \mathbf{B}_{PI-I-2} \mathbf{B}_{PI-Vdc} G_{Cdc} \mathbf{B}_{Vo-v} \\
-\mathbf{B}_{decpl-2})^{-1} \cdot (\mathbf{B}_{PI-I-2} \mathbf{B}_{PI-Vdc} G_{Cdc}) \cdot \left\{ \begin{bmatrix} (L_{m}^{2} - L_{s} L_{r}) s - 2R_{r} L_{s} \\ L_{m} \end{bmatrix} \right. \\
\cdot \begin{bmatrix} i_{rd0} & i_{rq0} \\ 0 & 0 \end{bmatrix} - \frac{\omega_{slip}}{\omega_{l}} \cdot \begin{bmatrix} v_{sd0} & v_{sq0} \\ 0 & 0 \end{bmatrix} \right] \cdot \mathbf{Y}_{AA}^{dq}(s) + \begin{bmatrix} L_{r} s + 2R_{r} \\ L_{m} \end{bmatrix} \cdot \begin{bmatrix} i_{rd0} & i_{rq0} \\ 0 & 0 \end{bmatrix} + \omega_{slip} \cdot \begin{bmatrix} -i_{sq0} & i_{sd0} \\ 0 & 0 \end{bmatrix} \right] \cdot \begin{bmatrix} s & -\omega_{l} \\ \omega_{l} & s \end{bmatrix}^{-1} \right\}$$

where the detailed expressions of the matrixes are listed in (A13) - (A18) in the Appendix.

In addition, as presented in Fig. 4(c), when small-signal voltage perturbation Δv_B is injected into the two-port network system from port B, there is a current response Δi_{BA} at port A. So, the admittance Y_{BA} can be expressed as:

$$\mathbf{Y}_{\mathrm{BA}}^{\mathrm{dq}}(s) = \frac{-\Delta \mathbf{i}_{\mathrm{BA}}^{\mathrm{dq}}(s)}{\Delta \mathbf{v}_{\mathrm{R}}^{\mathrm{dq}}(s)} = \frac{\Delta \mathbf{i}_{\mathrm{sdq}}(s)}{\Delta \mathbf{v}_{\mathrm{odg}}(s)}$$
(11)

where Δv_{odq} is the output voltage of the GSC, and Δi_{sdq} is the stator current of the DFIG.

Since the measured value of dc-link voltage is used for the voltage modulation on the RSC, the output voltage of the RSC is independent from the dc-link voltage. Hence, the injected small-signal perturbation at port B can only be transferred to the dc-link, but it cannot be transferred to port A. So, Δi_{BA} is equal to zero and the admittance Y_{BA} can be deduced as:

$$\mathbf{Y}_{\mathrm{BA}}^{\mathrm{dq}}(s) = \frac{\mathbf{0}}{\mathbf{\Delta}\mathbf{v}_{\mathrm{p}}^{\mathrm{dq}}(s)} = \mathbf{0} \tag{12}$$

Furthermore, as presented in Fig. 4(d), when small-signal voltage perturbation Δv_B is injected into the two-port network system from port B, there is a current response Δi_{BB} at port B. Thus, the admittance Y_{BB} can be given by (13).

$$\mathbf{Y}_{\mathbf{BB}}^{\mathbf{dq}}(s) = \frac{-\Delta \mathbf{i}_{\mathbf{BB}}^{\mathbf{dq}}(s)}{\Delta \mathbf{v}_{\mathbf{B}}^{\mathbf{dq}}(s)} = \frac{-\Delta \mathbf{i}_{\mathbf{odq}}(s)}{\Delta \mathbf{v}_{\mathbf{odq}}(s)}$$
(13)

where $\Delta \mathbf{v}_{odq}$ and $\Delta \mathbf{i}_{odq}$ are the voltage and current of the GSC.

In Fig. 2(a), assuming that ΔP_{rsc} is equal to zero, the transfer function from $\Delta \mathbf{v}_{odq}$ to $\Delta \mathbf{i}_{odq}$ can be deduced as:

$$\Delta \mathbf{i}_{odq}(s) = -(\mathbf{B}_{Lf} + \mathbf{B}_{PI-I-2} - \mathbf{B}_{PI-I-2} \mathbf{B}_{PI-Vdc} G_{Cdc} \mathbf{B}_{Vo-v}$$

$$-\mathbf{B}_{deepl-2})^{-1} \cdot [\mathbf{I} - (\mathbf{B}_{PI-I-2} - \mathbf{B}_{deepl-2}) \mathbf{B}_{pll-Ic} - \mathbf{B}_{pll-Vc}$$

$$-\mathbf{B}_{PI-I-2} \mathbf{B}_{PI-Vdc} G_{Cdc} \mathbf{B}_{Vo-i}] \cdot \Delta \mathbf{v}_{odq}(s) - \mathbf{B}_{Cf} \cdot \Delta \mathbf{v}_{odq}(s)$$
(14)

Substituting (14) into (13), Y_{BB} can be derived by (15).

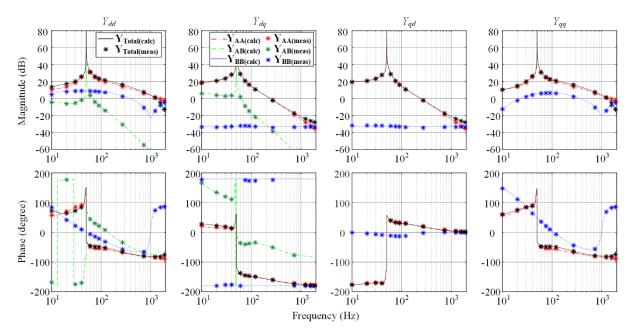


Fig. 5. Admittance measurement results of a GFM Type-3 wind generator under sub-synchronous speed ($\omega_r = 40$ Hz).

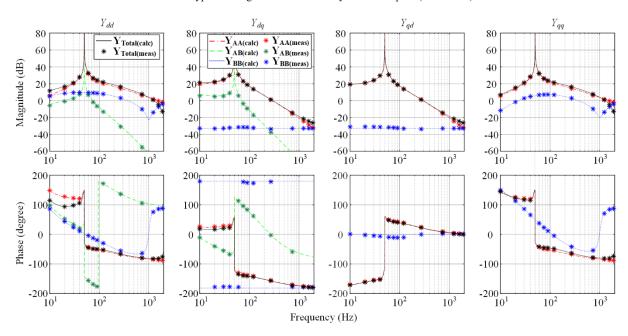


Fig. 6. Admittance measurement results of a GFM Type-3 wind generator under super-synchronous speed ($\omega_r = 60 \text{ Hz}$).

$$\mathbf{Y}_{BB}^{dq}(s) = (\mathbf{B}_{Lf} + \mathbf{B}_{PI-I-2} - \mathbf{B}_{PI-I-2} \mathbf{B}_{PI-Vdc} G_{Cdc} \mathbf{B}_{Vo-v} - \mathbf{B}_{decpl-2})^{-1} \cdot [\mathbf{I} - (\mathbf{B}_{PI-I-2} - \mathbf{B}_{decpl-2}) \mathbf{B}_{pll-Ic} - \mathbf{B}_{pll-Vc} - \mathbf{B}_{PI-I-2} \mathbf{B}_{PI-Vdc} G_{Cdc} \mathbf{B}_{Vo-i}] + \mathbf{B}_{Cf}$$
(15)

where the detailed expressions of the matrixes are listed in (A13) - (A22) in the Appendix.

Overall, the admittance components Y_{AA} , Y_{AB} , Y_{BA} , and Y_{BB} are obtained in (3), (10), (12), and (15). Then, the total

admittance Y_{Total} can also be obtained by adding the four admittance components Y_{AA}, Y_{AB}, Y_{BA}, and Y_{BB}.

IV. SIMULATION VERIFICATION

To verify the effectiveness of the proposed method, the simulation model of a 2 MW GFM Type-3 wind generator is built in Matlab/Simulink. The parameters of the DFIG and the RSC are listed in Table I and the parameters of the GSC are presented in Table II. In this section, the frequency scanning results and the calculated results of the terminal admittance will be provided for cross-validation.

TABLE I. PARAMETERS OF DFIG AND RSC

Parameters	Values
Rated phase voltage (peak value), V_{N1}	563 V (1 p.u.)
Rated angular frequency, ω_1	2π·50 rad/s (1 p.u.)
Rated active power of DFIG, P_{N1}	2 MW (1 p.u.)
Rotor speed, ω_r	$40\sim60\;Hz$
Pole pairs, n_p	2
Stator resistance, R_s	1.7 mΩ (0.007 p.u.)
Rotor resistance, R_r	1.5 mΩ (0.006 p.u.)
Stator leakage inductance, $L_{\sigma s}$	0.038 mH (0.05 p.u.)
Rotor leakage inductance, $L_{\sigma r}$	0.064 mH (0.08 p.u.)
Mutual inductance, L_m	2.9 mH (3.83 p.u.)
Designed current-loop bandwidth, ω_{i1}	2000 rad/s
Virtual inductance, L_{ν}	0.5 p.u.
Virtual resistance, R_{ν}	0.05 p.u.
Cut-off angular frequency of LPFs, ω_{LPF}	300 rad/s
Active power droop coefficient, m_p	$2.5\%\cdot\omega_{ m N}/P_{ m N}$
Reactive power droop coefficient, n_q	5%· $V_{ m N}/P_{ m N}$

TABLE II. PARAMETERS OF GSC

Parameters	Values
Rated phase voltage (peak value), V_{N2}	563 V (1 p.u.)
Rated active power of GSC, P_{N2}	667 kW (1/3 p.u.)
Rated dc-link voltage, V_{dc}	1.1 kV
Dc-link capacitance, C_{dc}	10 mF
Filter inductance, L_f	0.34 mH
Filter resistance, R_f	3.6 mΩ
Filter capacitance, C_f	75 μF
Designed current-loop bandwidth, ω_{i2}	2000 rad/s
Damping ratio of dc voltage loop, ζ_{Vdc}	1
Natural frequency of dc voltage loop, ω_{nVdc}	40 rad/s
Damping ratio of PLL, ζ_{pll}	1
Natural frequency of PLL, ω_{npll}	200 rad/s

Same as [4], zero power condition is used in this paper for analysis. The d-q admittance measurement results by frequency scanning under sub-synchronous speed with $\omega_r = 40$ Hz and super-synchronous speed with $\omega_r = 60$ Hz are shown in Fig. 5 and Fig. 6 respectively, where the subscripts 'calc' and 'meas' denote calculation and measurement. Besides, the red, green, and blue curves show the d-q admittance characteristics of \mathbf{Y}_{AA} , \mathbf{Y}_{AB} , and \mathbf{Y}_{BB} by calculation (i.e., equations (3), (10), and (15)). Since \mathbf{Y}_{BA} is equal to 0, it is omitted in Fig. 5 and Fig. 6. Moreover, the black curves show the admittance characteristics of \mathbf{Y}_{Total} , which is equal to the sum of \mathbf{Y}_{AA} , \mathbf{Y}_{AB} , \mathbf{Y}_{BA} and \mathbf{Y}_{BB} .

It can be seen from Fig. 5 and Fig. 6 that the measured admittances are basically overlapped with the calculated admittances in both sub-synchronous speed and supersynchronous speed cases, which demonstrates the correctness of the proposed method.

In addition, it can be seen that the dc-link coupling has stronger impact in the low-frequency range, because the magnitude of YAB is relatively higher. However, compared to YAA and YBB, the magnitude of YAB is lower. So, YAA and YBB have higher weights than YAB in the selected study case in this paper. It is worth mentioning that the conclusions may be different in other study cases (e.g., other DFIG systems with different power ratings or different control schemes). Nevertheless, with the proposed method, the weights of YAA, YAB, and YBB can be observed clearly by comparing the magnitudes of them, which is beneficial for the model reduction and simplification.

V. CONCLUSION

This paper proposes a decoupled two-port-network-based full-order small-signal impedance modeling method for Type-3 wind generators. With this method, the total ac terminal admittance can be decomposed into four admittance components, and each of them can be modeled independently. So, the complexity of the modeling process is degraded. Besides, each admittance component has a physical meaning (i.e., YAA represents the model of the RSC-side subsystem, YBB represents the model of the GSC-side subsystem, and YAB represents the model of the dc-link coupling.) Thus, the impact of the dc-link coupling can be analyzed quantitatively and intuitively. Compared with conventional approximate modeling methods, the proposed modeling method has higher accuracy, because all the information of the system can be captured. The admittance measurement results by simulation have verified the correctness of the proposed modeling method.

APPENDIX

The 2×2 matrixes in the impedance model shown in Fig. 2 are given by (A1) - (A22).

$$\mathbf{B}_{PI-I-1} = \begin{bmatrix} K_{p1} + K_{i1} / s & 0 \\ 0 & K_{p1} + K_{i1} / s \end{bmatrix}$$
 (A1)

$$\mathbf{B}_{\text{decpl-1}} = \begin{bmatrix} 0 & -\omega_{slip}\sigma L_r \\ \omega_{slip}\sigma L_r & 0 \end{bmatrix}$$
 (A2)

$$\mathbf{B}_{Lv} = \begin{bmatrix} sL_v + R_v & -\omega_1 L_v \\ \omega_1 L_v & sL_v + R_v \end{bmatrix}$$
 (A3)

$$\mathbf{B}_{\mathbf{L}s} = \begin{bmatrix} sL_s & -\omega_1 L_s \\ \omega_1 L_s & sL_s \end{bmatrix} \tag{A4}$$

$$\mathbf{B}_{\mathbf{Lm}} = \begin{bmatrix} sL_m & -\omega_{slip}L_m \\ \omega_{slip}L_m & sL_m \end{bmatrix}$$
 (A5)

$$\mathbf{B_{Lr}} = \begin{bmatrix} s\sigma L_r + R_r & -\omega_{slip}\sigma L_r \\ \omega_{slip}\sigma L_r & s\sigma L_r + R_r \end{bmatrix}$$
(A6)

$$\mathbf{B}_{Vs-i} = \frac{3}{2} \begin{bmatrix} v_{sq0}i_{sd0} & v_{sq0}i_{sq0} \\ -v_{sd0}i_{sd0} & -v_{sd0}i_{sq0} \end{bmatrix}$$
(A7)

$$\mathbf{B_{Vs-v}} = \frac{3}{2} \begin{bmatrix} -v_{sq0}v_{sd0} & -v_{sq0}v_{sq0} \\ v_{sd0}v_{sd0} & v_{sd0}v_{sq0} \end{bmatrix}$$
(A8)

$$\mathbf{B_{Vr-i}} = \frac{3}{2} \begin{bmatrix} v_{rq0} i_{sd0} & v_{rq0} i_{sq0} \\ -v_{rd0} i_{sd0} & -v_{rd0} i_{sq0} \end{bmatrix}$$
(A9)

$$\mathbf{B_{v_{r-v}}} = \frac{3}{2} \begin{bmatrix} -v_{rq0}v_{sd0} & -v_{rq0}v_{sq0} \\ v_{rd0}v_{sd0} & v_{rd0}v_{sq0} \end{bmatrix}$$
(A10)

$$\mathbf{B}_{\mathbf{Ir-i}} = \frac{3}{2} \begin{bmatrix} i_{rq0}i_{sd0} & i_{rq0}i_{sq0} \\ -i_{rd0}i_{sd0} & -i_{rd0}i_{sq0} \end{bmatrix}$$
(A11)

$$\mathbf{B_{Ir-v}} = \frac{3}{2} \begin{bmatrix} -i_{rq0} v_{sd0} & -i_{rq0} v_{sq0} \\ i_{rd0} v_{sd0} & i_{rd0} v_{sq0} \end{bmatrix}$$
(A12)

$$\mathbf{B}_{PI-I-2} = \begin{bmatrix} K_{p2} + K_{i2} / s & 0 \\ 0 & K_{p2} + K_{i2} / s \end{bmatrix}$$
 (A13)

$$\mathbf{B}_{\text{decpl-2}} = \begin{bmatrix} 0 & -\omega_1 L_f \\ \omega_1 L_f & 0 \end{bmatrix} \tag{A14}$$

$$\mathbf{B}_{Lf} = \begin{bmatrix} sL_f + R_f & -\omega_1 L_f \\ \omega_1 L_f & sL_f + R_f \end{bmatrix}$$
 (A15)

$$\mathbf{B}_{\mathbf{PI-Vdc}} = \begin{bmatrix} -K_{p_{-}Vdc} - K_{i_{-}Vdc} / s & 0\\ 0 & 0 \end{bmatrix}$$
 (A16)

$$G_{Cdc} = \frac{1}{(C_{dc}/2)s}$$
 (A17)

$$\mathbf{B}_{\mathbf{Vo-v}} = \frac{3}{2} \begin{bmatrix} v_{od0} & v_{oq0} \\ 0 & 0 \end{bmatrix} \tag{A18}$$

$$\mathbf{B}_{\mathbf{Vo-i}} = \frac{3}{2} \begin{bmatrix} i_{cd0} & i_{cq0} \\ 0 & 0 \end{bmatrix}$$
 (A19)

$$\mathbf{B}_{\mathbf{pll-Vc}} = \begin{bmatrix} 0 & -v_{cq0} \cdot G_{pll} / V_o \\ 0 & v_{cd0} \cdot G_{pll} / V_o \end{bmatrix}$$
(A20)

$$\mathbf{B}_{\text{pll-lc}} = \begin{bmatrix} 0 & -i_{cq0} \cdot G_{pll} / V_o \\ 0 & i_{cd0} \cdot G_{pll} / V_o \end{bmatrix}$$
(A21)

$$\mathbf{B}_{\mathbf{Cf}} = \begin{bmatrix} sC_f & -\omega_1 C_f \\ \omega_1 C_f & sC_f \end{bmatrix}$$
 (A22)

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