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A Robust Circuit and Controller Parameters Identification Method of Grid-Connected Voltage Source Converters Using Vector Fitting Algorithm

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Abstract—This article presents a vector fitting (VF) algorithm-based robust circuit and controller parameters identification method for grid-connected voltage source converters (VSCs). The dq -domain impedance frequency responses (IFRs) of the VSCs are first measured using frequency scanning method, based on which the corresponding measured phasor-domain IFRs are calculated. Then, polynomial transfer functions are generated by applying the VF algorithm on the measured phasor-domain IFRs, from which the circuit and controller parameters, i.e., LCL filter parameters, digital sampling time, current controller parameters, and phase-locked loop parameters, are identified. Influence of measurement noise on parameters identification accuracy and corresponding countermeasure to mitigate the adverse influence are also theoretically investigated. The proposed method is able to identify the circuit and controller parameters, when detailed parameters are missing due to industrial secrecy or parameters variation caused by operating condition change, temperature fluctuation, or aging. Effectiveness of the proposed circuit and controller parameters identification method is validated by theoretical demonstration, OPAL-RT-based real-time simulation, and experimental validation.

Index Terms—Impedance frequency responses, parameters identification, polynomial transfer function, vector fitting algorithm, voltage source converter.

I. INTRODUCTION

Renewable energy sources, such as wind power and solar power, have been increasingly penetrating into conventional

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power systems [2]. Grid-connected voltage source converters (VSCs), as the power electronic interface, are widely used to transmit the generated electrical energy into utility grid [3], [4]. It's important to know the accurate circuit and controller parameters of the VSCs to identify instability sources [5]–[9], to perform adaptive control [10]–[12], and to perform condition monitoring and fault diagnosis [13], [14]. However, subsystems of such a distributed power system are commonly provided by various manufacturers. Therefore, circuit and controller parameters of the VSCs are sometimes confidential due to industry secrecy and intellectual property rights [1], [15]. In addition, even if the nominal values are provided by the manufactures, parameters uncertainties of filter inductances and capacitances still exist due to manufacturing tolerance, operating condition variation, temperature fluctuation, and aging [10], [12], [16], [17].

Various methods have been proposed to estimate or identify the parameters of the VSCs [10], [11], [13], [14], [18]–[21]. The equivalent loss resistance between the VSC and grid, and both equivalent inductance and resistance between the VSC and grid are estimated based on evaluation of closed-loop transient responses of the current controller in [18] and [19], respectively. In addition, the active and reactive power-based model reference adaptive control approach is used to estimate the equivalent inductance and resistance between the VSC and grid in [11]. Furthermore, the parameters of grid equivalent, AC filter, switching, and conduction loss resistance are estimated using the extended harmonic domain in [20]. However, the parameter estimation methods proposed in [11], [18]–[20] are relatively complicated. In addition, only physical parameters can be estimated, whereas the controller parameters are still missing. Pseudorandom binary sequence is injected into current control loop of the VSCs, and the impedance information is obtained by performing the fast Fourier transform (FFT) on perturbed terminal voltage and output current, from which the parameters of L filter, transmission line impedance, and LCL filter are identified in [10], [13], and [14] respectively. The pseudorandom binary sequence-based parameter identification methods are relatively easy, since the perturbation signal is only composed of two electrical levels, i.e., $+1$ V and -1 V, which can be easily generated with a modest digital controller. However, only circuit parameters instead of controller parameters can be identified. A two-step parameters identification method is proposed in [21], where step one uses a three-phase fault to identify all voltage loop

parameters and proportional coefficient of current loop, and step two uses dc voltage reference jump disturbance to identify the integral coefficient of current loop and inductance of the L -filter. However, how to modify the two-step parameters identification method for LCL -filtered VSC is not explained in [21]. In addition, the implementation procedure is time-consuming.

Circuit or controller parameters of the VSCs are extracted from terminal impedance frequency responses (IFRs) in [1], [7], [9], [22]. Specifically, the concept is originally presented in [7], where parameters of dc voltage controller, current controller, and phase-locked loop (PLL) are simultaneously identified by equalizing theoretical and measured terminal IFRs under the assumption that the control structure is known. However, filter parameters are assumed to be known. In addition, detailed parameters extraction procedure is missing, which cannot provide the guideline on how to linearize the nonlinear elements (e.g., digital time delay) to enforce the theoretical impedance transfer function to be in the same form of fitted impedance transfer function which is commonly a linear polynomial transfer function. In [9], [22], we developed the concept to further identify the LCL filter parameters and digital sampling time of grid-side-current-controlled VSCs with capacitor current feedback active damping, where linearization of the digital time delay is one main focus. However, the method presented in [9], [22] is not robust, since influence of practical measurement noise on parameters identification accuracy and corresponding countermeasure to reduce the adverse influence are missing. The research gap is further filled in [1], where the adverse influence of measurement noise is weakened by fitting the measured IFRs with higher orders. However, only simple inner current control loop is investigated in [1], [9], [22]. How to further identify parameters of other controllers, e.g., PLL, should be further studied. In addition, further practical application of the identification results, e.g., controller parameters re-tuning for stability enhancement of the VSC-grid system, is missing in [1].

The frequency scanning method has widely been used to online measure grid impedance [23]–[27], and to online measure dq -domain IFRs of the VSCs [28]–[30]. The measured dq -domain IFRs are directly used for impedance-based stability criteria (IBSC) in [23], [24], [27], [29], [30]. Specifically, in [25], [28], parametric transfer functions are generated from the non-parametric IFRs using wideband system identification technique, based on which grid resistance, inductance, and capacitance are identified. In addition, discrete dq -domain IFRs of the VSCs are fitted into a parametric transfer function in [15], [31]–[33]. However, no circuit and controller parameters of the VSC are further identified in [15], [25], [28], [31]–[33]. Furthermore, the state-space model of the VSC is generated from phasor-domain IFRs using the vector fitting (VF) algorithm in [5], [6], [34], and from dq -domain IFRs using the matrix fitting (MF) algorithm in [35], respectively. However, the state variables of such a state-space representation are virtual, which indicates that actual inner dynamics cannot be revealed.

In order to further develop the identification algorithms proposed in [1], [7], [9]–[11], [13]–[15], [18]–[22], [25], [28],

[31]–[33], this article presents a robust method to identify circuit and controller parameters of the VSCs, i.e., LCL filter parameters, the proportional coefficient of the current controller, PLL controller parameters, and digital sampling time, based on measured dq -domain IFRs of the VSCs. The dq -domain IFRs of the VSC are first obtained by the frequency scanning method explained in [36], from which the phasor-domain IFRs are calculated. A polynomial transfer function can be generated from the phasor-domain IFRs of VSC using the VF algorithm, from which the circuit and controller parameters are then identified. The proposed parameters identification method can be used to identify instability source, to perform adaptive controller re-tuning, and to perform monitoring and fault diagnosis. Main contributions of the article are explained as follows. 1) Effects of current control loop and PLL on the four components of the dq -domain impedance model of the VSC are investigated, which is able to simplify the parameters identification algorithm. 2) A separation method of current control loop-contributed and PLL-contributed dq -domain IFRs is presented, which is able to extract the current control loop-contributed impedance component. 3) An optimal order selection scheme of the Pade approximation for digital time delay under both grid current control (GCC) and converter current control (CCC) modes is presented, which facilitates to represent the impedance model of the VSC as a polynomial transfer function. 4) An adaptive order selection scheme of the fitted transfer function of the IFRs is presented, which is able to mitigate the adverse influence of measurement noise on parameters identification accuracy.

The rest of this article is organized as follows. Section II gives the theoretical background. The principle of the proposed circuit and controller parameters identification method is explained in Section III. Section IV provides the theoretical demonstration results, where the influence of measurement noise on parameters identification accuracy and corresponding countermeasure to mitigate the influence are also discussed. Real-time simulation verification based on OPAL-RT digital simulator and power-hardware-in-the-loop (PHIL)-based experimental validation are shown in Section V. Finally, the conclusion is drawn in Section VI.

II. THEORETICAL BACKGROUND

In this section, analytical expressions of impedance models of the grid-connected VSCs under both GCC and CCC modes are reviewed, where the relations among dq , sequence, and phasor-domain impedance models are explained. In addition, the principle of the VF algorithm is introduced.

A. Analytical Expressions of the Impedance Models in DQ , Sequence, and Phasor-Domains

Fig. 1(a) shows the configuration of a typical grid-connected VSC under GCC mode with PLL. Based on the derivation procedure of the dq -domain impedance model of the L -filtered VSC in [37], the control block diagram of the LCL -filtered VSC under GCC mode is shown in Fig. 1(b), where the matrix

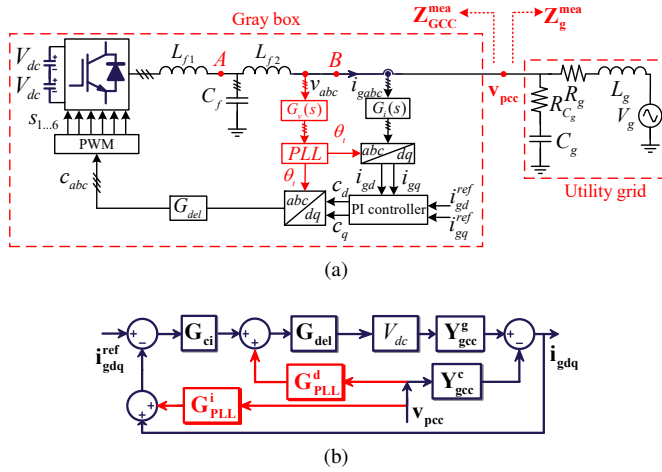


Fig. 1. Typical control strategy of a grid-connected VSC under GCC mode with inner current control loop and PLL. (a) One-line diagram. (b) Control block diagram [37].

representations are listed in the Appendix. The dq -domain impedance model can be derived from Fig. 1(b), shown as

$$\mathbf{Z}_{GCC} = -\left. \frac{\mathbf{v}_{pcc}}{i_{gdq}^{ref}} \right|_{i_{gdq}^{ref}=0} = (\mathbf{Y}_{gcc}^c - \underbrace{V_{dc} \mathbf{Y}_{gcc}^g \mathbf{G}_{del} (-\mathbf{G}_{ci} \mathbf{G}_{PLL}^i + \mathbf{G}_{PLL}^d)}_{\text{The effect of PLL}})^{-1} \cdot (\mathbf{I} + V_{dc} \mathbf{Y}_{gcc}^g \mathbf{G}_{del} \mathbf{G}_{ci}). \quad (1)$$

If PLL dynamic is ignored, (1) can be simplified as

$$\mathbf{Z}_{GCC}^{simp} = \mathbf{Z}_{gcc}^c + V_{dc} \mathbf{Z}_{gcc}^g \mathbf{Y}_{gcc}^g \mathbf{G}_{del} \mathbf{G}_{ci}, \quad (2)$$

where $\mathbf{Z}_{GCC}^{simp} = [Z_{GCCdd}^{simp}, Z_{GCCdq}^{simp}; Z_{GCCqd}^{simp}, Z_{GCCqq}^{simp}]$. Furthermore, the corresponding sequence-domain impedance model $\mathbf{Z}_{GCCpn}^{simp}$ can be calculated as [38]

$$\mathbf{Z}_{GCCpn}^{simp} = \begin{bmatrix} Z_{GCCpp}^{simp} & Z_{GCCpn}^{simp} \\ Z_{GCCnp}^{simp} & Z_{GCCnn}^{simp} \end{bmatrix} = \mathbf{T} \mathbf{Z}_{GCC}^{simp} \mathbf{T}^{-1}, \quad (3)$$

where $\mathbf{T} = \frac{1}{\sqrt{2}}[1, j; 1, -j]$. The phasor-domain impedance model Z_{GCC} can then be derived as [39]

$$Z_{GCC} = Z_{GCCpp}^{simp}(s - j\omega_1) = Z_{GCCnn}^{simp}(s + j\omega_1), \quad (4)$$

where ω_1 is the fundamental angular frequency. Specifically, $Z_{GCC} = Z_{GCCdd}^{simp} = Z_{GCCqq}^{simp}$ and $Z_{GCCdq}^{simp} = -Z_{GCCqd}^{simp}$. By substituting (38), (40), and (41) into (3), Z_{GCC} can be derived as

$$Z_{GCC} = (V_{dc} G_{ci} G_{del} + L_{f1} s) / (1 + L_{f1} C_f s^2) + L_{f2} s. \quad (5)$$

If the current sensing point in Fig. 1(a) is moved from point B to point A, the VSC is under CCC mode. Similarly, the dq -domain impedance model can be derived as

$$\mathbf{Z}_{CCC} = (((\mathbf{Y}_{ccc}^c - \underbrace{V_{dc} \mathbf{Y}_{ccc}^g \mathbf{G}_{del} (-\mathbf{G}_{ci} \mathbf{G}_{PLL}^i + \mathbf{G}_{PLL}^d)}_{\text{The effect of PLL}}))^{-1} \cdot (\mathbf{I} + V_{dc} \mathbf{Y}_{ccc}^g \mathbf{G}_{del} \mathbf{G}_{ci}))^{-1} + \mathbf{Z}_{C_f}^{-1} + \mathbf{Z}_{L_{f2}}. \quad (6)$$

The simplified dq -domain impedance model without considering PLL dynamic is shown as

$$\mathbf{Z}_{CCC}^{simp} = ((\mathbf{Z}_{ccc}^c + V_{dc} \mathbf{Z}_{ccc}^g \mathbf{Y}_{ccc}^g \mathbf{G}_{del} \mathbf{G}_{ci}))^{-1} + \mathbf{Z}_{C_f}^{-1} + \mathbf{Z}_{L_{f2}}. \quad (7)$$

The phasor-domain impedance model can be calculated from the corresponding sequence-domain impedance model which is derived from (7) using (3), shown as

$$Z_{CCC} = ((L_{f1} s + V_{dc} G_{ci} G_{del})^{-1} + C_f s)^{-1} + L_{f2} s. \quad (8)$$

The aforementioned relations among the dq , sequence, and phasor-domain impedance models lay the foundation of the proposed parameters identification method, as shown in Section III-B.

B. VF Algorithm

A series of discrete frequency responses of a single-input-single-output system, e.g., $g(\omega_1), g(\omega_2), \dots, g(\omega_N)$, can be fitted by an s-domain transfer function using the VF algorithm, shown as [40]

$$f(s) = \left(\sum_{i=0}^m B_i s^i \right) / \left(\sum_{i=0}^m A_i s^i \right) + E s, \quad (9)$$

where A_i and B_i ($i \in [0, m]$) are the coefficients of the denominator and numerator polynomials, respectively. E is non-zero only if the order of numerator is higher than the order of denominator. The root-mean-square value of the fitting errors at the N frequency points can be calculated as

$$E_{fit} = \sqrt{\frac{\sum_{i=1}^N (f(\omega_i) - g(\omega_i))^2}{N}}. \quad (10)$$

Note that the enhanced version of the VF algorithm, i.e., the MF algorithm, should be used to fit the frequency responses of a multi-input-multi-output system, which may be more time-consuming and bring in heavier computation burdens [35]. In order to avoid the application of the MF algorithm in the proposed parameters identification method, the measured two-dimensional dq -domain IFRs in form of (1) and (6) will be transferred into the one-dimensional phasor-domain IFRs in form of (5) and (8), as shown in Section III-B.

III. PROPOSED CIRCUIT AND CONTROLLER PARAMETERS IDENTIFICATION METHOD

In this section, characteristics of the four components of the dq -domain admittance model are analyzed. On its basis, principle of the proposed parameters identification method is explained. Finally, the detailed implementation procedure of the proposed method is discussed.

A. Characteristics of the DQ-Domain Admittance Model

(1) can be reformulated as

$$\mathbf{Z}_{GCC} = (\mathbf{I} - \mathbf{G}_{GCC}^{PLL})^{-1} \cdot \mathbf{Z}_{GCC}^{simp}, \quad (11)$$

where \mathbf{G}_{GCC}^{PLL} depicts the effect of PLL on \mathbf{Z}_{GCC} , shown as

$$\mathbf{G}_{GCC}^{PLL} = \begin{bmatrix} 0 & G_{GCCdq}^{PLL} \\ 0 & G_{GCCqq}^{PLL} \end{bmatrix} = V_{dc} \mathbf{Z}_{gcc}^c \mathbf{Y}_{gcc}^g \mathbf{G}_{del} (-\mathbf{G}_{ci} \mathbf{G}_{PLL}^i + \mathbf{G}_{PLL}^d), \quad (12)$$

where

$$\begin{aligned} G_{GCCdq}^{PLL} &= j(V_{c,q}^s + I_{g,q}^s G_{ci}) G_{PLL} G_{del} V_{dc} \\ G_{GCCqq}^{PLL} &= (V_{c,d}^s + I_{g,d}^s G_{ci}) G_{PLL} G_{del} V_{dc}. \end{aligned} \quad (13)$$

Therefore, dq -domain admittance model \mathbf{Y}_{GCC} can be calculated based on (11), shown as

$$\mathbf{Y}_{GCC} = \begin{bmatrix} Y_{GCCdd} & Y_{GCCdq} \\ Y_{GCCqd} & Y_{GCCqq} \end{bmatrix} = \mathbf{Y}_{GCC}^{\text{simp}} (\mathbf{I} - \mathbf{G}_{GCC}^{\text{PLL}}), \quad (14)$$

where

$$\begin{aligned} Y_{GCCdd} &= Y_{GCCdd}^{\text{simp}} & Y_{GCCqd} &= Y_{GCCqd}^{\text{simp}} \\ Y_{GCCdq} &= Y_{GCCdq}^{\text{simp}} - (Y_{GCCdq}^{\text{simp}} G_{GCCqq}^{\text{PLL}} + Y_{GCCdd}^{\text{simp}} G_{GCCdq}^{\text{PLL}}) \\ Y_{GCCqq} &= Y_{GCCqq}^{\text{simp}} - (Y_{GCCqq}^{\text{simp}} G_{GCCqq}^{\text{PLL}} + Y_{GCCqd}^{\text{simp}} G_{GCCdq}^{\text{PLL}}). \end{aligned} \quad (15)$$

It is worthwhile noting that (15) agrees with the complex transfer function-based dq -domain admittance model in [41]. (15) shows that the d - d and q - d admittance components are not affected by PLL dynamics. The characteristics of the four components of \mathbf{Y}_{GCC} can also be analyzed in a similar way.

B. Principle of the Proposed Parameters Identification Method

The proposed method first identifies the parameters of current controller, LCL filter, and digital sampling time, based on which PLL controller parameters are further identified.

1) *Identification of Parameters of Current Controller, LCL Filter, and Digital Sampling Time:* Assume that $\mathbf{Y}_{GCC}^{\text{mea}}$ is the measured IFRs of \mathbf{Y}_{GCC} . Based on (2) and (15), contribution of current control loop on $\mathbf{Y}_{GCC}^{\text{mea}}$, i.e., $\mathbf{Y}_{GCC}^{\text{simpmea}}$, can be calculated as

$$\mathbf{Y}_{GCC}^{\text{simpmea}} = \begin{bmatrix} Y_{GCCdd}^{\text{simpmea}} & -Y_{GCCqd}^{\text{simpmea}} \\ Y_{GCCqd}^{\text{simpmea}} & Y_{GCCdd}^{\text{simpmea}} \end{bmatrix} = \begin{bmatrix} 0 & -1 \\ 1 & 0 \end{bmatrix} \mathbf{Y}_{GCC}^{\text{mea}} \begin{bmatrix} 1 & 1 \\ 0 & 0 \end{bmatrix} + \mathbf{Y}_{GCC}^{\text{mea}} \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix}. \quad (16)$$

Then, the measured phasor-domain IFRs can be calculated from $\mathbf{Y}_{GCC}^{\text{simpmea}}$ using (3) and (4), which can further be fitted as a polynomial transfer function in the form of (9) using the VF algorithm. It can be seen that the extraction of the one-dimensional phasor-domain IFRs avoids the direct application of the MF algorithm on the two-dimensional $\mathbf{Y}_{GCC}^{\text{simpmea}}$. (5) should also be re-formulated as a polynomial transfer function to identify these parameters from the fitted transfer function. However, this reformulation is hindered by the digital time delay G_{del} which can be approximated as a polynomial function using the Pade approximation, shown as [42]

$$G_{del} = e^{-1.5T_s s} = \frac{q_l (1.5T_s s)^l + \dots q_i (1.5T_s s)^i + \dots q_0}{p_k (1.5T_s s)^k + \dots p_j (1.5T_s s)^j + \dots p_0}, \quad (17)$$

where $p_j = \frac{(l+k-j)!k!}{j!(k-j)!}$ ($j = 0, 1, \dots, k$) and $q_i = (-1)^i \frac{(l+k-i)!l!}{i!(l-i)!}$ ($i = 0, 1, \dots, l$).

As stated in [5], the fitting error of (9) with order $m = 5$ for a VSC is small enough, which will also be shown in Section IV-A. Therefore, appropriate values l and k should be selected to establish an equivalent polynomial transfer function of (5) with the orders of both numerator and denominator equal to 5. One possible Pade approximation of G_{del} is to set $l = 5$ and $k = 3$ in (17), shown as

$$G_{del}^{5-3}(s) \approx \frac{q_5 T_s^5 s^5 + q_4 T_s^4 s^4 + q_3 T_s^3 s^3 + q_2 T_s^2 s^2 + q_1 T_s s + q_0}{p_3 T_s^3 s^3 + p_2 T_s^2 s^2 + p_1 T_s s + p_0}, \quad (18)$$

where $p_3 = 405$, $p_2 = 4860$, $p_1 = 22680$, $p_0 = 40320$, $q_5 = -45.5625$, $q_4 = 607.5$, $q_3 = -4050$, $q_2 = 16200$, $q_1 = -37800$, and $q_0 = 40320$. By substituting (18) into (5), Z_{GCC} can be represented as

$$Z_{GCC} = \frac{e_5 s^5 + e_4 s^4 + e_3 s^3 + e_2 s^2 + e_1 s + e_0}{d_5 s^5 + d_4 s^4 + d_3 s^3 + d_2 s^2 + d_1 s + d_0} + f s, \quad (19)$$

where

$$\begin{aligned} d_5 &= p_3 L_{f1} C_f T_s^3 & d_4 &= p_2 L_{f1} C_f T_s^2 \\ d_3 &= p_3 T_s^3 + p_1 L_{f1} C_f T_s & d_2 &= p_2 T_s^2 + p_0 L_{f1} C_f \\ d_1 &= p_1 T_s & d_0 &= p_0, \end{aligned} \quad (20)$$

and

$$\begin{aligned} e_5 &= k_{pi} V_{dc} q_5 T_s^5 & e_4 &= k_{pi} V_{dc} q_4 T_s^4 + p_3 L_{f1} T_s^3 \\ e_3 &= k_{pi} V_{dc} q_3 T_s^3 + p_2 L_{f1} T_s^2 & e_2 &= k_{pi} V_{dc} q_2 T_s^2 + p_1 L_{f1} T_s \\ e_1 &= k_{pi} V_{dc} q_1 T_s + p_0 L_{f1} & e_0 &= k_{pi} V_{dc} q_0 & f &= L_{f2}. \end{aligned} \quad (21)$$

By equalizing (19) and (9) with $m = 5$, i.e., $f = E$, $d_i = A_i$ ($i \in [0, 5]$), and $e_j = B_j$ ($j \in [0, 5]$), the circuit and controller parameters can be identified as

$$\begin{aligned} L_{f2}^{iden} &= E & k_{pi}^{iden} &= \frac{B_0}{A_0 V_{dc}} & T_s^{iden} &= \frac{16A_1}{9A_0} \\ L_{f1}^{iden} &= \frac{B_1}{A_0} + \frac{15k_{pi}^{iden} V_{dc} T_s^{iden}}{16} \\ C_f^{iden} &= \frac{A_2}{A_0 L_{f1}^{iden}} - \frac{27T_s^{iden^2}}{224L_{f1}^{iden}}. \end{aligned} \quad (22)$$

Different from GCC mode, no matter what values are selected for l and k in (17) for CCC mode, the order of the numerator is always lower than the order of the denominator in the equivalent polynomial transfer function of (8). To establish an equivalent polynomial transfer function of (8) similar with (9) where $m = 5$, besides the Pade approximation in (18), another Pade approximation with $l = k = 4$ can be used, shows as

$$G_{del}^{4-4}(s) \approx \frac{q'_4 T_s^4 s^4 + q'_3 T_s^3 s^3 + q'_2 T_s^2 s^2 + q'_1 T_s s + q'_0}{p'_4 T_s^4 s^4 + p'_3 T_s^3 s^3 + p'_2 T_s^2 s^2 + p'_1 T_s s + p'_0}, \quad (23)$$

where $p'_4 = 121.5$, $p'_3 = 1620$, $p'_2 = 9720$, $p'_1 = 30240$, $p'_0 = 40320$, $q'_4 = 121.5$, $q'_3 = -1620$, $q'_2 = 9720$, $q'_1 = -30240$, and $q'_0 = 40320$.

On one hand, by substituting (18) into (8), Z_{CCC} can be represented as

$$Z_{CCC} = \frac{b_5 s^5 + b_4 s^4 + b_3 s^3 + b_2 s^2 + b_1 s + b_0}{a_6 s^6 + a_5 s^5 + a_4 s^4 + a_3 s^3 + a_2 s^2 + a_1 s + a_0} + c s, \quad (24)$$

where

$$\begin{aligned} a_6 &= k_{pi} V_{dc} q_5 C_f T_s^5 & a_5 &= (k_{pi} V_{dc} q_4 T_s^4 + p_3 L_{f1} T_s^3) C_f \\ a_4 &= (k_{pi} V_{dc} q_3 T_s^3 + p_2 L_{f1} T_s^2) C_f \\ a_3 &= p_3 T_s^3 + k_{pi} V_{dc} q_2 C_f T_s^2 + p_1 L_{f1} C_f T_s \\ a_2 &= p_2 T_s^2 + k_{pi} V_{dc} q_1 C_f T_s + p_0 L_{f1} C_f \\ a_1 &= p_1 T_s + k_{pi} V_{dc} q_0 C_f & a_0 &= p_0, \end{aligned} \quad (25)$$

and

$$\begin{aligned} b_5 &= k_{pi} V_{dc} q_5 T_s^5 & b_4 &= k_{pi} V_{dc} q_4 T_s^4 + p_3 L_{f1} T_s^3 \\ b_3 &= k_{pi} V_{dc} q_3 T_s^3 + p_2 L_{f1} T_s^2 & b_2 &= k_{pi} V_{dc} q_2 T_s^2 + p_1 L_{f1} T_s \\ b_1 &= k_{pi} V_{dc} q_1 T_s + p_0 L_{f1} & b_0 &= k_{pi} V_{dc} q_0 & c &= L_{f2}. \end{aligned} \quad (26)$$

By equalizing (24) and (9) with $m = 5$, i.e., $c = E$, $a_i = A_i$ ($i \in [0, 5]$), and $b_j = B_j$ ($j \in [0, 5]$), the circuit and controller parameters can be identified as

$$\begin{aligned} L_{f2}^{iden} &= E \quad k_{pi}^{iden} = \frac{B_0}{A_0 V_{dc}} \quad C_f^{iden} = \frac{A_5}{B_4} \\ T_s^{iden} &= \frac{16k_{pi}^{iden} V_{dc} (\frac{A_1}{B_0} - C_f^{iden})}{9} \\ L_{f1}^{iden} &= \frac{B_1}{A_0} + \frac{15k_{pi}^{iden} V_{dc} T_s^{iden}}{16}. \end{aligned} \quad (27)$$

On the other hand, by substituting (23) into (8), Z_{CCC} can be represented as

$$Z_{CCC} = \frac{b'_5 s^5 + b'_4 s^4 + b'_3 s^3 + b'_2 s^2 + b'_1 s + b'_0}{a'_6 s^6 + a'_5 s^5 + a'_4 s^4 + a'_3 s^3 + a'_2 s^2 + a'_1 s + a'_0} + c' s, \quad (28)$$

where

$$\begin{aligned} a'_6 &= p'_4 L_{f1} C_f T_s^4 \quad a'_5 = (k_{pi} V_{dc} q'_4 T_s^4 + p'_3 L_{f1} T_s^3) C_f \\ a'_4 &= p'_4 T_s^4 + k_{pi} V_{dc} q'_3 C_f T_s^3 + p'_2 L_{f1} C_f T_s^2 \\ a'_3 &= p'_3 T_s^3 + k_{pi} V_{dc} q'_2 C_f T_s^2 + p'_1 L_{f1} C_f T_s \\ a'_2 &= p'_2 T_s^2 + k_{pi} V_{dc} q'_1 C_f T_s + p'_0 L_{f1} C_f \\ a'_1 &= p'_1 T_s + k_{pi} V_{dc} q'_0 C_f \quad a'_0 = p'_0, \end{aligned} \quad (29)$$

and

$$\begin{aligned} b'_5 &= p'_4 L_{f1} T_s^4 \quad b'_4 = (k_{pi} V_{dc} q'_4 T_s^4 + p'_3 L_{f1} T_s^3) \\ b'_3 &= k_{pi} V_{dc} q'_3 T_s^3 + p'_2 L_{f1} T_s^2 \quad b'_2 = k_{pi} V_{dc} q'_2 T_s^2 + p'_1 L_{f1} T_s \\ b'_1 &= k_{pi} V_{dc} q'_1 T_s + p'_0 L_{f1} \quad b'_0 = k_{pi} V_{dc} q'_0 \quad c' = L_{f2}. \end{aligned} \quad (30)$$

By equalizing (28) and (9) with $m = 5$, i.e., $c' = E$, $a'_i = A_i$ ($i \in [0, 5]$), and $b'_j = B_j$ ($j \in [0, 5]$), the circuit and controller parameters can be identified as

$$\begin{aligned} L_{f2}^{iden} &= E \quad k_{pi}^{iden} = \frac{B_0}{A_0 V_{dc}} \quad C_f^{iden} = \frac{A_5}{B_4} \\ T_s^{iden} &= \frac{4k_{pi}^{iden} V_{dc} (\frac{A_1}{B_0} - C_f^{iden})}{3} \\ L_{f1}^{iden} &= \frac{B_1}{A_0} + \frac{3k_{pi}^{iden} V_{dc} T_s^{iden}}{4}. \end{aligned} \quad (31)$$

It can be seen that T_s^{iden} and L_{f1}^{iden} are different in (27) and (31), even (24) and (28) are in the same polynomial transfer function form. Compared to (23), the Pade approximation (18) is able to obtain more accurate identification results, which will be shown in Section IV-B.

2) Further Identification of PLL Controller Parameters:

Based on (2) and (12), the four components of Z_{GCC} can be calculated as

$$\begin{aligned} Z_{GCCdd} &= Z_{GCCdd}^{simp} + \frac{G_{GCCdq}^{PLL} Z_{GCCqd}^{simp}}{1 - G_{GCCqq}^{PLL}} \\ Z_{GCCdq} &= Z_{GCCdq}^{simp} + \frac{G_{GCCdq}^{PLL} Z_{GCCqq}^{simp}}{1 - G_{GCCqq}^{PLL}} \\ Z_{GCCqd} &= \frac{Z_{GCCqd}^{simp}}{1 - G_{GCCqq}^{PLL}} \quad Z_{GCCqq} = \frac{Z_{GCCqq}^{simp}}{1 - G_{GCCqq}^{PLL}}. \end{aligned} \quad (32)$$

In the frequency range above PLL bandwidth, G_{GCCdq}^{PLL} and G_{GCCqq}^{PLL} can be regarded as zero, since G_{PLL} is close to

zero. Therefore, $Z_{GCC} \approx Z_{GCC}^{simp}$ in the high-frequency range. On the other hand, since Z_{GCC}^{simp} is diagonally dominant (i.e., $Z_{GCCdd}^{simp} \gg Z_{GCCqd}^{simp}$ and $Z_{GCCqq}^{simp} \gg Z_{GCCdq}^{simp}$), $Z_{GCCdd} \approx Z_{GCCdd}^{simp}$, $Z_{GCCdq} \neq Z_{GCCdq}^{simp}$, $Z_{GCCqd} \neq Z_{GCCqd}^{simp}$, and $Z_{GCCqq} \neq Z_{GCCqq}^{simp}$ in low-frequency range.

Z_{GCC} can be calculated based on (5) using the identified parameters of current controller, LCL filter, and digital sampling time, which is denoted as Z_{GCC}^{iden} . According to (13) and (32), G_{PLL} can be identified as

$$G_{PLL}^{iden} = \frac{Z_{GCCqq}^{mea} - Z_{GCC}^{iden}}{Z_{GCCqq}^{mea} (V_{c,d}^{smea} + I_{g,d}^{smea} G_{ci}^{iden}) G_{del}^{iden} V_{dc}}. \quad (33)$$

On its basis, PLL controller parameters can further be identified. PLL controller parameters of the VSC under CCC mode can be identified in a similar way.

In addition to the VSCs under GCC and CCC modes studied in this article, the VSCs can be controlled by various feedback strategies (e.g., capacitor-current proportional feedback, capacitor-voltage derivative feedback, and PCC-voltage feedforward) and outer loop control strategies (e.g., dc-link voltage control and power control) in practice. In [22], the identification algorithm in this article is modified to cope with GCC mode with capacitor-current proportional feedback. In [43], the output impedance model of the VSC under capacitor-voltage derivative feedback is proved to be the same as that under capacitor-current proportional feedback, which results in the same parameters identification algorithm. Modification of the identification algorithm to cope with PCC-voltage feedforward strategy should be further investigated. In this article, contributions of current control loop and PLL on the IFRs are identified by (16) to facilitate the aforementioned step-by-step parameters identification. This impedance division concept is further developed in [44] to identify the impedance contributions of PLL and outer power control loop, and in [45] to identify the impedance contributions of current control loop, PLL, dc-link voltage control loop, and PCC-voltage control loop. Based on the impedance division results in [44], [45], the parameters of various control loops can further be identified in a similar way as explained in this article.

In addition to the aforementioned various proportional-integral-based feedback/outer control loops, the VSCs can also be under other advanced control strategies, e.g., the model predictive control which can include nonlinearities and constraints easily [46], [47]. An optimization algorithm is defined to minimize the cost function whose input is the output of the predictive model. Extension of the parameters identification concept in this article for the model predictive control can be a future work.

In some worse cases, not only the circuit and controller parameters but also the internal control structure is not provided by the vendors due to the industrial secrecy and intellectual property. Therefore, the VSC becomes a black box, and identification of internal parameters becomes impossible. However, the IFRs of the VSC can be fitted as a state-space model using the VF algorithm, whose state variables are virtual and do not have any physical meanings. The relevant work has been reported in [5], [6], [34], [35]. Although the virtual state-

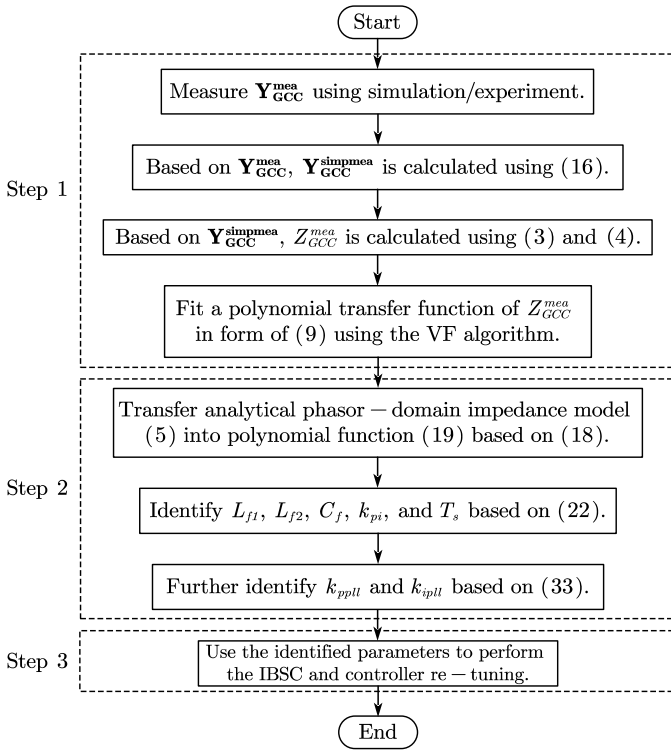


Fig. 2. Flowchart of the proposed parameters identification method of the VSCs under GCC mode.

space model can still be fitted from the terminal IFRs when the internal control structure is known, this article aims to explore the possibility to further identify the internal parameters based on both the IFRs and the VF algorithm.

C. Implementation Procedure of the Proposed Parameters Identification Method

Fig. 2 shows the implementation procedure of the proposed parameters identification method of the grid-connected VSCs, which consists of three main steps. In step 1, \mathbf{Y}_{GCC}^{mea} is obtained using the frequency scanning method, from which \mathbf{Y}_{GCC}^{simp} is extracted using (16). Then, the corresponding phasor-domain IFRs \mathbf{Z}_{GCC}^{mea} can be obtained using (3) and (4), which can be fitted as a polynomial function in form of (9) using the VF algorithm.

In step 2, the circuit and controller parameters of the VSC are identified from the fitted polynomial function. Taking GCC mode as an example, phasor-domain impedance model (5) is first reformulated as a polynomial function (19) based on the Pade approximation (18). Then, LCL filter parameters (i.e., L_{f1} , L_{f2} , and C_f), proportional coefficient of the current controller (i.e., k_{pi}), and digital sampling time (i.e., T_s), are identified based on (22). On its basis, PLL controller parameters are further identified using (33). The circuit and controller parameters of the VSC under CCC mode can be identified in a similar way.

In step 3, the identified circuit and controller parameters are used in the IBSC and further controller re-tuning. For example, if instability phenomena occur when the VSC is connected to the weak grid, as shown in Fig. 1(a), the magnitude interaction

TABLE I
PARAMETERS OF THE TWO VSCs UNDER STUDY

Symbol	Description	GCC Mode	CCC Mode
V_{dc}	DC-link voltage	400 V	400 V
ω_1	Fundamental angular frequency	314 rad/s	314 rad/s
L_{f1}	Inverter-side filter inductance	4 mH	3 mH
L_{f2}	Grid-side filter inductance	1.6 mH	2 mH
C_f	Filter capacitance	5 μ F	10 μ F
f_{sw}	Switching frequency	10 kHz	10 kHz
f_s	Sampling frequency	10 kHz	10 kHz
V_g	Grid voltage (phase-to-phase magnitude)	380 V	380 V
k_{pi}	Proportional gain of current controller	0.0375 Ω	0.0325 Ω
k_{ii}	Integrator gain of current controller	3.1212 Ω/s	4.8750 Ω/s
k_{pll}	Proportional gain of PLL	5 rad/(Vs)	10 rad/(Vs)
k_{ipll}	Integrator gain of PLL	6000 rad/(Vs ²)	4000 rad/(Vs ²)
i_{gd}^{ref}	d -axis current reference	20 A	20 A
i_{gq}^{ref}	q -axis current reference	0 A	0 A

points of the measured dq -domain IFRs of both VSC and weak grid, i.e., \mathbf{Z}_{GCC}^{mea} and \mathbf{Z}_g^{mea} , are identified. The parameters of current controller and PLL controller can then be re-tuned to mitigate the instability phenomena, which is able to help the vendors to re-design the control system to improve the stability of the VSC-grid system.

IV. THEORETICAL VERIFICATION

In this section, the effectiveness of the proposed parameters identification method is theoretically validated. On its basis, influence of measurement noise on parameters identification accuracy and corresponding countermeasure to mitigate the adverse influence are discussed.

A. Theoretical Verification of the Proposed Parameters Identification Method

The circuit and controller parameters of the VSCs under GCC and CCC modes are shown in Table I. \mathbf{Z}_{GCC}^{mea} and \mathbf{Z}_{GCC}^{simp} are calculated based on (1) and (2), respectively, of which the Bode diagrams are plotted in Fig. 3(a). It can be seen that $\mathbf{Z}_{GCC}^{mea} \approx \mathbf{Z}_{GCC}^{simp}$ in high-frequency range. In addition, \mathbf{Z}_{GCCdq} , \mathbf{Z}_{GCCqd} , and \mathbf{Z}_{GCCqq} are significantly affected by PLL dynamics in low-frequency range, whereas \mathbf{Z}_{GCCdd} is slightly influenced. The correctness of the theoretical analysis results in Section III-B2 is thus validated.

82 frequency points of \mathbf{Z}_{GCC} logarithmically distributed between 1 Hz and 5 kHz, i.e., \mathbf{Z}_{GCC}^{theo} in Fig. 3(a), are sampled to theoretically validate the effectiveness of the proposed parameters identification method. According to step 1 in Fig. 2, the corresponding theoretical phasor-domain IFRs \mathbf{Z}_{GCC}^{theo} can be calculated from \mathbf{Z}_{GCC}^{simp} , of which the Bode diagram is plotted in Fig. 3(b). Polynomial transfer functions in the form of (9) with orders 1st, 2nd, 3rd, and 5th are generated by applying the VF algorithm on the 82 frequency points, of which the Bode diagrams are plotted in Fig. 3(b). The fitting errors using different orders are calculated by (10) and shown in the second row of Table II, which indicates that fitting accuracy is generally increased as the fitting order increases. The 5th order is selected, since it achieves the trade-off between accuracy and complexity. The coefficients of the fitted 5th-order polynomial function are listed in the second column of Table III, based on which L_{f2} , k_{pi} , C_f ,

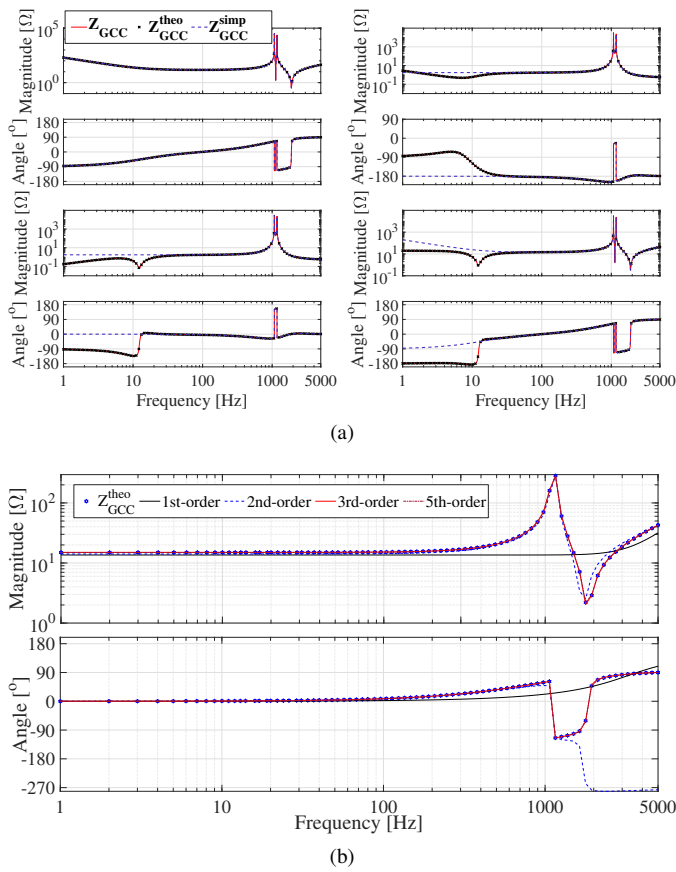


Fig. 3. Theoretical verification of the proposed parameters identification method for GCC mode. (a) Bode diagrams of the dq -domain IFRs. (b) Fitting results of Z_{GCC}^{theo} with different orders.

TABLE II
FITTING ERRORS OF THE TWO CONVERTERS IN TABLE I WITH DIFFERENT ORDERS

Mode	1st order	2nd order	3rd order	5th order	7th order	9th order
GCC	39.0509	6.3220	0.1405	5.3127×10^{-5}	0.00289	3.7863×10^{-5}
CCC	6.1678	3.7238	2.8129×10^{-4}	2.9681×10^{-8}	1.1460×10^{-7}	1.1870×10^{-11}

T_s , and L_{f1} are calculated from (22). PLL parameters can further be identified based on (33). The identification errors of these parameters are also calculated in Table III. It can be seen that the identified circuit and controller parameters highly agree with the actual values in Table I. In addition, identification accuracy of LCL filter parameters based on the proposed method is higher than that proposed in [14], where the identification errors of L_{f1} , L_{f2} , and C_f are 8%, 2%, and 12%, respectively. It is worthwhile noting that different from experimental verification, the actual parameter values are exactly known in the theoretical verification, which enables better analysis of parameters identification accuracy.

Similarly, the dq -domain impedance models of the VSC under CCC mode with considering PLL Z_{CCC} and without considering PLL Z_{CCC}^{simp} are calculated based on (6) and (7), respectively, of which the Bode diagrams are plotted in Fig. 4(a). Similar with Fig. 3(a), it can be seen that, in low-frequency range, Z_{CCCdq} , Z_{CCCqd} , and Z_{CCCqq} are significantly affected by PLL dynamics, whereas Z_{CCCdd} is slightly influenced. The correctness of the theoretical analysis results

TABLE III
COEFFICIENTS OF THE FITTED 5TH-ORDER POLYNOMIAL TRANSFER FUNCTIONS AND CORRESPONDING IDENTIFIED PARAMETERS

	Theo. GCC	Theo. CCC	Mea. GCC
A_5	1	1	1
A_4	1.2908e+05	4.2246e+04	1.3233e+05
A_3	6.6447e+09	3.1850e+09	6.3287e+09
A_2	1.3508e+14	2.7315e+13	1.4522e+14
A_1	3.2974e+17	2.6342e+17	4.2349e+17
A_0	6.4315e+21	1.3946e+21	6.7525e+21
B_5	-5.1425	3.7252e-04	-0.0373
B_4	3.0641e+05	1.0006e+05	5.2919e+05
B_3	1.0916e+10	4.2147e+09	3.0293e+10
B_2	1.7531e+15	3.1614e+14	3.8291e+15
B_1	1.6082e+19	2.5319e+18	1.4834e+19
B_0	9.6472e+22	1.8130e+22	1.1123e+23
E	0.0016	0.0020	0.0016
L_{f2}^{iden} [mH]	1.6 (0%)	2 (0%)	1.6 (0%)
k_{pi}^{iden} [Ω]	0.0375 (0%)	0.0325 (0%)	0.0412 (9.87%)
C_f^{iden} [μF]	5.29 (5.80%)	9.99 (0.10%)	5.13 (2.60%)
T_s^{iden} [μs]	91.15 (8.85%)	105 (5.00%)	111.50 (11.50%)
L_{f1}^{iden} [mH]	3.8 (5.00%)	3.1 (3.33%)	3.9 (2.50%)
k_{ppl}^{iden} [rad/(Vs)]	4.923 (1.54%)	9.738 (2.62%)	4.839 (3.22%)
k_{ipl}^{iden} [rad/(Vs ²)]	6125 (2.08%)	4235 (5.88%)	5706 (4.90%)

in Section III-B2 is thus again validated. Fitting results of the 82-frequency-point-based phasor-domain IFRs using different orders are plotted in Fig. 4(b). The fitting errors using different orders are calculated by (10) and shown in the third row of Table II, which indicates that the 5th-order transfer function achieves the trade-off between accuracy and complexity. The identified circuit and controller parameters are listed in the third column of Table III, which highly agree with the actual values in Table I. On the other hand, the internal parameters identified by (31) are $L_{f2}^{iden} = 2$ mH (0%), $k_{pi}^{iden} = 0.0325$ Ω (0%), $C_f^{iden} = 9.99$ μF (0.10%), $T_s^{iden} = 78.75$ μs (21.25%), and $L_{f1}^{iden} = 2.58$ mH (14.00%). It can be seen that the identification accuracy using (31) is poorer than that using (27).

The dq -domain impedance characteristics in the high-frequency range are dominated by the current control loop, as explained in (32). Therefore, parameters of the current control loop can theoretically be identified from only the high-frequency dq -domain impedance data. However, PLL bandwidth calculation is impossible for the gray-box VSCs, which indicates that identification of the aforementioned high-frequency range is also impossible. To solve this gray-box issue, the current control loop-related dq -domain impedance data is extracted from terminal impedance data using (16), which is one main contribution of this article. On its basis, phasor-domain impedance data is calculated using (3) and (4). The aforementioned impedance transformation and application of the VF algorithm on the phasor-domain impedance data are illustrated in Figs. 3 and 4, which show that instead of using only the high-frequency dq -domain impedance data, the whole frequency range of the phasor-domain impedance data can be used, which thus does not require the identification of aforementioned high-frequency range and could provide more frequency points for parameters identification.

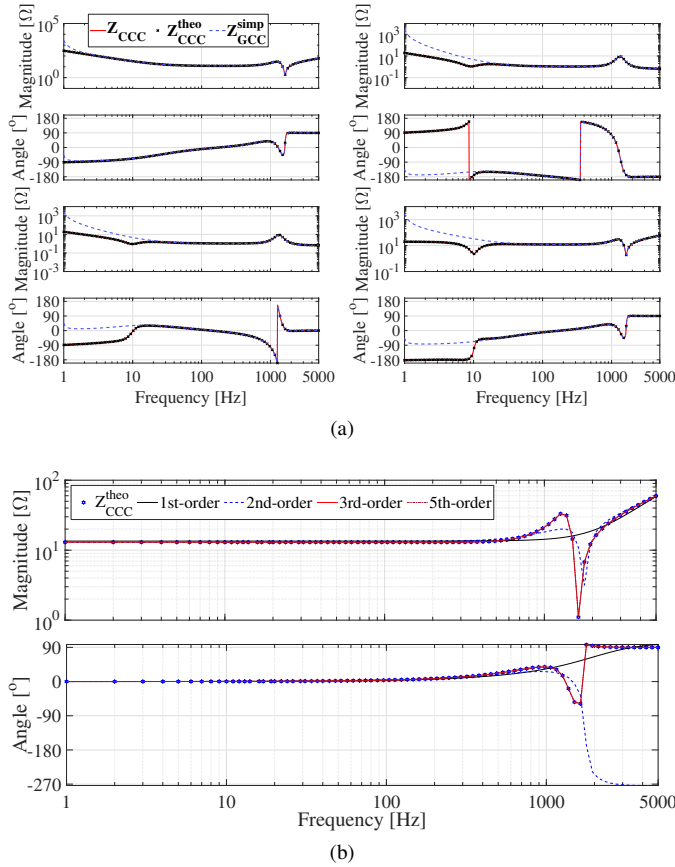


Fig. 4. Theoretical verification of the proposed parameters identification method for CCC mode. (a) Bode diagrams of the dq -domain IFRs. (b) Fitting results of Z_{CCC}^{theo} with different orders.

B. Influence of Measurement Noise on Parameters Identification Accuracy

To quantitatively analyze the influence of measurement noise on parameters identification accuracy, 82 two-dimensional random variables under normal distribution with mean value 0 and different standard deviations σ from 0 to 1.6 are designed and collected in $\mathbf{X}_{82} = \{\mathbf{x}_1, \mathbf{x}_2, \dots, \mathbf{x}_{82}\}$. The 82 random variables are then added to the aforementioned 82 analytical dq -domain IFRs of \mathbf{Z}_{CCC} , which is denoted as \mathbf{Z}_{CCC}^{82} . The perturbed 82 dq -domain IFRs \mathbf{Z}_{CCCp}^{82} can be expressed as

$$\mathbf{Z}_{CCCp}^{82} = \mathbf{Z}_{CCC}^{82} \times (1 + \mathbf{X}_{82}/100), \quad (34)$$

where $\mathbf{x}_i \sim N(0, \sigma^2)$ ($i = 1, 2, \dots, 82$). Similar to Fig. 4(b), parameters are identified from the fitted 5th-order polynomial transfer functions of the perturbed phasor-domain IFRs, as shown in Table IV. It can be seen that the identification accuracy becomes poor when the measurement noise becomes large, which indicates that the fitted 5th-order transfer function cannot incorporate the measurement noise well.

A higher order (e.g., 10th order) can then be selected. To extract the circuit and controller parameters from the fitted 10th-order transfer function, the parameters extraction scheme in (27) should be modified. Similar with the parameters identification process for the fitted 5th-order transfer function in Section III-B, l and k in the Pade approximation (17) can

TABLE IV
IDENTIFIED PARAMETERS FROM THE FITTED 5TH-ORDER POLYNOMIAL TRANSFER FUNCTIONS WITH DIFFERENT MEASUREMENT NOISES

	CCC mode (5th-order)				
	$\sigma = 0$	$\sigma = 0.4$	$\sigma = 0.8$	$\sigma = 1.2$	$\sigma = 1.6$
A_5	1	1	1	1	1
A_4	4.1853e+04	-2.9443e+04	-3.0751e+03	-2.4859e+04	-1.3226e+04
A_3	3.1373e+09	1.7661e+09	2.3681e+09	1.6249e+09	2.0446e+09
A_2	2.6904e+13	1.3883e+13	2.0304e+13	1.3817e+13	1.8096e+13
A_1	2.5933e+17	1.5631e+17	2.0386e+17	1.4456e+17	1.7909e+17
A_0	1.3726e+21	9.6834e+20	1.2310e+21	9.6726e+20	1.1833e+21
B_5	3.4681e-04	0.0147	-0.1583	-0.1117	0.2965
B_4	1.0007e+05	9.9386e+04	9.9982e+04	1.0330e+05	1.0997e+05
B_3	4.1753e+09	-3.0019e+09	-8.0411e+08	-2.7571e+09	-1.0421e+09
B_2	3.1137e+14	1.7460e+14	2.4070e+14	1.6642e+14	2.0941e+14
B_1	2.4928e+18	1.3993e+18	1.8709e+18	1.2931e+18	1.6518e+18
B_0	1.7844e+22	1.2601e+22	1.6023e+22	1.2554e+22	1.5217e+22
E	0.0020	0.0020	0.0020	0.0020	0.0020
L_{f2}^{iden} [mH]	2 (0%)	2 (0%)	2 (0%)	2 (0%)	2 (0%)
k_{pi}^{iden} [Ω]	0.0325 (0%)	0.0325 (0%)	0.0326 (0.31%)	0.0325 (0%)	0.0322 (0.92%)
C_f^{iden} [μF]	9.99 (0.10%)	10.06 (0.60%)	10.00 (0%)	9.68 (3.20%)	9.09 (9.10%)
T_s^{iden} [μs]	104.92 (4.92%)	54.23 (45.77%)	63.03 (36.97%)	42.34 (57.66%)	61.25 (38.75%)
L_{f1}^{iden} [mH]	3.08 (2.67%)	2.11 (29.67%)	2.29 (23.67%)	1.85 (38.33%)	2.13 (29.00%)

TABLE V
IDENTIFIED PARAMETERS FROM THE FITTED 10TH-ORDER POLYNOMIAL TRANSFER FUNCTIONS WITH DIFFERENT MEASUREMENT NOISES

	CCC mode (10th-order)				
	$\sigma = 0$	$\sigma = 0.4$	$\sigma = 0.8$	$\sigma = 1.2$	$\sigma = 1.6$
A_{10}	1	1	1	1	1
A_9	2.7278e+05	4.0656e+04	4.9734e+04	5.9781e+04	3.8977e+04
A_8	5.7949e+10	7.6188e+09	7.8443e+09	7.9267e+09	7.6942e+09
A_7	8.2491e+15	2.8397e+14	3.3077e+14	3.7832e+14	2.6013e+14
A_6	7.5109e+20	1.8244e+19	1.9052e+19	1.9376e+19	1.8013e+19
A_5	5.9001e+25	5.9944e+23	6.8233e+23	7.4068e+23	5.2649e+23
A_4	2.3553e+30	1.5304e+28	1.6107e+28	1.6426e+28	1.3952e+28
A_3	8.9435e+34	3.9382e+32	4.4102e+32	4.5547e+32	3.2603e+32
A_2	7.7218e+38	3.3804e+36	3.7412e+36	3.9325e+36	2.5030e+36
A_1	6.9555e+42	2.8087e+40	3.1684e+40	3.2995e+40	2.3560e+40
A_0	3.4147e+46	1.3439e+44	1.5167e+44	1.6172e+44	7.6583e+43
B_{10}	2.5763e-05	-0.0115	-0.4085	0.4974	-0.4638
B_9	1.0001e+05	9.9393e+04	9.7115e+04	1.0474e+05	1.2420e+05
B_8	2.7276e+10	4.0083e+09	2.3596e+09	8.8876e+09	2.3176e+08
B_7	5.7921e+15	7.5466e+14	7.7857e+14	8.1010e+14	9.3691e+14
B_6	8.2396e+20	2.8207e+19	2.7615e+19	4.2752e+19	1.3652e+19
B_5	7.4916e+25	1.7904e+24	1.8984e+24	1.9427e+24	2.1624e+24
B_4	5.8734e+30	5.9099e+28	6.3555e+28	7.5181e+28	3.7243e+28
B_3	2.3291e+35	1.4613e+33	1.5697e+33	1.6012e+33	1.6438e+33
B_2	8.7607e+39	3.7602e+37	4.1489e+37	4.2911e+37	2.4517e+37
B_1	6.8568e+43	2.8229e+41	3.2095e+41	3.4071e+41	2.7167e+41
B_0	4.4391e+47	1.7374e+45	1.9532e+45	2.0473e+45	9.0312e+44
E	0.0020	0.0020	0.0020	0.0020	0.0020
L_{f2}^{iden} [mH]	2 (0%)	2 (0%)	2 (0%)	2 (0%)	2 (0%)
k_{pi}^{iden} [Ω]	0.0325 (0%)	0.0323 (0.62%)	0.0322 (0.92%)	0.0317 (2.46%)	0.0295 (9.23%)
C_f^{iden} [μF]	10.00 (0%)	10.07 (0.70%)	10.30 (3.00%)	9.55 (4.50%)	8.05 (19.50%)
T_s^{iden} [μs]	98.26 (1.74%)	105.10 (5.10%)	101.69 (1.69%)	110.84 (10.84%)	283.55 (183.55%)
L_{f1}^{iden} [mH]	2.97 (1.00%)	3.12 (4.00%)	3.10 (3.33%)	3.16 (5.33%)	6.05 (101.67%)

be chosen as either $l = m, k = m - 2$ or $l = k = m - 1$ for the m th-order transfer function. It can be found that, in the case $m > 10$, the identification accuracy is higher if $l = k = m - 1$. p_0, p_1, q_0 , and q_1 in (17) can then be calculated as

$$\begin{aligned} p_0 &= q_0 = (2m - 2)! \\ p_1 &= (m - 1)(2m - 3)! \\ q_1 &= -(m - 1)(2m - 3)!. \end{aligned} \quad (35)$$

Then, the circuit and controller parameters can be identified as (The detailed derivation process is omitted here.)

$$\begin{aligned} L_{f2}^{iden} &= E \quad k_{pi}^{iden} = \frac{B_0}{A_0} \quad C_f^{iden} = \frac{A_m}{B_{m-1}} \\ T_s^{iden} &= \frac{2q_0 k_{pi}^{iden} (\frac{A_1}{B_0} - C_f^{iden})}{3p_1} = \frac{4k_{pi}^{iden} (\frac{A_1}{B_0} - C_f^{iden})}{3} \\ L_{f1}^{iden} &= \frac{B_1}{A_0} + \frac{3k_{pi}^{iden} T_s^{iden}}{4}. \end{aligned} \quad (36)$$

The identified circuit and current controller parameters using (36) are listed in Table V. It can be seen from Tables IV and V that the fitted 10th-order transfer functions can identify the

TABLE VI
IDENTIFIED PARAMETERS WITH MEASUREMENT NOISE $\sigma = 1.6$ USING
DIFFERENT ORDERS

	CCC mode				
	$m = 12$	$m = 14$	$m = 16$	$m = 18$	$m = 20$
A_m	1	1	1	1	1
A_1	4.4303e+49	5.4718e+58	4.3222e+67	3.5690e+76	2.5947e+85
A_0	2.1560e+53	2.5508e+62	1.9866e+71	1.6053e+80	1.1162e+89
B_{m-1}	1.0011e+05	9.0716e+04	9.1227e+04	8.9605e+04	8.6642e+04
B_1	4.4514e+50	5.3701e+59	4.2676e+68	3.5466e+77	2.6042e+86
B_0	2.7547e+54	3.3034e+63	2.5656e+72	2.0736e+81	1.4563e+90
E	0.0020	0.0020	0.0020	0.0020	0.0020
L_{f2}^{iden} [mH]	2 (0%)	2 (0%)	2 (0%)	2 (0%)	2 (0%)
k_{pi}^{iden} [Ω]	0.0320 (1.54%)	0.0324 (0.31%)	0.0323 (0.62%)	0.0323 (0.62%)	0.0326 (0.31%)
C_g^{pi} [μF]	9.99 (0.10%)	11.02 (10.20%)	10.96 (9.60%)	11.16 (11.60%)	11.54 (15.40%)
T_g^{iden} [μs]	103.82 (3.82%)	95.73 (4.27%)	101.33 (1.33%)	104.25 (4.25%)	109.22 (9.22%)
L_{f1}^{iden} [mH]	3.06 (2.00%)	3.04 (1.33%)	3.13 (4.33%)	3.23 (7.67%)	3.40 (13.33%)

parameters more accurately than the fitted 5th-order transfer functions when $\sigma = 0.4, 0.8$, and 1.2 . To obtain a more accurate identification result for $\sigma = 1.6$, higher orders can be used. The identified parameters when $m = 12, 14, 16, 18$, and 20 are listed in Table VI. It can be seen that these fitted higher-order transfer functions are able to identify parameters more accurately than the 10th-order transfer function.

Similarly, (22) can also be modified to identify the circuit and controller parameters from the fitted higher-order transfer functions for the VSCs under GCC mode, which is omitted in this article for simplicity.

V. SIMULATION AND EXPERIMENTAL VERIFICATION

In this section, the effectiveness of the proposed parameters identification method is verified by the OPAL-RT-based real-time simulation results and PhIL-based experimental results.

A. Real-Time Simulation Verification Based on OPAL-RT Platform

Real-time simulation verification based on OP5600 OPAL-RT digital simulator is performed to validate the effectiveness of the presented parameters identification method for controller parameters re-tuning to improve system stability. The OP5600 combines the power and reliability of Intel Xeon E5 processing cores with the high-performance latest generation Xilinx Virtex-6 FPGA (ML605) to address a wide range of rapid control prototyping applications with OPAL-RT's RT-LAB or HYPERSIM software platforms. If the real-time simulator is used, the practical unpredictable influence factors (e.g., variation of LCL parameters, parasitic resistance, effect of isolation transformer, and accuracy of sensors) in the real VSC can be avoided, and the capability of the VF algorithm itself can be the point of focus. Therefore, first both the circuit and control sub-systems are modeled in the OPAL-RT simulator. The code is then generated from the Matlab/Simulink-based model and downloaded into the OP5600 hardware. The simulation model implemented in OPAL-RT software is run in the CPU, while the D/A and A/D conversions are implemented in the FPGA. Both software and hardware platforms allow high-speed and real-time simulation. The real-time simulation results obtained by OPAL-RT platform are then post-processed in Matlab.

The real-time simulation verification may use the multi-rate simulation solver, which aims to reduce computational burden

by adopting the most appropriate time steps, i.e., a small time step is selected for the sub-system with fast dynamics, whereas a large time step is selected for the sub-system with slow dynamics. For example, the circuit sub-system that requires high fidelity is implemented in a single CPU core, while the control sub-system that should use a larger time step is run in another CPU core. In this article, only one CPU core is used for simplicity. Specifically, the simulation time steps of both circuit and control sub-systems are $20 \mu\text{s}$, and the average execution time of the computation in each step is $2.75 \mu\text{s}$ which gives 13.76% of CPU usage.

1) *Case 1:* Fig. 1(a) shows the configuration of the studied system, where the gray-box VSC under GCC mode is connected to the utility grid. Fig. 5(a) shows the real-time simulation verification results of three-phase PCC voltages and grid currents when R_g changes from 0.2Ω to 0.5Ω , and L_g changes from 0.2 mH to 1 mH at 1 s . In addition, $R_{C_g} = 0.1 \Omega$ and $C_g = 10 \mu\text{F}$. It can be seen that the system becomes unstable at 1 s . The frequency spectrum of the grid current between 1 s and 2 s is shown in Fig. 5(b), which indicates that the system oscillates at 1364 Hz . In order to identify the origins of the high-frequency oscillation phenomena, the Bode diagrams of the dq -domain IFRs of the VSC, the stable grid, and the unstable grid measured by the frequency scanning method are plotted as $\mathbf{Z}_{GCC}^{\text{mea}}$, $\mathbf{Z}_{g1}^{\text{mea}}$, and $\mathbf{Z}_{g2}^{\text{mea}}$ in Fig. 6(a), respectively. Fig. 6(b) shows that magnitude interaction point of the VSC and the grid moves from point A (1781 Hz) to point B (1376 Hz). The phase angle difference at point B is $74^\circ - (-107^\circ) = 181^\circ > 180^\circ$, which indicates that the system is unstable at 1376 Hz . In addition, the Bode diagrams of the theoretical dq -domain IFRs of the VSC calculated by (1), the stable grid, and the unstable grid are plotted as \mathbf{Z}_{GCC} , \mathbf{Z}_{g1} , and \mathbf{Z}_{g2} in Fig. 6(a), respectively. It can be seen that the frequency scanning-obtained and the theoretically-derived dq -domain IFRs highly agree with each other, which shows the effectiveness of the measured dq -domain IFRs for IBSC. The minor difference between the identified instability frequency 1376 Hz in Fig. 6(b) and the real-time simulation-obtained instability frequency 1364 Hz in Fig. 5(b) results from the limited resolution of the step size of frequency scanning, which can be reduced by increasing the measurement frequency points. The current controller parameters should be re-tuned to mitigate the high-frequency instability phenomena.

According to step 1 and step 2 of the flowchart of the proposed parameters identification method in Fig. 2, circuit and controller parameters can be identified from $\mathbf{Z}_{GCC}^{\text{mea}}$, as shown in Table III. The Bode diagram of the measured dq -domain IFRs the VSC by decreasing k_{pi} from identified value 0.0412Ω to re-tuned value 0.030Ω is shown as $\mathbf{Z}_{GCC}^{\text{meare1}}$ in Fig. 7(a). Fig. 7(b) shows that the phase angle difference of $\mathbf{Z}_{GCC}^{\text{meare1}}$ and $\mathbf{Z}_{g2}^{\text{mea}}$ at magnitude interaction point D (1376 Hz) is $74^\circ - (-99^\circ) = 173^\circ < 180^\circ$, which indicates that the system is stabilized.

The real-time simulation verification results of the three-phase grid currents after current controller parameters re-tuning are shown after 2 s in Fig. 5(a). The frequency spectrum in Fig. 5(c) shows that the grid current has a decaying frequency component at 1821 Hz between 2 s and 3 s .

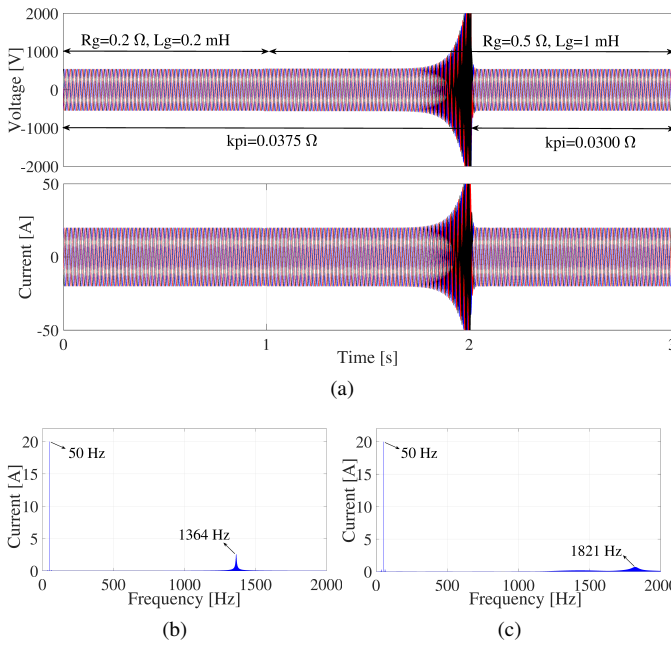


Fig. 5. Real-time simulation verification results for case 1 based on OPAL-RT platform. (a) Time-domain waveforms of three-phase PCC voltages and grid currents. (b) DFT of grid current between 1 s and 2 s. (c) DFT of grid current between 2 s and 3 s.

The real-time simulation verification results agree with the impedance-based stability analysis results in Fig. 6(a). The re-tuned current controller parameters can be delivered to the vendor for system parameters modification.

2) *Case 2*: Fig. 8(a) shows the real-time simulation verification results of three-phase PCC voltages and grid currents when R_g changes from 0.2 Ω to 0.7 Ω , and L_g changes from 0.2 mH to 6 mH at 1.9 s. In addition, $R_{C_g} = 0.1 \Omega$ and $C_g = 10 \mu\text{F}$. It can be seen that the system becomes unstable at 1.9 s. The frequency spectrum of the grid current between 1.9 s and 3.3 s is shown in Fig. 8(b), which indicate that the system oscillates at 38 Hz and 62 Hz. To identify the origins of the low-frequency oscillation phenomena, the Bode diagrams of the dq -domain IFRs of the VSC, the stable grid condition, and the unstable grid condition measured by the frequency scanning method are plotted as $\mathbf{Z}_{GCC}^{\text{mea}}$, $\mathbf{Z}_{g1}^{\text{mea}}$, and $\mathbf{Z}_{g3}^{\text{mea}}$ in Fig. 6(a), respectively. Fig. 6(c) shows that magnitude intersection point of the VSC and the grid moves from point A (1781 Hz) to point C (12 Hz). The phase angle difference at point C is $33^\circ - (-150^\circ) = 183^\circ > 180^\circ$, which indicates that the system will be unstable at 62 Hz and 38 Hz. PLL controller parameters should be re-tuned to mitigate the low-frequency instability phenomena.

Based on the identified parameters in Table III, the low-frequency instability phenomena can be mitigated by decreasing PLL controller parameters. For example, the Bode diagram of the measured dq -domain IFRs of the VSC by decreasing k_{ppl} and k_{ipl} from identified values 4.839 rad/(Vs) and 5706 rad/(Vs²) to re-tuned values 3 rad/(Vs) and 2000 rad/(Vs²) is shown as $\mathbf{Z}_{GCC}^{\text{meare2}}$ in Fig. 7(a). Fig. 7(c) shows that the phase angle difference of $\mathbf{Z}_{GCC}^{\text{meare2}}$ and $\mathbf{Z}_{g3}^{\text{mea}}$ at magnitude intersection point E (7 Hz) is $21^\circ - (-138^\circ) = 159^\circ < 180^\circ$,

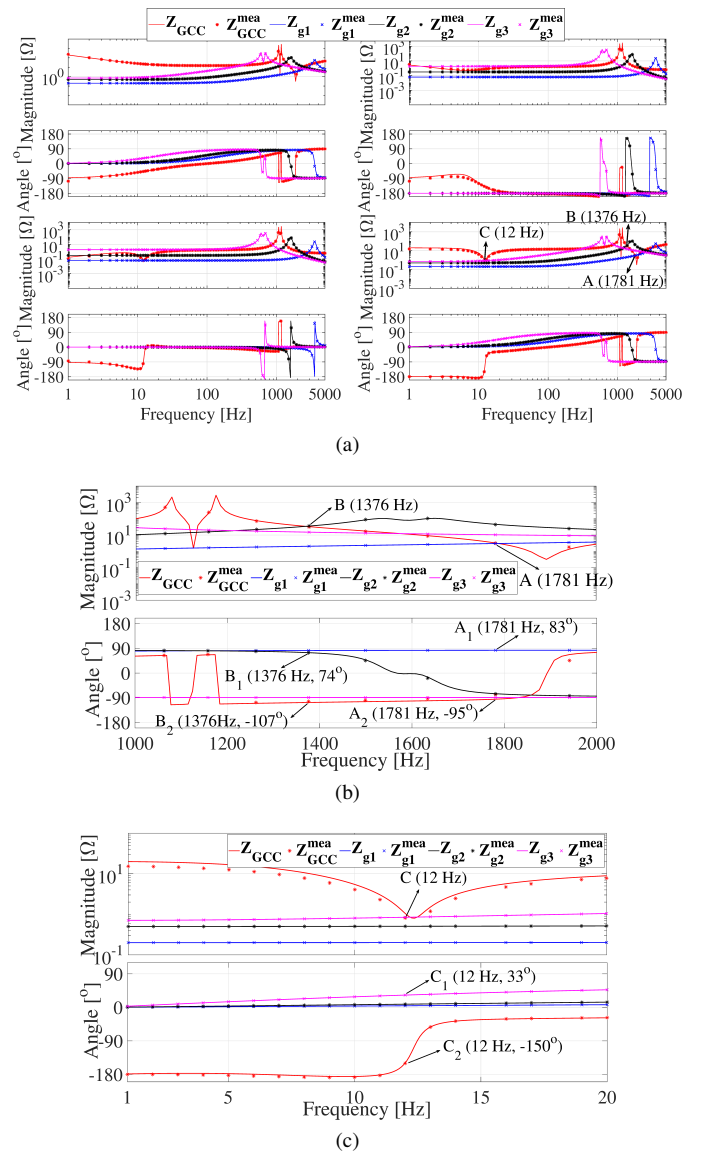


Fig. 6. Bode diagrams of the dq -domain IFRs of the VSC and grid. (a) Full view of the dq -domain IFRs. (b) Zoomed view of q -axis IFRs in high-frequency range. (c) Zoomed view of q -axis IFRs in low-frequency range.

which indicates that the system is stabilized.

The real-time simulation verification results of the three-phase grid currents after current controller parameters re-tuning are shown after 3.3 s in Fig. 8(a). The frequency spectrum in Fig. 8(c) shows that the grid current has two decaying frequency components at 43 Hz and 57 Hz between 3.3 s and 10 s. The real-time verification results agree with the impedance-based stability analysis results in Fig. 7(c).

In addition to the theoretical verification of the adaptive order-selection scheme for measurement noise mitigation in Tables IV, V, and VI, the similar verification can also be performed in simulation or experimental environment. For example, based on the frequency scanning strategy in [36], three-phase grid current responses when PCC voltage is perturbed can further be manually perturbed by a random white noise using (34). This artificially-produced measurement noise of three-phase grid current responses can then bring in inaccuracy

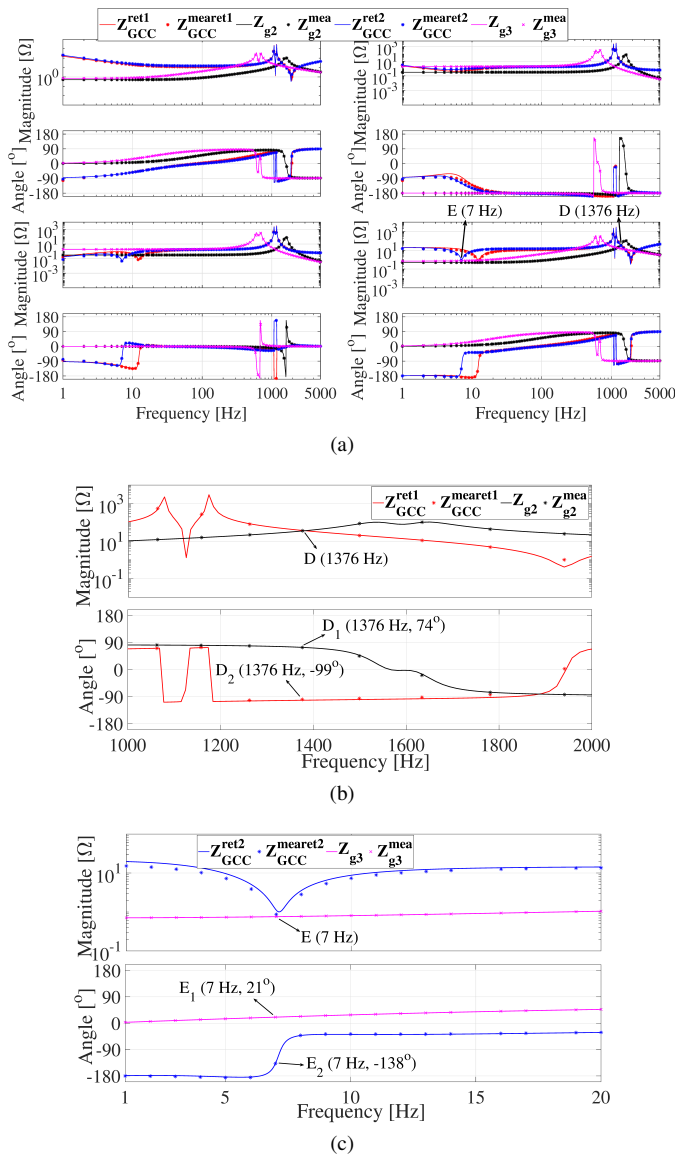


Fig. 7. Bode diagrams of the dq -domain IFRs of the VSC and grid by re-tuning current controller or PLL controller parameters. (a) Full view of the dq -domain IFRs. (b) Zoomed view of q -axis IFRs in high-frequency range. (c) Zoomed view of q -axis IFRs in low-frequency range.

of the dq -domain IFRs calculation, which makes the same effect as directly adding artificially-produced measurement noise to the dq -domain IFRs. However, the former scheme is less intuitive than the latter scheme. Since this article aims to theoretically show that the VF algorithm with adaptive order-selection can extract the circuit and controller parameters from the nonideal IFRs, the latter scheme is adopted. Furthermore, the IFRs measurement in step 1 of Fig. 2 and the parametric transfer function fitting in step 2 of Fig. 2 are implemented in MATLAB/Simulink environment in our article. Actually, Matlab is not strictly needed. For example, online impedance measurement of the grid and the VSC is investigated in [28], where real-time non-parametric IFRs are measured in OPAL-RT platform, and parametric polynomial impedance transfer function is generated in the LabVIEW platform.

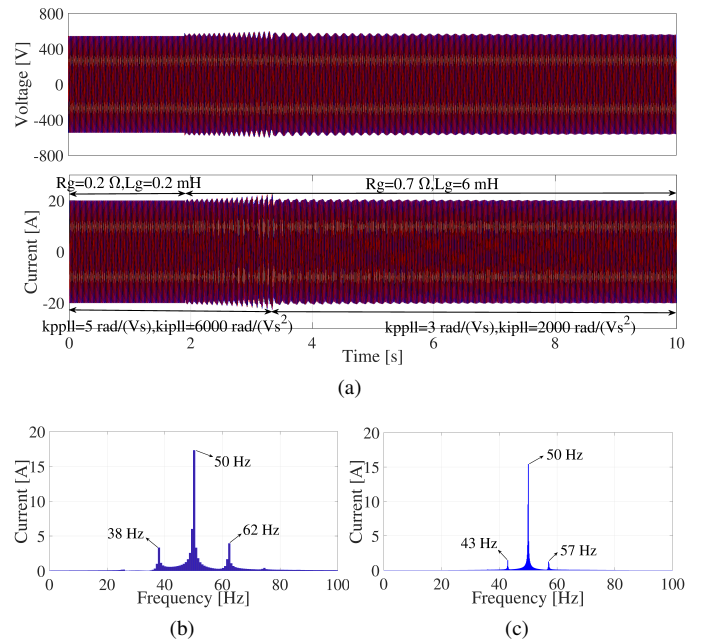


Fig. 8. Real-time simulation verification results for case 2 based on OPAL-RT platform. (a) Time-domain waveforms of three-phase PCC voltages and grid currents. (b) DFT of grid current between 1.9 s and 3.3 s. (c) DFT of grid current between 3.3 s and 10 s.

B. Power-Hardware-in-the-Loop Experimental Validation

1) *Description of Laboratory Setup:* Fig. 9 shows the picture and single-line diagram of the PHiL-based experimental setup in the Norwegian National Smart Grid Laboratory. The physical VSC is under CCC mode, and its rated power is 60 kVA. Its circuit and controller parameters are as follows. V_{dc} is 700 V. L_{f1} , L_{f2} , and C_f are 500 μ H, 200 μ H, and 50 μ F, respectively. f_s and f_{sw} are 10 kHz. k_{pi} , k_{ii} , k_{ppll} , and k_{ipll} are 0.0325 Ω , 32.5 Ω/s , 2.5 rad/(Vs), and 250 rad/(Vs²), respectively. i_{gd}^{ref} and i_{gq}^{ref} are 25 A and 0 A, respectively. In addition, the grid is emulated in an EGSTON 200 kVA COM-PISIO system unit, where V_g and ω_1 are 300 V and 314 rad/s, respectively. A 1:1 Delta-Wye transformer provides galvanic isolation between the VSC and the grid emulator. Electrical measurements (i.e., i_{abc} , v_{abc} , and i_{gabc}) are captured with a Tektronix MSO3014 and exported into an OP5600 real-time simulator. The current controller and the PLL are directly implemented in the VSC and achieved on a Xilinx Zynq 7030-based processor, but critical time components are implemented as FPGA Xilinx Virtex5 blocks. The current references are sent in real-time by the OP5600 real-time simulator via a high speed fiber optic link. In addition, the grid emulator (i.e., the grid voltage reference v_{gabc}^{ref} and the multi-tone voltage perturbation v_{gabc}^{pert}) is implemented in Matlab/Simulink and executed on the real-time simulator. Based on the measured v_{abc} and i_{gabc} , the dq -domain IFRs of the VSC (i.e., Z_{GCC}^{mea}) can be extracted using the FFT in the Matlab post-processing code [48].

2) *Experimental Results:* A multi-tone voltage perturbation v_{gabc}^{pert} with 28 frequency components logarithmically distributed in the frequency range [1, 5000] Hz is injected into grid voltage reference v_{gabc}^{ref} . The overall perturbation

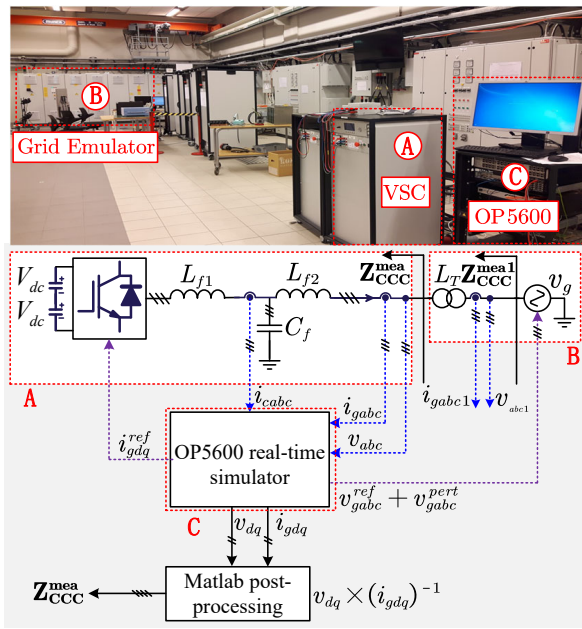


Fig. 9. The picture and single-line diagram of the PHIL-based experimental platform in the Norwegian National Smart Grid Laboratory jointly operated by SINTEF Energy Research and NTNU.

magnitude is about 5% of grid voltage, so that it is low enough to keep the normal system operation during the perturbation injection but high enough to provide necessary signal-to-noise ratio. Figs. 10(a) and 10(b) show the snapshots of v_{abc} and i_{gabc} without and with performing the voltage perturbation injection, respectively. It can be observed that the voltage and current are slightly distorted during the injection. \mathbf{Z}_{CCC}^{mea} is then calculated based on the DFTs of the perturbed v_{abc} and i_{gabc} , and its Bode diagram is plotted in Fig. 11. The measured Bode diagram has a good fit with the expected result, e.g., the negative-resistor feature of the qq -axis element in the low-frequency range. The measurement accuracy of the off-diagonal elements is poorer than that of the diagonal elements. In addition, the dd and qq -axis elements of \mathbf{Z}_{CCC}^{theo} in Fig. 4(a) and those of \mathbf{Z}_{CCC}^{mea} in Fig. 11 have the similar trends, which indicates the effectiveness of the frequency scanning result (Note that the circuit and controller parameters used in simulation and experiment are different.). Besides, the experimentally-measured Bode diagram in Fig. 11 is smoother than the theoretically-derived Bode diagram in Fig. 4 (i.e., the high-frequency phase angles of the diagonal elements of \mathbf{Z}_{CCC}^{mea} in Fig. 11 and those of \mathbf{Z}_{CCC}^{theo} in Fig. 4(a) slowly increase and quickly jump to 90° , respectively.), due to the unpredictable parasitic resistance in practice.

In contrast with the ideal isolation transformer in the simulation environment, the isolation transformer in the experiment can affect the high-frequency impedance measurements. The 70 kVA, 400 V isolation transformer has 4.34% impedance, which accounts for $315.76 \mu\text{H}$ equivalent inductance L_T . In order to visualize this effect, the grid voltage v_{abc1} and grid current i_{gabc1} in Fig. 9 are measured, based on which the IFRs determined by both the VSC and the isolation transformer are calculated, and the Bode diagram is plotted in

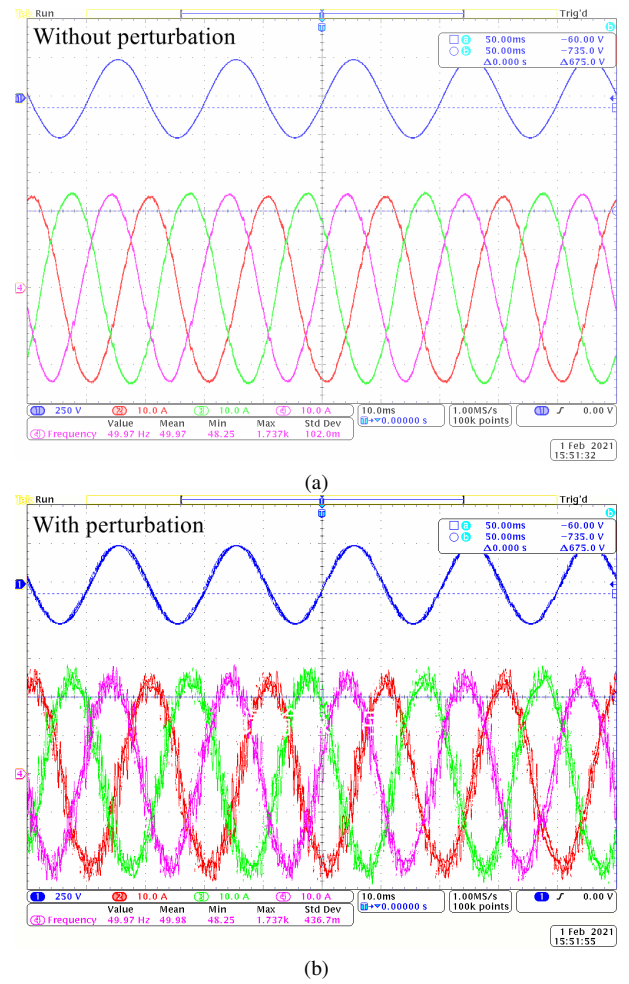


Fig. 10. Oscilloscope captures of v_{abc} and i_{gabc} (a) without and (b) with the multi-tone voltage perturbation injection.

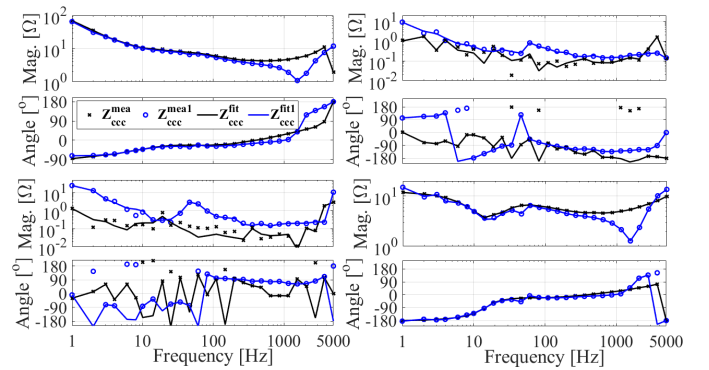


Fig. 11. Bode diagrams of the experimentally-measured dq -domain IFRs and the fitted results.

Fig. 11 as \mathbf{Z}_{CCC}^{mea1} . It can be seen that the isolation transformer mainly influences the high-frequency impedance feature of the VSC, whereas the low-frequency impedance feature is slightly changed.

In order to verify the effectiveness of the proposed parameters identification algorithm in (36) for the experimental results, the VF algorithm is applied on the dd -axis elements of \mathbf{Z}_{CCC}^{mea} and \mathbf{Z}_{CCC}^{mea1} , where the order varies from 8th to 23th. E of the fitted transfer functions in form of (9) is then

TABLE VII

IDENTIFIED L_{f2} AND L_g FROM FIG. 11 BASED ON THE VF ALGORITHM

Fitting order	8th	11th	14th	17th	20th	23th
L_{f2} [μ H]	256.28	345.59	285.12	262.78	234.23	228.96
L_g [μ H]	871.78	864.64	926.15	909.63	866.07	785.08

identified, based on which L_{f2} and $L_g = L_{f2} + L_T$ are further identified from (36) and listed in Table VII. As the fitting order increases, the identified value of L_{f2} generally approaches the reference value of L_{f2} (i.e., 200 μ H). Note that the actual value of L_{f2} in the laboratory may be not exactly the same as the reference value, due to manufacturing tolerance, operating condition variation, temperature fluctuation, and aging. Furthermore, the identified values of L_g are larger than those of L_{f2} , and the discrepancies reveal the transformer inductance and corresponding effect of the transformer on impedance measurement.

The application scenario of the proposed parameters identification method based on the VF algorithm should be emphasized here. In industrial practice, the IFRs can be obtained in several ways. In case 1, the system planner can theoretically derive the impedance models of the VSCs as shown in (1) and (6), if internal control structures and parameters are provided by the vendors. In case 2, when detailed information of the VSCs is confidential for the system planner due to the industry secrecy and intellectual property protection, the vendors tend to deliver lookup tables which provide discrete IFRs of the VSCs, as shown in Figs. 3 and 4. In case 3, when black-box simulation models are provided by the manufactures, the system planner can perform frequency scanning in commercial softwares, e.g., PSCAD/EMTDC and Matlab/Simulink, as shown in Figs. 6 and 7. In case 4, if both discrete IFRs and black-box simulation models are not provided by the vendors, the system planner can experimentally measure the IFRs of the VSCs by connecting these VSCs with a configurable strong grid, as shown in Fig. 9. The uncertainty and inaccuracy generally increase from case 1 to case 4. This article focuses on the capability of the VF algorithm itself to identify internal parameters from IFRs, and the algorithm is especially efficient for cases 1, 2, and 3, as shown in Tables III and IV. Furthermore, the quantitative measurement noise can be properly dealt with based on an adaptive order selection scheme of the VF algorithm, as shown in Table VI. However, compared with cases 1, 2, and 3, the impedance measurement and corresponding parameters identification using a real VSC in case 4 are more challenging, due to various unpredictable factors (e.g., variation of LCL parameters, parasitic resistance, effect of isolation transformer, and accuracy of sensors). How to eliminate the adverse effects of these unpredictable factors and improve the accuracy of the impedance measurement results of a real VSC are out of scope of our article, and can be found in [26], [27], [29], [30].

VI. CONCLUSION

This article presents a VF algorithm-based robust parameters identification method, which is able to identify circuit

and controller parameters of the grid-connected VSCs based on the measured dq -domain IFRs. The proposed parameters identification method can be used when internal parameters are missing due to intellectual property right or filter parameters variation caused by operating condition change, temperature fluctuation, and aging. The identification accuracy is dependent on three factors, i.e., the measurement accuracy of the dq -domain IFRs, the order of the fitted transfer function, and the Pade approximation of the digital time delay. The impedance measurement accuracy can be improved using delicate devices such as frequency spectrum analyzer. The appropriate order of the fitted transfer function can be selected in a trial-and-error way. In addition, the corresponding Pade approximation should be established according to the order of the fitted transfer function. Furthermore, the effect of measurement noise on identification accuracy can be mitigated by increasing the order of the fitted transfer function. The OPAL-RT-based real-time simulation verification results show that the proposed parameters identification method helps to re-tune controller parameters of the gray-box VSC to mitigate instability phenomena under weak grid conditions. The PHIL-based experimental validation results show that the VF algorithm can be applied on a physical VSC in the laboratory, where unpredictable influence factors (e.g., variation of LCL parameters, parasitic resistance, effect of isolation transformer, and accuracy of sensors) may be involved.

APPENDIX

The transfer function matrices in (1) and (6) are shown as follows.

$$\begin{aligned} \mathbf{Z}_{L_{f1}} &= \begin{bmatrix} sL_{f1} & -\omega_1 L_{f1} \\ \omega_1 L_{f1} & sL_{f1} \end{bmatrix} & \mathbf{Z}_{L_{f2}} &= \begin{bmatrix} sL_{f2} & -\omega_1 L_{f2} \\ \omega_1 L_{f2} & sL_{f2} \end{bmatrix} \\ \mathbf{Z}_{C_f} &= \begin{bmatrix} \frac{s}{(s^2 + \omega_1^2)C_f} & \frac{\omega_1}{(s^2 + \omega_1^2)C_f} \\ -\frac{\omega_1}{(s^2 + \omega_1^2)C_f} & \frac{s}{(s^2 + \omega_1^2)C_f} \end{bmatrix}. \end{aligned} \quad (37)$$

For the VSCs under GCC mode,

$$\begin{aligned} \mathbf{Y}_{\text{gcc}}^c &= ((\mathbf{Z}_{L_{f1}}^{-1} + \mathbf{Z}_{C_f}^{-1})^{-1} + \mathbf{Z}_{L_{f2}})^{-1} \\ \mathbf{Y}_{\text{gcc}}^g &= (\mathbf{Z}_{L2}(\mathbf{I} + (\mathbf{Z}_{L_{f2}}^{-1} + \mathbf{Z}_{C_f}^{-1})\mathbf{Z}_{L_{f1}}))^{-1}. \end{aligned} \quad (38)$$

For the VSCs under CCC mode,

$$\mathbf{Y}_{\text{ccc}}^c = \mathbf{Y}_{\text{ccc}}^g = \mathbf{Z}_{L_{f1}}^{-1}. \quad (39)$$

In addition,

$$\mathbf{G}_{\text{del}} = \begin{bmatrix} G^{\text{del}} & 0 \\ 0 & G^{\text{del}} \end{bmatrix} = \begin{bmatrix} e^{-1.5T_s s} & 0 \\ 0 & e^{-1.5T_s s} \end{bmatrix}, \quad (40)$$

where T_s is the sampling time.

$$\mathbf{G}_{\text{ci}} = \begin{bmatrix} G^{ci} & 0 \\ 0 & G^{ci} \end{bmatrix} = \begin{bmatrix} k_{pi} + \frac{k_{ii}}{s} & 0 \\ 0 & k_{pi} + \frac{k_{ii}}{s} \end{bmatrix}, \quad (41)$$

where k_{pi} and k_{ii} are proportional and integrator coefficients of current controller, respectively. V_{dc} is taken into account to design k_{pi} and k_{ii} .

$\mathbf{G}_{\text{PLL}}^i$ models the small-signal perturbation path from the system voltage to current in the controller d - q frame, shown as

$$\mathbf{G}_{\text{PLL}}^i = \begin{bmatrix} 0 & I_q^s G_{\text{PLL}} \\ 0 & -I_d^s G_{\text{PLL}} \end{bmatrix}, \quad (42)$$

where I_d^s and I_q^s are d -axis and q -axis components of three-phase currents in system reference frame, respectively. G_{PLL} is defined as

$$G_{PLL} = \frac{k_{ppll} + k_{ipll}/s}{s + V_d^s(k_{ppll} + k_{ipll}/s)}, \quad (43)$$

where k_{ppll} and k_{ipll} are proportional and integrator coefficients of PLL, respectively. V_d^s is the d -axis component of three-phase terminal voltages in system reference frame.

G_{PLL}^d models the small-signal perturbation path from the system voltage to duty cycle in the controller d - q frame, shown as

$$G_{PLL}^d = \begin{bmatrix} 0 & -D_q^s G_{PLL} \\ 0 & D_d^s G_{PLL} \end{bmatrix}, \quad (44)$$

where D_d^s and D_q^s are d -axis and q -axis components of three-phase duty cycles in system reference frame, respectively.

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