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Co-design of the LCL Filter and Control for Grid-Connected Inverters

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Abstract

In most grid-connected inverters (GCI) with an LCL filter, since the design of both the LCL filter and the controller is done separately, considerable tuning efforts have to be exerted when compared to inverters using an L filter. Consequently, an integrated co-design of the filter and the controller for an LCL-type GCI is proposed in this paper. The control strategy includes only a PI current controller and a proportional grid voltage feed-forward controller. The capacitor is removed from the LCL filter and the design procedure starts from an L-type GCI with a PI current controller. After the PI controller has been settled, the capacitor is added back to the filter. Hence, it introduces a resonance frequency, which is identified based on the crossover frequencies to accommodate the preset PI controller. Using the proposed co-design method, harmonic standards are satisfied and other practical constraints are met. Furthermore, the grid voltage feed-forward control can bring an inherent damping characteristic. In such a way, the good control performance offered by the original L-type GCI and the sharp harmonic attenuation offered by the latter designed LCL filter can be well integrated. Moreover, only the grid current and grid voltage are sensed. Simulation and experimental results verify the feasibility of the proposed design methodology.

Key words: Grid-connected inverter, LCL filter, Current control, Grid voltage feed-forward control, Resonance damping, Co-design

I. INTRODUCTION

Grid-connected inverters (GCIs) are widely used in distributed generation (DG) systems where the sources are mostly renewable energies like wind, solar, etc. The traditional L filter is gradually being replaced by LCL filters, due to their stronger harmonic elimination capability [1][2].

A PI controller is a simple robust controller that can be effectively used for current control in GCIs with an L filter. This is due to the fact that PI controller are easy to design to achieve good performance. For instance, in [3], a PI controller was designed to achieve a maximum crossover frequency while considering the control delay and the PWM transport delay. When combined with proportional grid voltage feed-forward control, grid disturbances can also be rejected effectively.

However, the design of PI controller for an LCL-type GCI is not easy due to the resonance of the LCL filter, which may lead to instability and a complex design in terms of the grid current feed-back controllers and grid voltage feed-forward controllers [4]-[6]. With the conventional design method, using PI current control is not enough to achieve stability. In [7], the regions of the active damping control decided by the resonance frequency were discussed. It was concluded that active damping was needed for lower resonance frequencies. Most of the proposed active damping strategies are realized by using multi-variable feedback controls [8]-[12]. Some of them may also lower the system order by using multi-variable feedback controls [13]-[15]. However, multi-variable feedback control lifts the cost of the sensors and makes the control strategy more complicated. Therefore, in [4], a filter-based active damping solution is proposed. In [16], it is concluded that when converter-side current is used as a control feedback variable, there is an inherent damping term embedded in the control loop, which can neutralize the resonance introduced by an LCL-filter. This damping term can also be obtained when grid feed-forward is implemented, which will be discussed in this paper.
When the resonance frequency increases, the phenomenon of the magnitude curve crossing over 0 dB three times can be observed in the Bode diagram of an LCL-type GCI with a PI controller. Hence, according to the Nyquist Stability Criterion [17], three crossover frequencies should be considered together to confirm the stability. However, this has not been discussed in the literature. To accurately ensure stability, three cross over frequencies will be discussed in this paper. They are considered together in the design according to the Nyquist Stability Criterion.

In the conventional designs of an LCL-type GCI, the design of both the LCL filter and the controller is done separately. They always start with the parameter design of the LCL filter, which aims to satisfy switching current harmonic standards and other practical constraints [18]–[20]. Thereafter, the controller is added and designed according to this preset LCL filter. For the LCL filter, various optimization goals are pursued, such as minimum reactive power [21], low volume, low loss [22], and highest power efficiency [23]. In some converters, the controller is considered during the design of the filter. In [24], a generalized design method of an LCL-type active power filter is proposed, with a capacitor current feedback control loop being used for active damping. In [25], the LC filter of the inverter is designed when considering the performance of the close-loop system. However, among the conventional methods of GCI design, most of the power stage filters and controllers are designed separately. Hence, LCL-type GCIs suffer from complexity in terms of hardware and software, and are hard to properly tune. Sometimes poor performance is achieved in comparison to the L-type GCIs.

In DG systems, the primary task of energy conversion requires a further simplification such as a lower number of variables to be sensed. As required by industry applications, GCIs should behave well in terms of current reference tracking, grid disturbance rejecting, and robustness to grid impedance variations. All of these are well featured by L-type GCIs. Hence, a novel co-design method for an LCL-type GCI and its controller is proposed in this paper to achieve all of these purposes. This is theoretically feasible based on multi crossover frequencies according to the Nyquist Stability Criterion.

In the first step of the co-design method proposed in this paper, the capacitor is removed from the LCL filter. This converts it to an L-type filter, so that the design starts from an L-type GCI employing a PI controller. After the PI controller has been set for the L filter, the capacitor is added back to the L filter. This will introduce a resonance frequency to the LCL filter, which will be adapted to the preset PI controller by identifying the phase margin of three crossover frequencies. Furthermore, proportional feed-forward control of the grid voltage is employed to ensure performance under a highly distorted or weak grid. This brings an inherent damping characteristic. By an easy method, the LCL filter and controller can be co-designed, and both good close-loop performance and sharp harmonic attenuation can be achieved. Moreover, only the output current and grid voltage are sensed. Simulation and experiment results verify the feasibility of the proposed co-design method.

II. Concept of the Proposed Co-Design

The power stage model and controller of a GCI with an LCL filter is shown in Fig. 1(a), where the dc link can be simplified as a constant voltage $U_{dc}$. If the dc capacitor $C_d$ is large enough, Resistors $r_1$, $r_2$, and $r_g$ can be neglected, while the grid internal impedance $L_g$ is considered [16]. The corresponding control scheme is presented in Fig. 1(b), where $G_{eq}(s)$ is the PI current controller and $G_{eq}(s)$ is the grid voltage feed-forward controller.

When the capacitor $C_d$ is removed from the LCL filter, the filter becomes an L type, and $L = L_1 + L_2$. In Fig. 1(b), this means “short-circuit” the dashed block in the power stage model. The $e^{-\omega T_s}$ term represents the system delay, and $T_s$ is the one-sample control delay plus the half-sample PWM transport delay [3].

For an L-type GCI, a PI current controller together with a unity proportional feed-forward controller for the grid voltage is commonly employed for current reference tracking and grid voltage disturbance rejection. The state of the art technique for designing a PI current controller is based on maximizing the proportional part and minimizing the integral time constant to obtain the maximum crossover frequency. This results in a good coordination between the dynamics and the steady-state.

![Fig. 1. Single-phase GCI with LCL filter.](image-url)
In detail, a PI controller \( G_{PI}(s) = k_p(1+1/\tau s) \) for an L-type GCI is governed by: \( k_p = \omega_{r,max} L / U_{dc} \) and \( \tau = 10 / \omega_{c(max)} \), where \( \omega_{c(max)} \) represents the maximum cross over frequency that determines the expected phase margin \( \phi_n \) [3].

As a result of this design, the open-loop transfer function from the duty-cycle \( d \) to the output-current \( i_2 \) is shown as:

\[
G^*_d(s) = \frac{i_2(s)}{d(s)} = \frac{U_{dc} G_{PI}(s)e^{-\tau i_2}}{(L_1 + L_2)s} = \omega_{c(max)}(1 + \frac{\omega_{c(max)}^2}{10s})e^{-\tau i_2} \tag{1}
\]

Fig. 2(a) shows an open-loop Nyquist diagram of an L-type GCI after the design. GCIs with an L filter have a low harmonic attenuation capability. Hence, they can be improved by an LCL filter, which can be realized by returning the filter capacitor \( C_f \) to the splitting point M of the L filter, as shown by the dashed block in Fig. 1(a). For the LCL structure, the open-loop transfer function from the duty-cycle to the output-current is given by:

\[
G^*_L(s) = \frac{i_2(s)}{d(s)} = \frac{U_{dc} G_{PI}(s)e^{-\tau i_2}}{L_1 L_2 C_f s} \frac{1}{s^2 + \omega_r^2} = G^*_d(s) \frac{\omega_r^2}{s^2 + \omega_r^2} \tag{2}
\]

Here, \( \omega_r = \sqrt{(L_1 + L_2)/L_1 L_2 C_f} \) is the resonance frequency of the LCL filter.

With the PI controller \( G_{PI}(s) \) unchanged and \( G^*_d(s) \) remaining invariant with a total inductance \( L = L_1 + L_2 \), the stability after upgrading relies on the value of the resonance frequency \( \omega_r \). Based on (2), Fig. 2(b)-(c) illustrates the open loop Nyquist diagrams of LCL-type GCIs with three different filter resonance frequencies, which are represented in p.u. values, with the sampling frequency \( 1/T \) as the base frequency, i.e. \( \omega_{r,p.u} = \omega_r T \). These Nyquist curves show the loci determined by a positive \( \omega_r \) from 0 to infinity. The radius of the semicircles in (b)-(d) is infinity, which is the result of the filter resonance frequency. \( P_{crit} \) denotes the critical point: -1+j0.

It can be estimated from (2) that the Nyquist curve will cross the negative real axis from the left of the critical point \( P_{crit} \) when \( \omega_{r,p.u} < 1.06 \), which means instability. However, it will cross the negative real axis from the right of \( P_{crit} \) when \( \omega_{r,p.u} > 1.06 \), which means stability. Furthermore, if \( \omega_{r,p.u} \) is increased to 9, the GCI will return to instability. Therefore, the determination of \( \omega_{r,p.u} \) is very important in the design.

It is convenient to further discuss the relative stability in the Bode diagram. Fig. 3 illustrates a Bode diagram through three different filter resonance frequencies (see lines 1, 2, 3).

It can be seen that in the case of a lower \( \omega_{r,p.u} = 1 \) (see line 1), there is only one negative crossing of 0 dB in the
When the capacitor $C_f$ is removed from the LCL filter in Fig. 1, it becomes an L filter with $L = L_1 + L_2$. The PI current controller can be designed easily because a comparatively large phase margin can be reserved, which is set as $\phi_m = 55^\circ$ here. The integral term of the PI controller reduces the phase margin, i.e. about $5^\circ$. Hence, for an L-type GCI with a sample interval of $T=50 \mu s$, the phase margin $\phi_m$ can be set as $49.1^\circ$ and the gain margin $\lambda_C$ can be set as $7.97 \text{ dB}$. According to these constrains, the parameters of the PI controller can be optioned to get the maximum band width $\omega_{c(max)}$ [3].

The capacitor $C_f$ then returns and subsequently the L filter changes back to an LCL filter. It can be determined by a numerical evaluation that if $\omega_r$ approaches infinity, the stability margin will be determined by the gain margin at frequency $\omega_n$, which is the same as its L-type counterpart (see Fig. 3). Assuming a gain margin $\lambda_{LCL}$ for the LCL-type GCI (it should be smaller than its L-type counterpart for the sake of feasibility) and referring to (2), the lower limit of $\omega_{r, low}$ can be determined by solving:

$$\lambda_L - \lambda_{LCL} = 20 \cdot \log_{10}(\omega_{c, low}^2 - \omega_n^2)$$

(3)

Although the gain margin increases with $\omega_r$, the phase margin $\phi_{m3}$ decreases. Then from (2), the upper limit of $\omega_{r, up}$ can be found as follows:

$$\frac{\omega_{c,(max)}}{\omega_{c, up}} \approx 1$$

$$\arctan\left(\frac{10\omega_n}{\omega_{c,(max)}}\right) - \omega_{c, up} T_d - 2\pi = -3\pi + \phi_{m3}$$

(4)

Although the stability is guaranteed, there is still a resonance peak, which may cause problems when the GCI is aiming to track current harmonics around the resonance frequency. However, in DG systems where GCIs are mostly in charge of energy conversion, the resonance peak should not be troublesome.

B. Parameters design of the LCL filter

Once the range of the resonance frequency $\omega_r$ has been identified, the parameters of the LCL filter should be determined. It is well known that the larger the total inductance of the filter, the larger the fundamental voltage drop. This also increases the amount of reactive power that can be absorbed. On the other hand, for low values of the inductance, a high current ripple is obtained, which results in higher iron losses and more current stress on the semiconductors. Therefore, constraints on the inductances should be set as a priority. The preliminary parameters of the...
LCL filter, including the resonance frequency \( (\omega_r) \), the maximum allowable grid current ripple in a percentage of the rated value \( (\%i = \Delta i / I_{\text{rated}}) \), and the inductor split factor \( (k_L = L_2 / L_1) \), are selected by using the following steps:

1) Design of \( \%i \): A lower value of \( \%i \) requires a larger total inductance. According to IEEE std. 519-1992 [28], the upper limit is 30% without a loss of generality. The value of \( \%i \) can be initialized to 25% and then adjusted according to the amount of total inductance;

2) Design of \( \omega_r \): Given \( \%i \), a larger value of the total inductance requires a higher \( \omega_r \) [18]. Therefore, \( \omega_r \) should be set as lower as possible, e.g. the lower limitation required by the PI controller;

3) Design of \( k_L \): The inverter current ripple reaches its maximum at \( k_L = 1 \). Therefore, \( k_L \) should be set as a trade-off between the current ripple (e.g., \( \%i = [15\%, 40\%] \)) and the reactive power (e.g., <5%). A good trade-off can be achieved by selecting \( k_L = 0.3 \).

After that, the values of the filter components can be determined. The harmonic in \( u_{\text{PWM}} \) corresponding to the maximum carrier sideband harmonic of the grid current can be identified by applying a Bessel function. In terms of an H-bridge with a Unipolar SPWM, the \( \pm (N+1) \)th order harmonic current \( (N \) is the carrier ratio) represents the most significant component [29]. The amplitude of the corresponding harmonic voltage is governed by:

\[
\hat{U}_{1,1} = \frac{2U_{dc}}{\pi} J_1(M\pi)
\]

(5)

here, the subscript of \( \hat{U}_{1,1} \) represents the first sideband of the first carrier band, and \( J_1 \) is a first order Bessel function. \( M \) is the modulation depth. \( M \) varies little with load changes. Therefore, it can be approximated by the modulation depth for the no-load condition, i.e. \( M = \hat{u}_g / U_{dc} \), where \( \hat{u}_g \) is the magnitude of \( u_g \).

Back to Fig. 1, the output current can be determined as follows:

\[
i(s) = \frac{\alpha_r}{s(L_1 + L_2)(s^2 + \omega_r^2)} [u_{\text{PWM}}(s) - (L_1 C_j s^2 + 1) u_g(s)]
\]

(6)

When \( u_{\text{PWM}}(s) = \hat{U}_{1,1} \) and \( u_g(s) = 0 \), the inverter-side inductance can be set as:

\[
L_i = \frac{\sqrt{2} \hat{U}_{1,1} (1 + k_L) \omega_{2N+1}}{\omega_r^2 - \omega_{2N+1}^2}
\]

(7)

where \( \omega_{2N+1} \) is the angular frequency of the \( (2N+1) \)th order harmonic.

The grid-side inductance and filter capacitance can be selected as:

\[
L_2 = k_L L_1
\]

(8)

and:

\[
C_f = \frac{L_1 + L_2}{L_1 \omega_r^2}
\]

(9)

IV. DESIGN EXAMPLE WITH PERFORMANCE EVALUATION

According to the above procedure, a design example of a 2.5 kW GCI with an LCL filter is presented in Table I, where the total inductance is 0.025 p.u., the inverter current ripple is 25%, and the capacitive power is 0.02 p.u. Consequently, the presented design fits well with the practical constraints. Then, the evaluation of control performance is carried out as follows.

A. Reference tracking

The load step response of both the L-type and the LCL-type filters are presented in Fig. 4. They show a similar bandwidth but a higher overshoot that lasts for about 2T. However, such a peak only occurs in the averaged model. Hence, it can be used to check the stability margins. During the initial overshoot, the averaged model is not valid. As a result, a more smooth dynamics is available, as proved in Section V.

<table>
<thead>
<tr>
<th>TABLE I</th>
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<tbody>
<tr>
<td><strong>PARAMETERS OF LCL FILTER AND REACTIVE COMPONENT</strong></td>
</tr>
<tr>
<td>( U_{dc} )</td>
</tr>
<tr>
<td>( u_g )</td>
</tr>
<tr>
<td>( I_{2,a} )</td>
</tr>
<tr>
<td>( L_1 )</td>
</tr>
<tr>
<td>( L_2 )</td>
</tr>
<tr>
<td>( C_f )</td>
</tr>
<tr>
<td>( \omega_{L,p} )</td>
</tr>
<tr>
<td>( %i )</td>
</tr>
</tbody>
</table>

B. Rejection of grid disturbances

The inverter output impedance is defined as:

\[
Z_0(s) = \frac{u_g(s)}{i_L(s)}
\]

(10)

The larger the value of \( Z_0 \), the stronger the rejection of grid voltage disturbances [10] [26]. Hence, if the output impedance is high enough in the whole frequency range, the power quality will be much more improved in terms of
harm omic distortion. Fig. 1(b) shows that if an exact full grid voltage feed-forward control is employed:

$$G_f (s) = (L_C s^2 + 1) e^{\omega_T s}$$  (11)

The output impedance tends to be infinite. However, the second-order differentiator and the lead phase blocks can be neglected. Thus, it is possible to approximate $e^{\omega_T s} \approx 1$ and $L_C s^2 = 1$, so that $G_f (s) = 1$ is still preferred. With these assumptions, the Bode diagram is illustrated in Fig. 5. It shows a lot of improvement in the output impedance (compare line 2 and line 1), which is only little degraded by the grid impedance. It is worth mentioning that if multi-feedback active damping control is employed (e.g., capacitor current feedback [27]), the subsequent negative impact from the grid impedance will be noticeable. As a result, the traditional unity feed-forward control $G_f (s) = 1$ may not be the best solution.

C. Inherent damping characteristic from the feed-forward control of grid voltage

When unity grid voltage feed-forward control is used, the grid voltage $u_g$ is measured at the point of common coupling (PCC). Assuming weak grid conditions, i.e., $L_g = n L_2$, the grid voltage can be expressed as follows:

$$u_g = \frac{n}{1+n} u_c + \frac{1}{1+n} u_s$$  (12)

The open-loop transfer function of an LCL-type GCI with the grid voltage feed-forward control can be expressed as:

$$G_{LCL-GCI}^o (s) = \frac{\omega_{\text{r}}^2}{s^2 + \omega_{\text{r}}^2 \left[ 1 - G_f (s) \cdot \frac{n k_L}{1+(1+n) k_L} e^{-\omega_T s} \right]}$$  (13)

where $\omega_{\text{r}} = \sqrt{\frac{L_1 + L_2 + L_g}{L_1 L_2 L_g C}}$.

While the open-loop transfer function of the corresponding L-type counterpart can be expressed as:

$$G_{L-GCI}^o (s) = \frac{U_{dc} \cdot G_p (s) \cdot e^{-\omega_T s}}{L_{T,s} s}$$  (14)

where $L_{T,s} = L_1 + L_2 + L_g$.

The delay block $e^{-\omega_T s}$ can be approximated by its first order Taylor series, i.e., $e^{-\omega_T s} \approx 1 - T_d \cdot s$. Hence, (13) can be expressed as:

$$G_{LCL-GCI}^o \approx G_{LCL-GCI}^o \frac{\omega_{\text{r}}^2}{s^2 + K_D T_d \omega_{\text{r}}^2 \left[ \frac{1}{s} - K_D \cdot \omega_{\text{r}}^2 \right]}$$  (15)

where $K_D = g_f \frac{n k_L}{1+(1+n) k_L} \in (0,1)$.
From (15), it can be observed that if \( G_y(s) \) is a positive constant \( g_y \), a damping coefficient \( \zeta \) is introduced due to the phase delay introduced by \( e^{-\tau s} \). This can be expressed as:

\[
\zeta = \frac{1}{2} \frac{K_D}{\sqrt{1-K_D^2}} \omega_r T_d
\]  

(16)

As illustrated in Fig. 6, the unity feed-forward under a weak grid not only preserves the stability margin, but also makes the loop gain almost unchanged (compare line 3 with line 2). Compared with the existing active damping methods, where the bandwidth is mostly reduced by negative feedback of state variables, the proposed approach gives the active damping benefit without any additional control loop.

V. SIMULATION AND EXPERIMENTAL RESULTS

A. Simulation results

A Matlab/Simulink model is developed to verify the proposed integral design of the GCI together with the LCL filter, using the parameters listed in Table I. The harmonics content shown in Table II are introduced to the grid voltage. Load step changes from no-load to full-load and vice versa are performed under weak grid condition with different grid impedances.

The waveforms shown in Fig. 7(a) illustrate that by using a PI controller, the grid current is severely distorted, and the compatibility of the grid impedance is poor, due to the resonance at the load switching instant if \( L_g = L_2 \). However, such a condition is greatly improved by the grid voltage feed-forward control, as shown in Fig. 7(b), where \( L_g = 3L_2 \). Moreover, the system dynamics are smoother than the averaging model based analysis (see Fig. 4, which has stiff grid conditions), due to the strong proportional part and the comparatively weak integral part of the PI controller. Actually, the LCL-type GCI behaves similar to the L-type one, except for the sharp harmonic attenuation, as detailed in Fig. 7(c), where the switching grid current under stiff grid conditions is about 0.26% of the rated current.

<table>
<thead>
<tr>
<th>COMPONENTS OF GRID HARMONICS</th>
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<tr>
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<td>5th</td>
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<td>7th</td>
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<td>9th</td>
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</table>

B. Experimental results

![Photograph of the single phase GCI prototype.](image)
The proposed design strategy is experimentally verified on a 2.5 kW prototype, as shown in Fig. 8.

The dc and ac buses are formed by an isolated dc source and the utility grid, respectively. The control is implemented by a 32 bit fixed-point 100 MHz TMS320F2808 DSP. Unipolar-SPWM is used, the switching frequency is 10 kHz and the sampling frequency is 20 kHz. Since there is no need for active damping, only the grid current and the grid voltage are sensed. The sampling action is taken at the top and bottom of the carrier signal. The dead time for the power switches is set to 1 μs. The parameters are selected as shown in Table I.

Firstly, under stiff grid conditions, load steps from no-load to full-load, and vice versa, are performed. The resulting waveforms of the inverter side current and grid side current are shown in Fig. 9. They show smooth and fast dynamics and good power quality. The THD of the grid current is well below the standard requirements, as shown in Fig. 9(b) (i.e. 2.78% vs. 5%). Notice that there is some deviation that is mostly accounted for by the measurement susceptible to EMI during IGBT switching, and reflected by the harmonics of the 200th order that should theoretically be null. Note that $\%_{\text{THD}}$ is about 25%, implying that the dominant iron loss is low.

Then, load steps under weak grid conditions are emulated by inserting another inductor $L_g = 1 \text{ mH}$ between the PCC and the grid. The waveforms shown in Fig. 10 illustrate the good behavior of the system even for large grid impedance values. Moreover, there is no obvious deterioration of the power quality. This is ensured by the large output impedance and the preserved loop gain (see Fig. 5 and Fig. 6).

VI. CONCLUSIONS

The integral design of a grid-connected inverter (GCI) control and its LCL filter is proposed in this paper. First, the capacitor is removed from the LCL filter and the design starts from a GCI with an L filter and a PI controller. Then, the capacitor returns to the filter and introduces a resonance, which is identified to accommodate the preset PI controller by identifying the phase margin of three cross over frequencies. Furthermore, the performance under distorted and weak grid conditions is ensured by the unity feed-forward of the grid voltage. Moreover, the feed-forward control of the grid voltage results in an inherent damping effect on the resonance. Hence, there is no need for any kind of active damping control. In such an easy way, the controller and filter design are set as a whole system, so that the control merits of the L-type GCI and the harmonic attenuation merits offered by the LCL filter are well integrated. The good dynamics, power quality, and strong robustness in terms of grid impedance variations are verified by simulation and experimental results. This shows that the proposed design is a promising tool for distributed generation systems.

Fig. 9. Simulations under weak and stiff grid conditions

![Fig. 9. Simulations under weak and stiff grid conditions](image)

Fig. 10. Experimental waveforms of grid voltage and current in weak grid ($L_g = 1 \text{ mH}$)

![Fig. 10. Experimental waveforms of grid voltage and current in weak grid ($L_g = 1 \text{ mH}$)](image)
Co-design of LCL Filter and Control for Grid-Connected Inverters

REFERENCES


Yu Zhang was born in Jiangsu Province, China. He received his B.E., M.E. and Ph.D. degrees in Electrical Engineering from the Huazhong University of Science and Technology (HUST), Wuhan, China, in 1992, 1995 and 2005, respectively. From 1995 to 2002, he was an Engineer working on power supply applications in a telecommunication company. He is currently an Associate Professor in the School of Electrical and Electronic Engineering, HUST, where he teaches power electronics. His current research interests include power electronics modeling and control, parallel UPSs, and renewable energy generation. He has developed several power systems, such as UPSs, and achieved 4 Scientific and Technological Progress Awards from the Chinese government and various academic societies. He is the author of more than 60 technical papers and is now a Member of the UPS Standard Committee of China.

Mingyu Xue was born in Zhejiang Province, China, in 1984. He received his B.S. degree in Electrical Engineering and Automation from the Dalian University of Technology (DLUT), Dalian, China, in 2006, and his M.S. degree in Power Electronics and Drives from the Huazhong University of Science and Technology (HUST), Wuhan, China, in 2008. He is currently working toward his Ph.D. degree at HUST. He is now with the Shanghai United Imaging Healthcare Company. His current research interests include power electronic converters, renewable energy and distributed generation.

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